

Vishay Siliconix

# P-Channel 20-V (D-S) MOSFET

| PRODUCT SUMMARY     |  |                    |                       |  |
|---------------------|--|--------------------|-----------------------|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}$ ( $\Omega$ )                  | I <sub>D</sub> (A) | Q <sub>g</sub> (Typ.) |  |
| - 20                | $0.030 \text{ at V}_{GS} = -4.5 \text{ V}$ | - 12 <sup>a</sup>  |                       |  |
|                     | 0.041 at V <sub>GS</sub> = - 2.5 V         | - 12 <sup>a</sup>  | 15 nC                 |  |
|                     | 0.056 at V <sub>GS</sub> = - 1.8 V         | - 12 <sup>a</sup>  | 15110                 |  |
|                     | 0.150 at V <sub>GS</sub> = - 1.5 V         | - 2                |                       |  |

### **FEATURES**

- · Halogen-free
- TrenchFET<sup>®</sup> Power MOSFET
- New Thermally Enhanced PowerPAK<sup>®</sup> SC-70 Package

Load Switch, PA Switch and Battery Switch for Portable

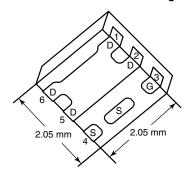
- Small Footprint Area
- Low On-Resistance

**APPLICATIONS** 



RoHS

### PowerPAK SC-70-6L-Single



# Marking Code Part # code BEX • XXX Lot Traceability and Date code

# G

Ordering Information: SiA411DJ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

| Parameter  |                                   | Symbol           | Limit                 | Unit |
|--|-----------------------------------|------------------|-----------------------|------|
| Drain-Source Voltage   | $V_{DS}$                          | - 20             | V                     |      |
| Gate-Source Voltage  |                                   | $V_{GS}$         | ± 8                   | v    |
|  | T <sub>C</sub> = 25 °C            |                  | - 12 <sup>a</sup>     |      |
| Continuous Drain Current (T <sub>.1</sub> = 150 °C)          | T <sub>C</sub> = 70 °C            | I <sub>D</sub>   | - 12 <sup>a</sup>     |      |
| Continuous Brain Current (1) = 100 °C)                       | T <sub>A</sub> = 25 °C            | υ                | - 8.8 <sup>b, c</sup> |      |
|  | T <sub>A</sub> = 70 °C            |                  | - 7 <sup>b, c</sup>   | A    |
| Pulsed Drain Current   |                                   | I <sub>DM</sub>  | - 20                  |      |
| Continuous Source-Drain Diode Current                        | T <sub>C</sub> = 25 °C            | I <sub>s</sub> _ | - 12 <sup>a</sup>     |      |
| Continuous Cource-Diam Diode Current                         | T <sub>A</sub> = 25 °C            | 'S               | - 2.9 <sup>b, c</sup> |      |
| Maximum Power Dissipation                                    | T <sub>C</sub> = 25 °C            |                  | 19                    |      |
|  | T <sub>C</sub> = 70 °C            | P <sub>D</sub>   | 12                    | w    |
|  | T <sub>A</sub> = 25 °C            | ' Б              | 3.5 <sup>b, c</sup>   |      |
|  | T <sub>A</sub> = 70 °C            |                  | 2.2 <sup>b, c</sup>   |      |
| Operating Junction and Storage Temperature R                 | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150      | °C                    |      |
| Soldering Recommendations (Peak Temperature) <sup>d, e</sup> |                                   |                  | 260                   | 7    |

| THERMAL RESISTANCE RATINGS                  |              |            |         |      |      |  |
|---|--------------|------------|---------|------|------|--|
| Parameter                                   | Symbol       | Typical    | Maximum | Unit |      |  |
| Maximum Junction-to-Ambient <sup>b, f</sup> | t ≤ 5 s      | $R_{thJA}$ | 28      | 36   | °C/W |  |
| Maximum Junction-to-Case (Drain)            | Steady State | $R_{thJC}$ | 5.3     | 6.5  |      |  |

### Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. See Solder Profile (http://www.vishay.com/ppg?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 80 °C/W.

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| <b>SPECIFICATIONS</b> $T_J = 25  ^{\circ}\text{C}$ , Parameter | Symbol  | Test Conditions  | Min.  | Тур.   | Max.  | Unit  |  |
|--|---|--|-------|--------|-------|-------|--|
| Static   |   |  |       |        |       |       |  |
| Drain-Source Breakdown Voltage                                 | V <sub>DS</sub>   | $V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$   | - 20  |        |       | V     |  |
| V <sub>DS</sub> Temperature Coefficient                        | $\Delta V_{DS}/T_{J}$   | l <sub>D</sub> = - 250 μA  |       | - 19.5 |       | mV/°C |  |
| V <sub>GS(th)</sub> Temperature Coefficient                    | $\Delta V_{GS(th)}/T_J$   |  |       | 2.3    |       |       |  |
| Gate-Source Threshold Voltage                                  | V <sub>GS(th)</sub>   | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$  | - 0.4 |        | - 1   | V     |  |
| Gate-Source Leakage  | I <sub>GSS</sub>  | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$   |       |        | ± 100 | nA    |  |
| <u> </u>   | I <sub>DSS</sub>  | V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V  |       |        | - 1   | μΑ    |  |
| Zero Gate Voltage Drain Current                                |   | V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C  |       |        | - 10  |       |  |
| On-State Drain Current <sup>a</sup>                            | I <sub>D(on)</sub>  | $V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$   | - 20  |        |       | Α     |  |
|  | (-,   | V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.9 A  |       | 0.025  | 0.030 |       |  |
|  |   | V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 5.0 A  |       | 0.033  | 0.041 |       |  |
| Drain-Source On-State Resistance <sup>a</sup>                  | R <sub>DS(on)</sub>   | V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 1.8 A  |       | 0.045  | 0.056 | Ω     |  |
|  |   | V <sub>GS</sub> = - 1.5 V, I <sub>D</sub> = - 0.7 A  |       | 0.075  | 0.150 | 1     |  |
| Forward Transconductance <sup>a</sup>                          | 9 <sub>fs</sub>   | V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 5.9 A   |       | 21     |       | S     |  |
| Dynamic <sup>b</sup>   | 0.0   | 25   |       | 1      |       | 1     |  |
| Input Capacitance  | C <sub>iss</sub>  |  |       | 1200   |       |       |  |
| Output Capacitance   | C <sub>oss</sub> V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz |  | 210   |        | pF    |       |  |
| Reverse Transfer Capacitance                                   | C <sub>rss</sub>  | 23 / US /  |       | 155    |       | -     |  |
| ·  |   | V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 8 V, I <sub>D</sub> = - 8.8 A                                      |       | 25     | 38    | nC    |  |
| Total Gate Charge  | $Q_g$   | V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.8 A                                    |       | 15     | 23    |       |  |
| Gate-Source Charge   | Q <sub>gs</sub>   |  |       | 2.5    |       |       |  |
| Gate-Drain Charge  | Q <sub>gd</sub>   | 30 30  |       | 4      |       |       |  |
| Gate Resistance  | R <sub>g</sub>  | f = 1 MHz  |       | 5      |       | Ω     |  |
| Turn-On Delay Time   | t <sub>d(on)</sub>  |  |       | 12     | 20    |       |  |
| Rise Time  | t <sub>r</sub>  | $V_{DD} = -10 \text{ V}, R_L = 1.4 \Omega$<br>$I_D \cong -7 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ |       | 65     | 100   | - ns  |  |
| Turn-Off Delay Time  | t <sub>d(off)</sub>   |  |       | 40     | 60    |       |  |
| Fall Time  | t <sub>f</sub>  |  |       | 100    | 150   |       |  |
| Turn-On Delay Time   | t <sub>d(on)</sub>  | $V_{DD} = -10 \text{ V}, R_L = 1.4 \Omega$   |       | 5      | 10    |       |  |
| Rise Time  | t <sub>r</sub>  |  |       | 25     | 35    |       |  |
| Turn-Off Delay Time  | t <sub>d(off)</sub>   |  |       | 40     | 60    |       |  |
| Fall Time  | t <sub>f</sub>  | •  |       | 12     | 20    |       |  |
| <b>Drain-Source Body Diode Characterist</b>                    | 1   |  |       | l      |       |       |  |
| Continuous Source-Drain Diode Current                          | uous Source-Drain Diode Current I <sub>S</sub> T <sub>C</sub> =             |  |       |        | - 12  | _     |  |
| ulse Diode Forward Current                                     |   |  |       |        | 20    | - A   |  |
| Body Diode Voltage   | V <sub>SD</sub>   | I <sub>S</sub> = -7 A, V <sub>GS</sub> = 0 V   |       | - 0.85 | - 1.2 | V     |  |
| Body Diode Reverse Recovery Time                               | t <sub>rr</sub>   |  |       | 20     | 40    | ns    |  |
| Body Diode Reverse Recovery Charge                             | Q <sub>rr</sub>   |  |       | 10     | 20    | nC    |  |
| Reverse Recovery Fall Time                                     | t <sub>a</sub>  |  |       | 13     |       | ns    |  |
| Reverse Recovery Rise Time                                     | t <sub>b</sub>  |  |       | 7      |       |       |  |

#### Notes:

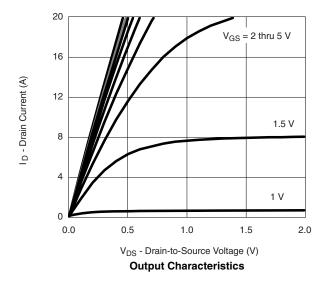
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

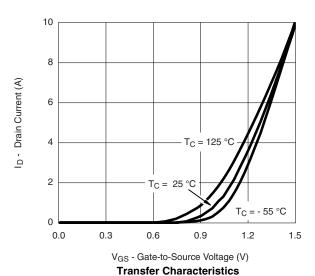
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

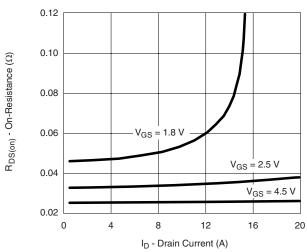


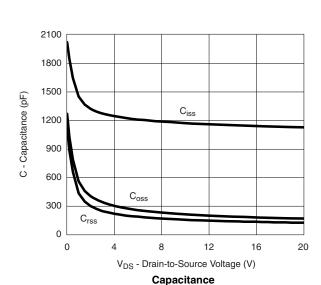
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

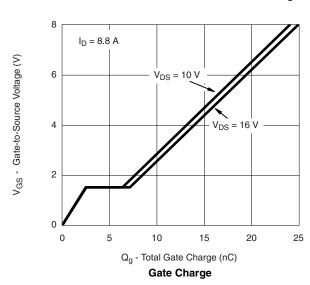


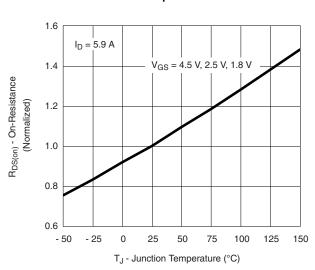










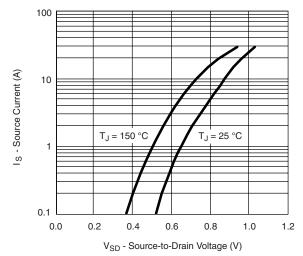


On-Resistance vs. Junction Temperature

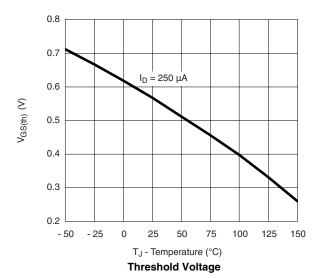
# Vishay Siliconix

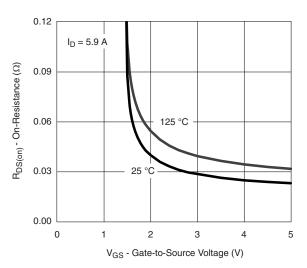
# VISHAY.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

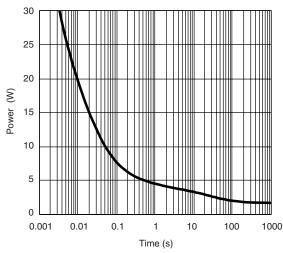


### Soure-Drain Diode Forward Voltage

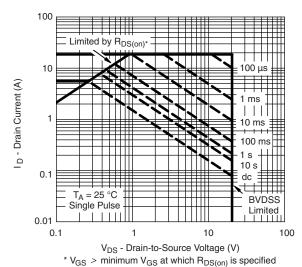




On-Resistance vs. Gate-to-Source Voltage



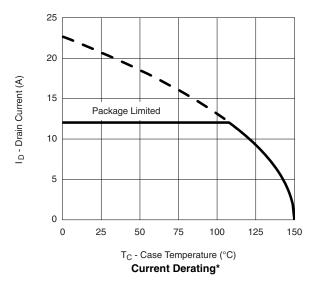
Single Pulse Power, Junction-to-Ambient

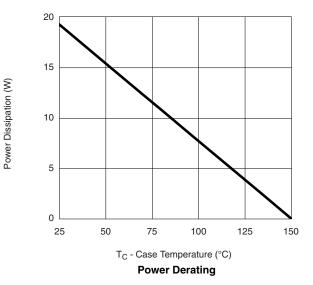




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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



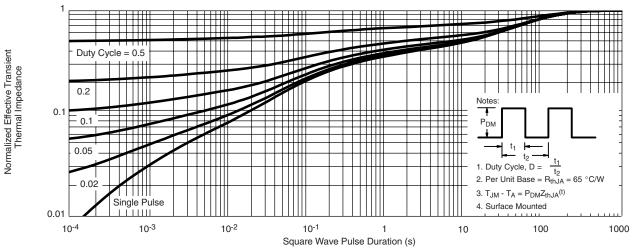


<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

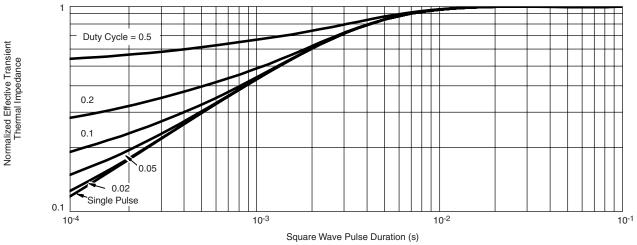
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?74464">https://www.vishay.com/ppg?74464</a>.



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