

## N- and P-Channel 2.5-V (G-S) MOSFET

### PRODUCT SUMMARY

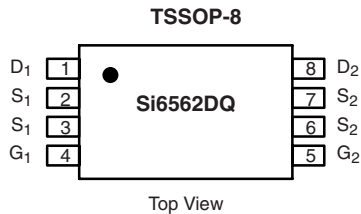
	$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
N-Channel	20	0.030 at $V_{GS} = 4.5$ V	$\pm 4.5$
		0.040 at $V_{GS} = 2.5$ V	$\pm 3.9$
P-Channel	- 20	0.050 at $V_{GS} = - 4.5$ V	$\pm 3.5$
		0.085 at $V_{GS} = - 2.5$ V	$\pm 2.7$

### FEATURES

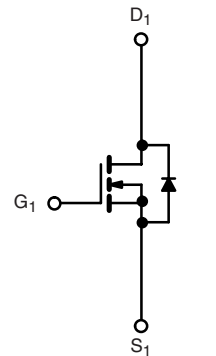
- Halogen-free Option Available
- TrenchFET® Power MOSFETS: 2.5 V Rated



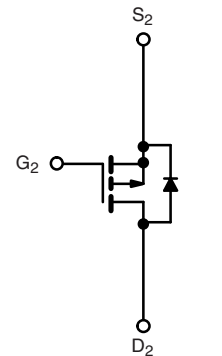
**RoHS\***  
COMPLIANT



**Ordering Information:** Si6562DQ-T1  
Si6562DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET



P-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V <sub>DS</sub>	20	- 20	V
Gate-Source Voltage		V <sub>GS</sub>	± 12	± 12	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	± 4.5	± 3.5	A
	T <sub>A</sub> = 70 °C		± 3.6	± 2.7	
Pulsed Drain Current		I <sub>DM</sub>	± 30	± 30	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.25	- 1.25	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.0		W
	T <sub>A</sub> = 70 °C		0.64		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	N- or P-Channel	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	125	°C/W

Notes:

a. Surface Mounted on FR4 board,  $t \leq 10$  s.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	N-Ch	0.6			V
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	P-Ch	- 0.6			
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 12 V	N-Ch P-Ch			± 100 ± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	N-Ch			1	μA
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V	P-Ch			- 1	
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	N-Ch			25	
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	P-Ch			- 25	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 4.5 V	N-Ch	30			A
		V <sub>DS</sub> ≥ - 5 V, V <sub>GS</sub> = - 4.5 V	P-Ch	- 30			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4.5 A	N-Ch		0.023	0.030	Ω
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.5 A	P-Ch		0.040	0.050	
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 3.9 A	N-Ch		0.030	0.040	
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 2.7 A	P-Ch		0.060	0.085	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4.5 A	N-Ch		20		S
		V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 3.5 A	P-Ch		10		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.25 A, V <sub>GS</sub> = 0 V	N-Ch		0.65	1.2	V
		I <sub>S</sub> = - 1.25 A, V <sub>GS</sub> = 0 V	P-Ch		0.72	- 1.2	
Dynamic <sup>b</sup>							
Total Gate Charge	Q <sub>g</sub>	N-Channel V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4.5 A	N-Ch		13	25	nC
Gate-Source Charge	Q <sub>gs</sub>		P-Ch		14.5	25	
Gate-Drain Charge	Q <sub>gd</sub>	P-Channel V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.5 A	N-Ch		3.0		
			P-Ch		3.5		
Turn-On Delay Time	t <sub>d(on)</sub>	N-Channel V <sub>DD</sub> = 10 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≡ 1 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 6 Ω	N-Ch		22	50	ns
Rise Time	t <sub>r</sub>		P-Ch		27	50	
Turn-Off Delay Time	t <sub>d(off)</sub>	P-Channel V <sub>DD</sub> = - 10 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≡ - 1 A, V <sub>GEN</sub> = - 10 V, R <sub>G</sub> = 6 Ω	N-Ch		40	80	
			P-Ch		30	60	
Fall Time	t <sub>f</sub>		N-Ch		50	100	
			P-Ch		57	100	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.25 A, dI/dt = 100 A/μs	N-Ch		20	40	
		I <sub>F</sub> = - 1.25 A, dI/dt = 100 A/μs	P-Ch		21	40	
					30	60	
					60	100	

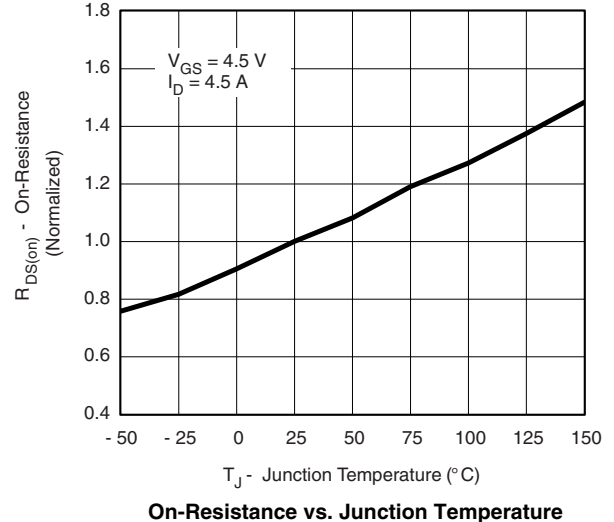
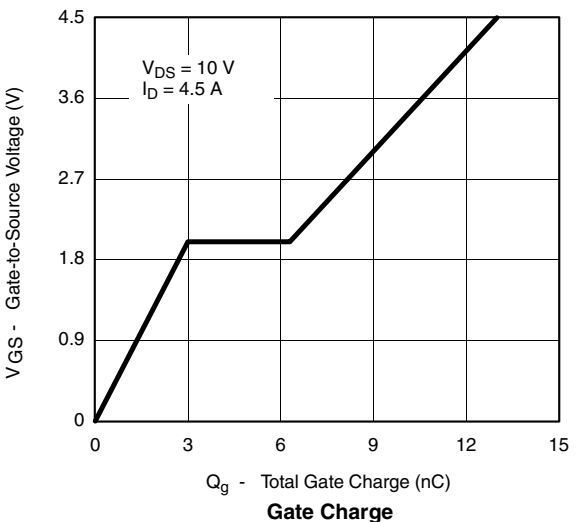
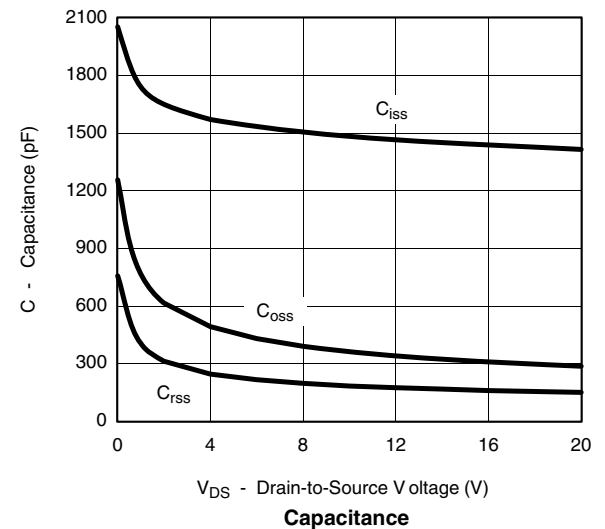
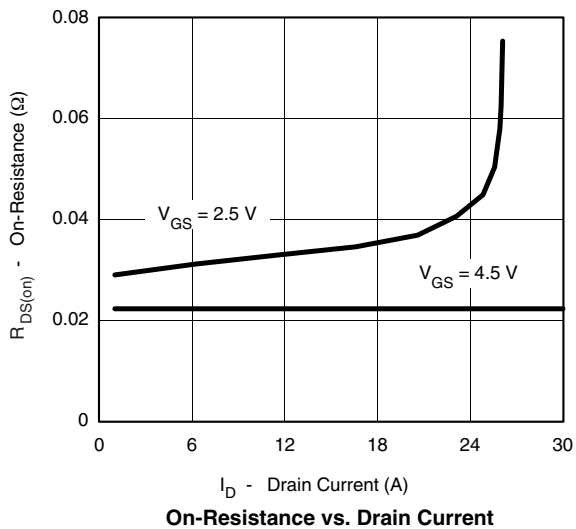
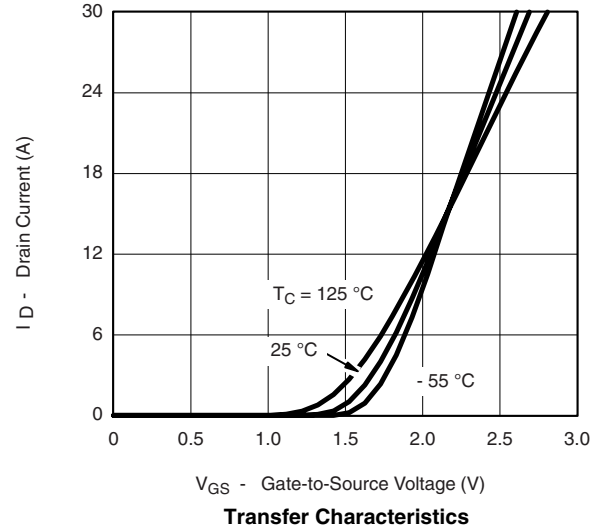
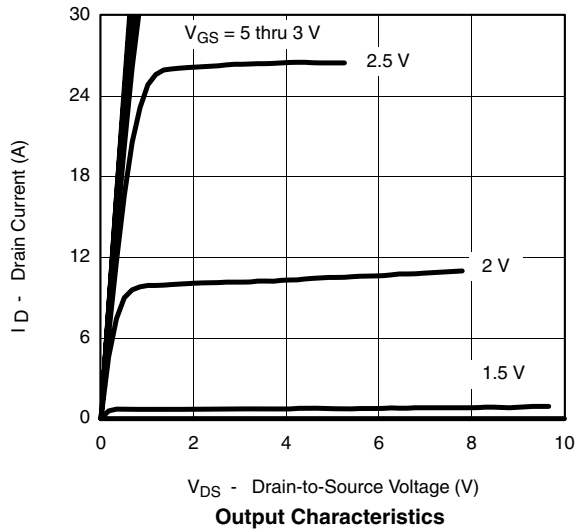
Notes:

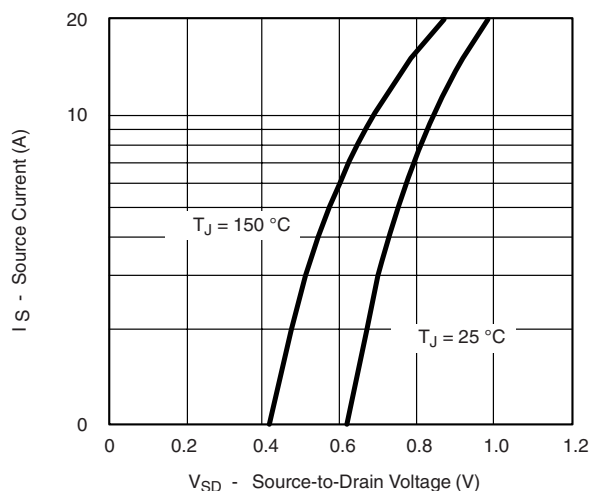
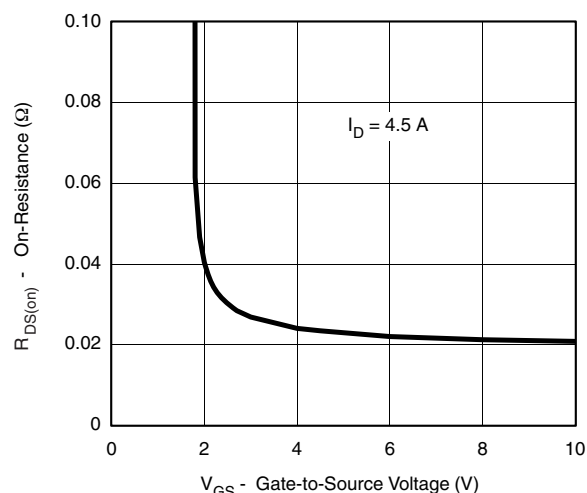
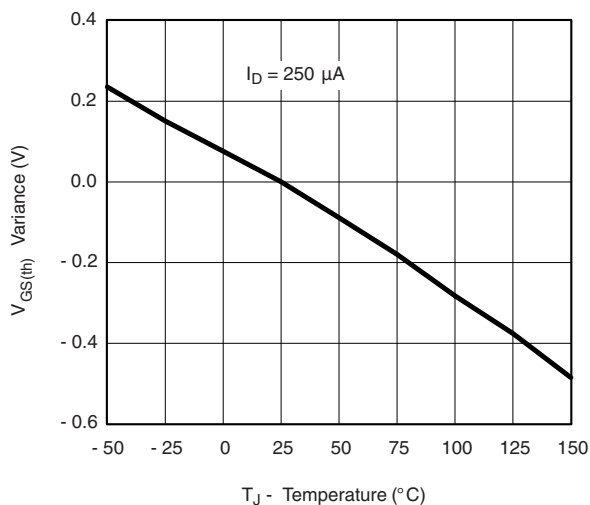
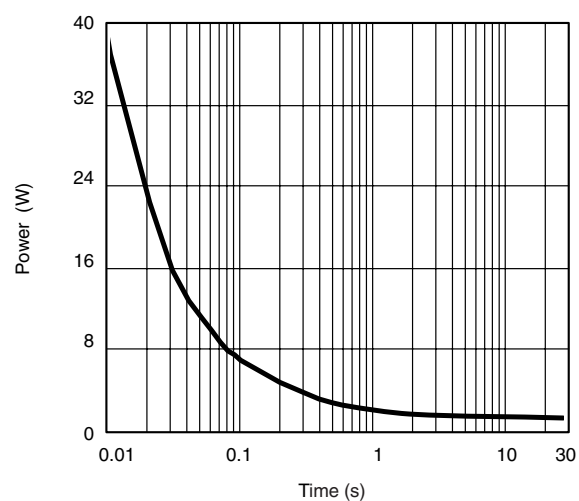
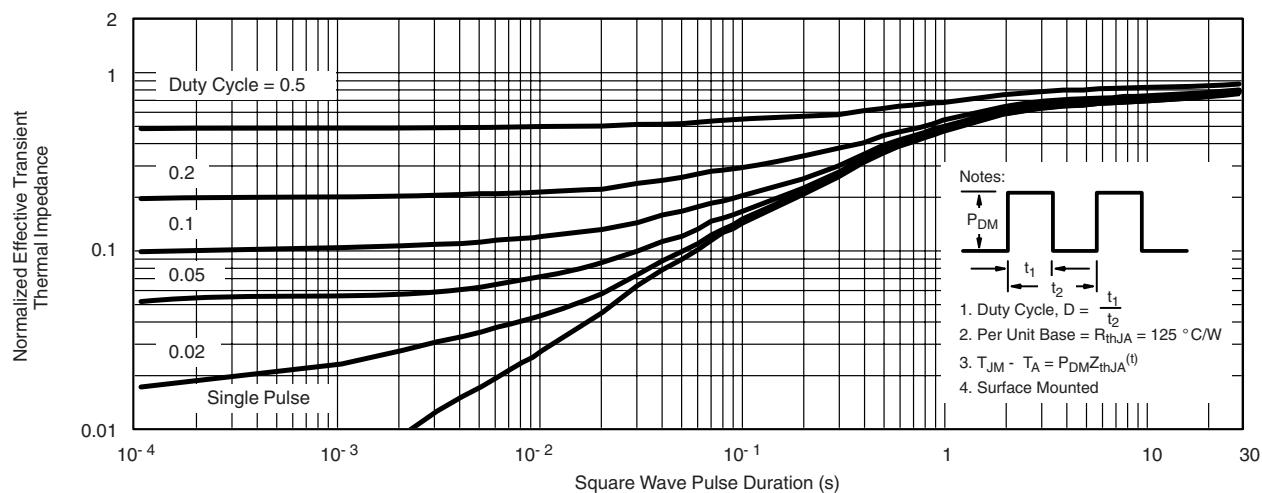
a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

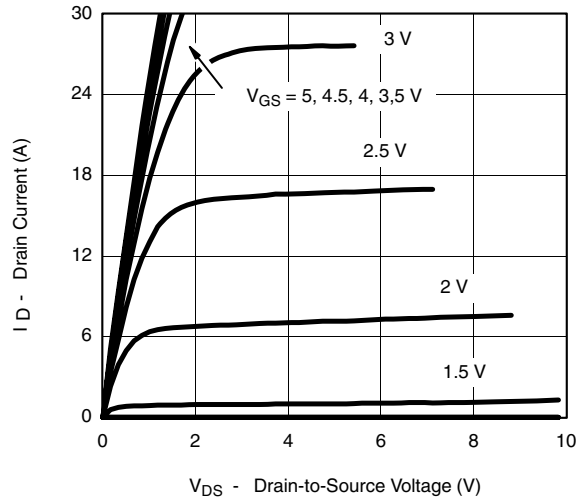
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

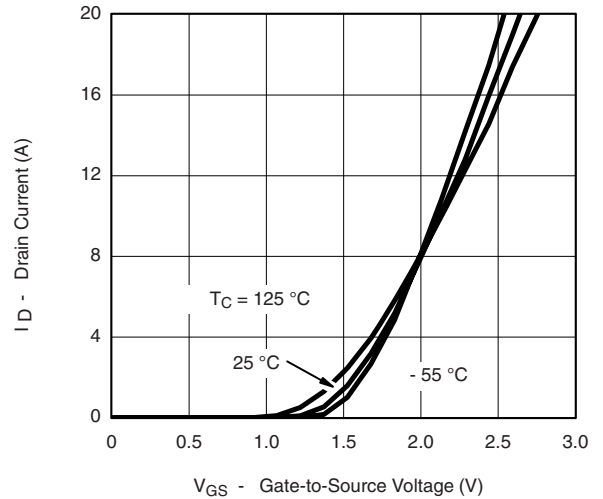


**N-CHANNEL TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power****Normalized Thermal Transient Impedance, Junction-to-Ambient**

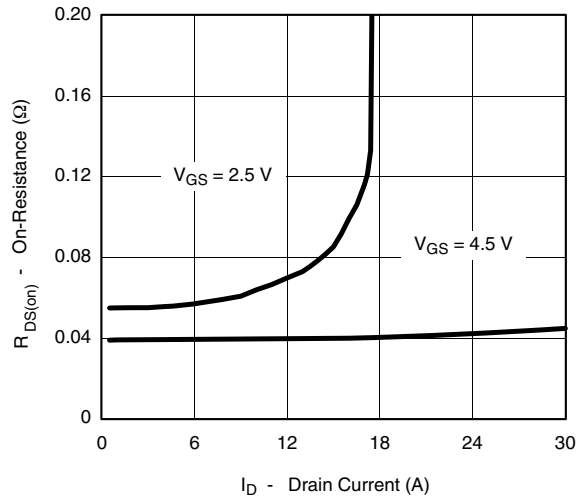
## P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



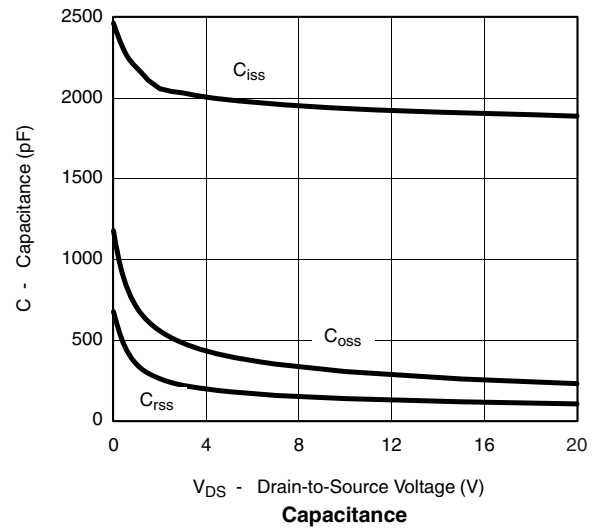
Output Characteristics



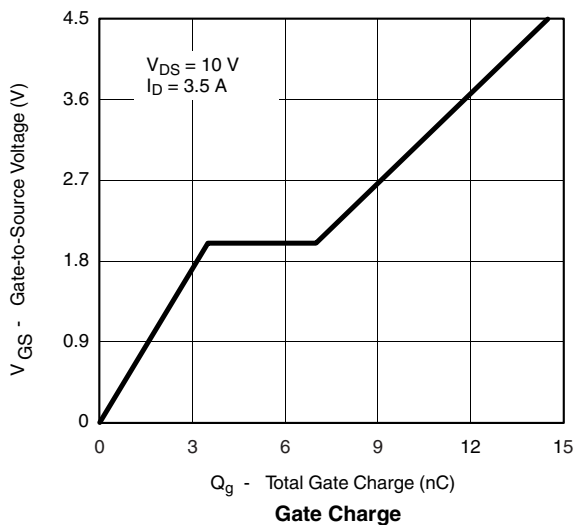
Transfer Characteristics



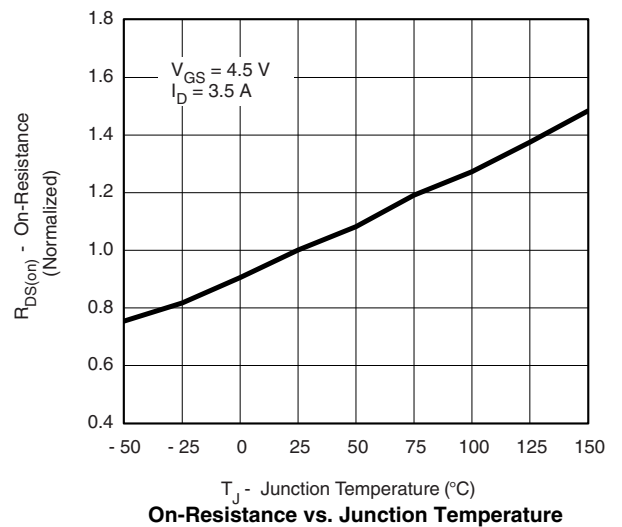
On-Resistance vs. Drain Current



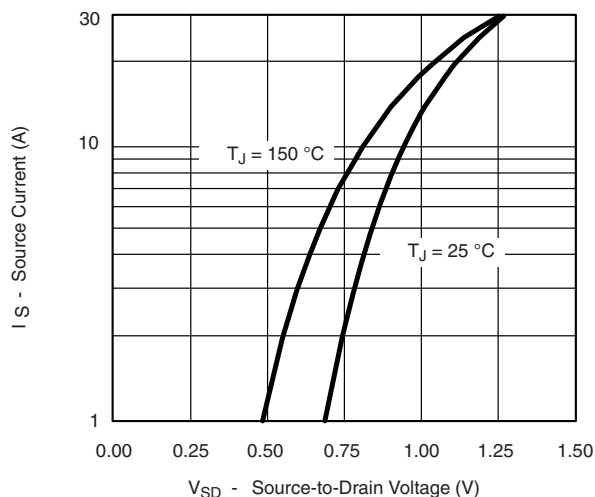
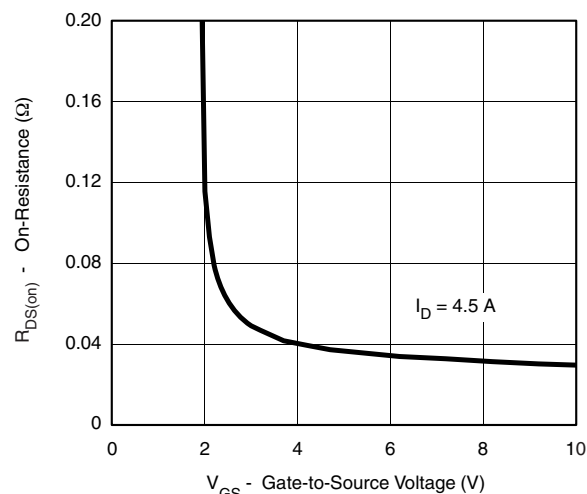
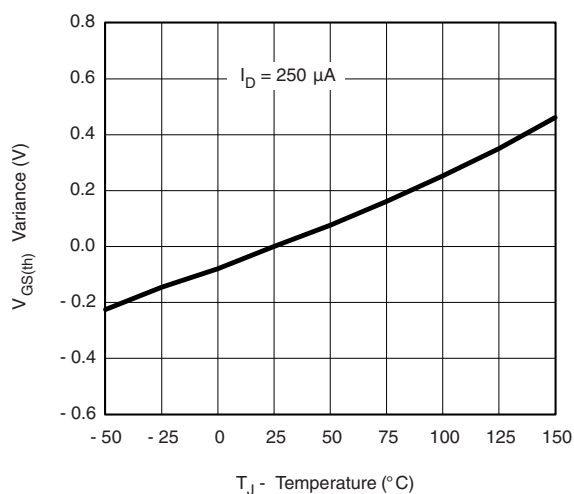
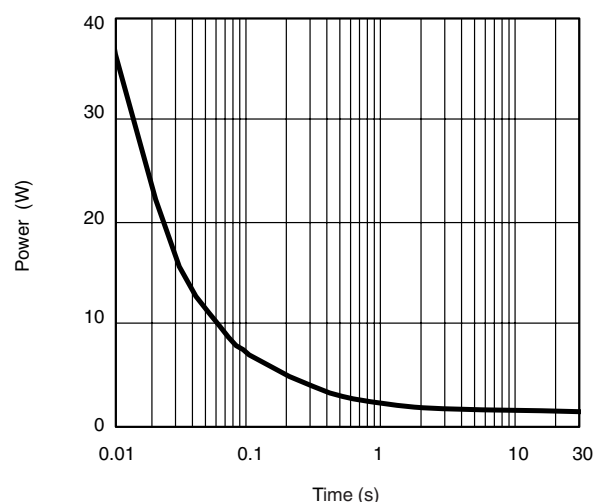
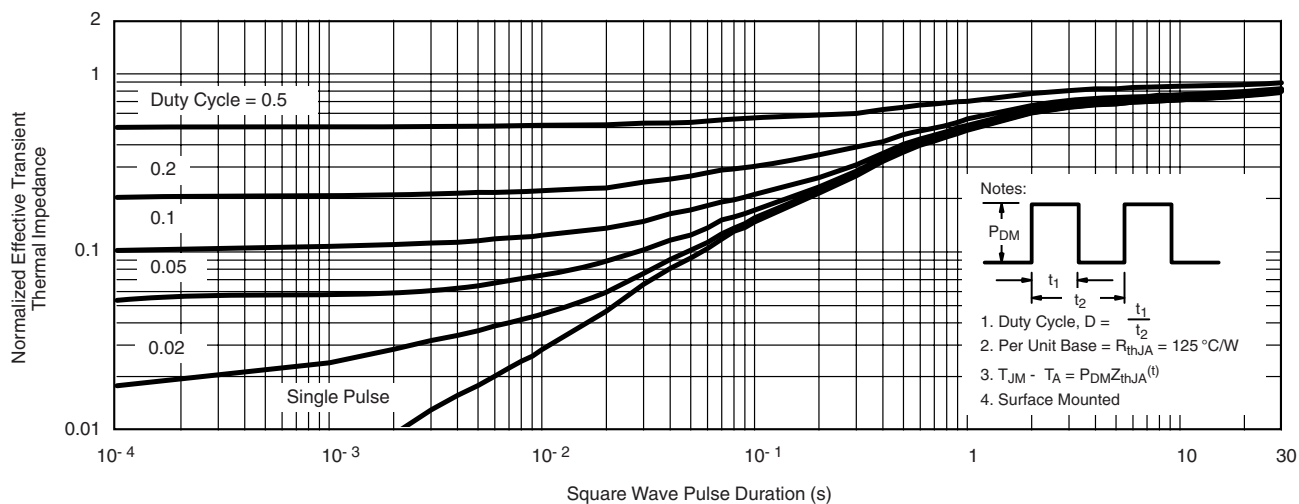
Capacitance



Gate Charge



On-Resistance vs. Junction Temperature

**P-CHANNEL TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power****Normalized Thermal Transient Impedance, Junction-to-Ambient**

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