

Vishay Siliconix

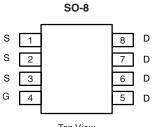
P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 30	0.014 at V _{GS} = - 10 V	- 11.5			
	0.022 at V _{GS} = - 4.5 V	- 9.2			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC





Top View

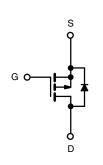
Ordering Information: Si4825DY-T1-E3 (Lead (Pb)-free) Si4825DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^\circ C$, unles	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		V
Gate-Source Voltage	ource Voltage		± 25		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 25 °C	– I _D	- 11.5	- 8.1	
	T _A = 70 °C		- 9.2	- 6.5	
Pulsed Drain Current		I _{DM}	- 50		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 2.5	- 1.3	
Maximum Power Dissipation ^a	T _A = 25 °C	– P _D	3.0	1.5	W
	T _A = 70 °C		1.9	0.9	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R _{thJA}	32	42	
Maximum Junction-to-Ambient ^a	Steady State		68	85	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	15	18	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted									
Parameter	Symbol	DOI Test Conditions		Тур.	Max.	Unit			
Static									
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1.0		- 3.0	V			
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 25 V			± 100	nA			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μA			
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 55 °C			- 5	μΛ			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq$ - 5 V, V_{GS} = - 10 V	- 50			А			
Drain-Source On-State Resistance ^a	Brach	V _{GS} = - 10 V, I _D = - 11.5 A		0.012	0.014	- Ω			
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 9.2 A		0.018	0.022				
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 11.5 A		28		S			
Diode Forward Voltage ^a	V_{SD}	$I_{\rm S}$ = - 2.5 A, $V_{\rm GS}$ = 0 V		- 0.8	- 1.2	V			
Dynamic ^b									
Total Gate Charge	Qg			55	71				
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 11.5 A		15.5		nC			
Gate-Drain Charge	Q _{gd}			7.5					
Gate Resistance	Rg	f = 1 MHz		3.5	5.3	Ω			
Turn-On Delay Time	t _{d(on)}			15	25				
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		13	20				
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 1 A, ${\rm V}_{\rm GEN}$ = - 10 V, ${\rm R}_{\rm g}$ = 6 Ω		97	150	ns			
Fall Time	t _f			51	75				
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.5 A, dl/dt = 100 A/μs		45	80				

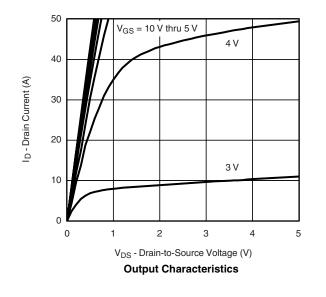
Notes:

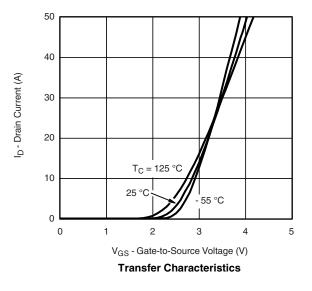
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

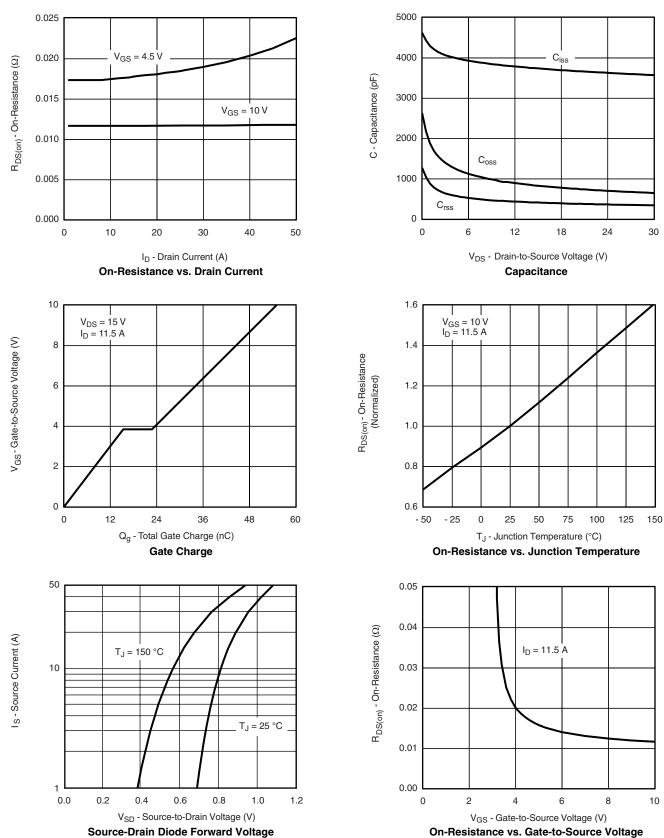




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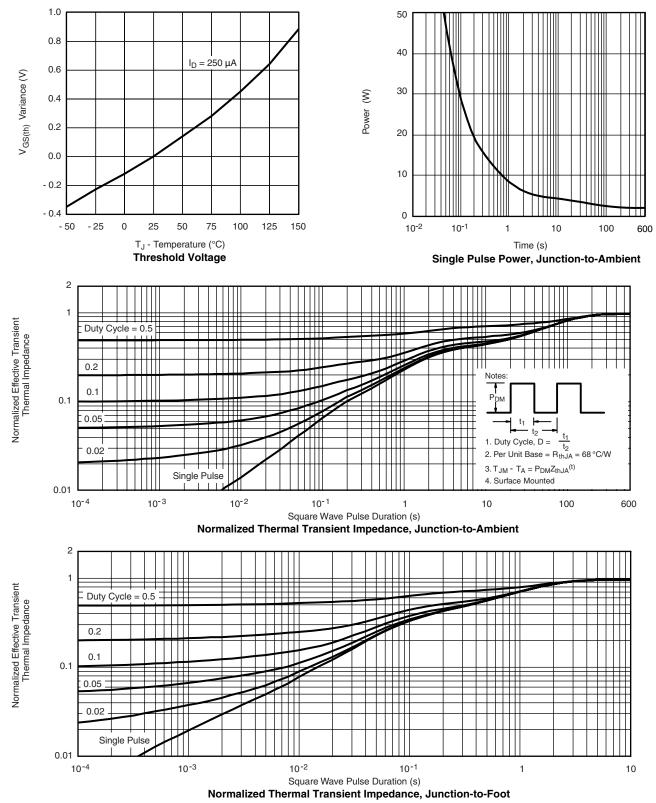


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Si4825DY

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71291.

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