



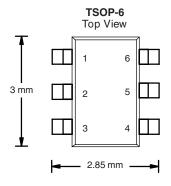
# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
30	0.028 at V <sub>GS</sub> = 10 V	6.7		
	0.038 at V <sub>GS</sub> = 4.5 V	5.7		

#### **FEATURES**

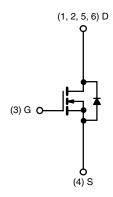
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3424DV-T1-E3 (Lead (Pb)-free)

Si3424DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	6.7	5.0	٨
	T <sub>A</sub> = 70 °C		5.4	4.0	
Pulsed Drain Current		I <sub>DM</sub>	30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	1.0	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.0	1.14	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.73	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian una lumation ta Anabianta	t ≤ 5 s	R <sub>thJA</sub>	40	62.5	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	25	30	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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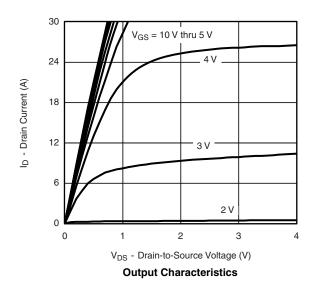
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$				V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	ı	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1			
	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			5	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.7 A		0.023	0.028			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 5.7 \text{ A}$		0.032	0.038	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 6.7 \text{ A}$		14		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V		0.8	1.2	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			11.5	18			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6.7 \text{ A}$		1.6		nC		
Gate-Drain Charge	$Q_{gd}$			3.2				
Turn-On Delay Time	t <sub>d(on)</sub>			7	15			
Rise Time	t <sub>r</sub>	$t_r$ $V_{DD} = 15 \text{ V}, R_L = 15 \Omega$		10	20	ns		
Turn-Off Delay Time	t <sub>d(off)</sub>			20	40			
Fall Time	t <sub>f</sub>			11	20			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 1.7 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		40	80			

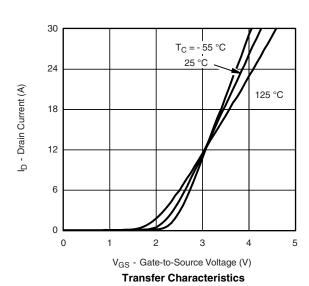
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



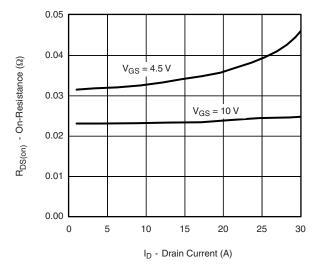




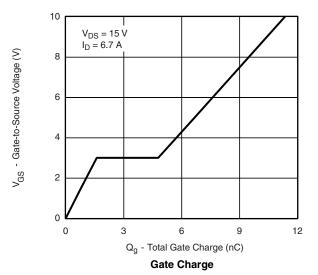


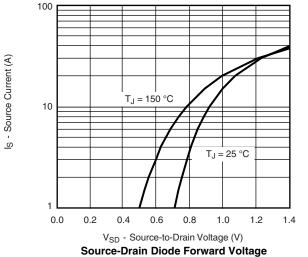


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#### On-Resistance vs. Drain Current

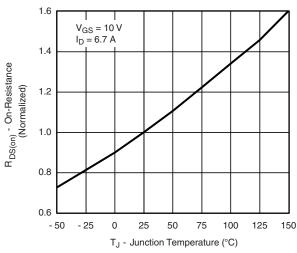




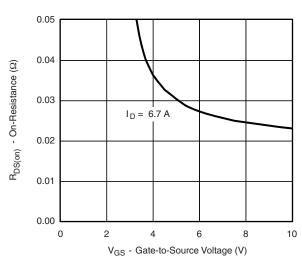
600 C<sub>iss</sub> 400 C<sub>oss</sub> C<sub>rss</sub> 0 5 10 15 20 25 30

V<sub>DS</sub> - Drain-to-Source Voltage (V)

#### Capacitance



On-Resistance vs. Junction Temperature

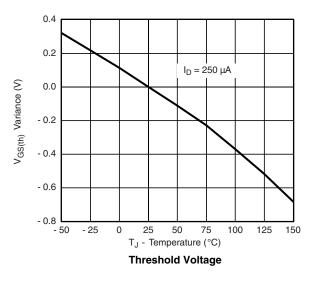


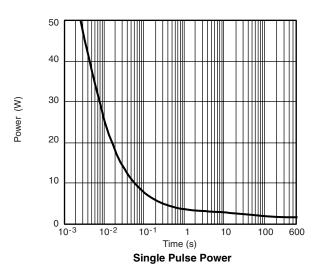
On-Resistance vs. Gate-to-Source Voltage

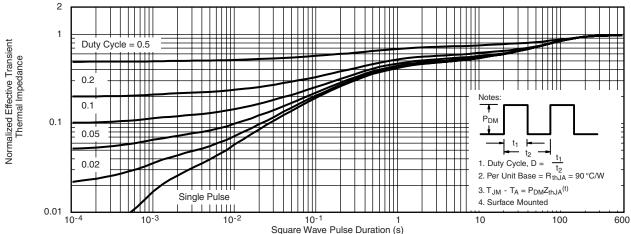
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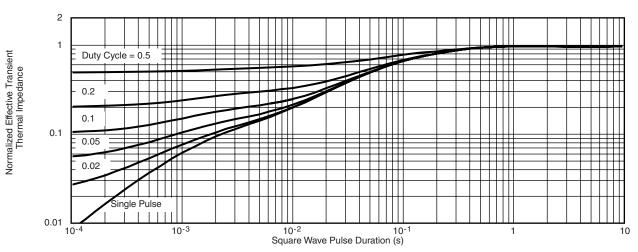
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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