Monolithic Quad SPST CMOS Analog Switches

(Obsolete for non-hermetic. Use DG201B/202B as pin-for-pin replacements.)

FEATURES

- BENEFITS
- ±15-V Input Range
- Low Off Leakage—I_{D(on)}: 0.1 nA
- Low On-Resistance— $r_{DS(on)}$: 115 Ω
- 44-V Maximum Supply Ratings
- TTL and CMOS Compatible
- Wide Input Range
- Low Distortion Switching
- Can Be Driven from Comparators or Op Amps Without Limiting Resistors

APPLICATIONS

- Disk Drives
- Radar Systems
- Communications Systems
- Sample-and-Hold

DESCRIPTION

The DG201A_MIL and DG202_MIL are quad SPST analog switches designed to provide accurate switching over a wide range of input signals. When combining a low on-resistance and a wide signal range (\pm 15 V) with low charge-transfer these devices are well suited for industrial and military applications.

Built on Vishay Siliconix' high voltage metal gate process to achieve optimum switch performance, each switch conducts equally well in both directions when on. When off these switches will block up to 30 V peak-to-peak and have a 44-V absolute maximum power supply rating.

These two devices are differentiated by the type of switch actions (See Truth Table).

The DG201A_MIL/202_MIL are available in hermetic packages. For plastic packages, use the DG201B/202B as pin-for-pin replacements.

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION

DG201A_MIL







LCC

Logic	DG201A_MIL	DG202_MIL
0	ON	OFF
1	OFF	ON

DG201A_MIL/202_MIL

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ORDERING INFORMATION				
Temp Range	Package	Part Number		
–55 to 125°C		DG201AAK		
	16-Pin CerDIP	DG201AAK/883, JM38510/12302BEA		
		7705301EA		
		DG202AK		
		DG202AK/883		
	16-Pin Sidebraze	JM38510/12302BEC		
–55 to 125°C		7705301EC		
	LCC-20	77053012A		

ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to V-

V+
GND
Digital Inputs ^a V _S , V _D
Current, Any Terminal Except S or D
Continuous Current, S or D
Peak Current, S or D (Pulsed at 1 ms, 10% duty cycle max) 70 mA

Storage Temperature	(K, Z Suffix)
	(J, Y Suffix)
Power Dissipation (Package)b
16-Pin CerDIP and Sidebraz	
LCC-20 ^d	

Notes:

- Signals on S_X , D_X , or IN_X exceeding V+ or V– will be clamped by internal diodes. Limit forward diode current to maximum current ratings. All leads welded or soldered to PC Board. Derate 12 mW/°C above 75°C Derate 10 mW/°C above 75°C a.
- b.
- c. d.
- **SCHEMATIC DIAGRAM (TYPICAL CHANNEL)**







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SPECIFICATIONS ^a								
		Test Conditions Unless Specified		A Suffix –55 to 125°C				
Parameter	Symbol	V+ = 15 V, V- = -15V $V_{IN} = 2.4 V, 0.8 V^{f}$	Temp ^b	Min ^d	Тур ^с	Max ^d	Unit	
Analog Switch					-			
Analog Signal Range ^e	V _{ANALOG}		Full	-15		15	V	
Drain-Source On-Resistance	r _{DS(on)}	$V_{D} = \pm 10 \text{ V}, \text{ I}_{S} = 1 \text{ mA}$	Room Full		115	175 250	Ω	
Source Off Leakage Current	I _{S(off)}	$V_{S}=\pm14~V,~V_{D}=\mp14~V$	Room Full	-1 -100	±0.02	1 100		
Drain Off Leakage Current	I _{D(off)}	$V_D = \pm 14 \text{ V}, V_S = \mp 14 \text{ V}$	Room Full	-1 -100	±0.02	1 100	nA	
Drain On Leakage Current	I _{D(on)}	$V_{S} = V_{D} = \pm 14 V$ Ro		-1 -200	±0.15	1 200		
Digital Control								
Input Current with Input Voltage High		V _{IN} = 2.4 V	Room Full	-1 -1	-0.0004			
	INH	V _{IN} = 15 V	Room Full		0.003	1 10	μΑ	
Input Current with Input Voltage Low	I _{INL}	V _{IN} = 0 V	Room Full	-1 -10	-0.0004			
Dynamic Characteristics								
Turn-On Time	t _{ON}	See Switching Time	Room		480	600		
Turn-Off Time	t _{OFF}	Test Circuit	Room		370	450	ns	
Charge Injection	Q	$C_{L} = 1000 \text{ pF}, \text{ V}_{g} = 0 \text{ V}$ $R_{g} = 0 \Omega$	Room		20		рС	
Source-Off Capacitance	C _{S(off)}		Room		5			
Drain-Off Capacitance	C _{D(off)}	V _S = 0 V, V _{IN} = 5 V, f = 1 MHz	Room		5		pF	
Channel On Capacitance	C _{D(on)} + C _{S(on)}	$V_{D} = V_{S} = 0 \text{ V}, V_{IN} = 0 \text{ V}$ $f = 1 \text{ MHz}$	Room		16		F.	
Off Isolation	OIRR	V _{IN} = 5 V, R _L = 75 Ω	Room		70			
Channel-to-Channel Crosstalk	X _{TALK}	$V_{\rm N} = 3 V, \ {\rm K}_{\rm S} = 70 \Omega^2$ $V_{\rm S} = 2 V, \ {\rm f} = 100 \ {\rm kHz}$	Room		90		dB	
Power Supply								
Positive Supply Current	l+		Room		0.9	2		
Negative Supply Current	I–	All Channels On or Off	Room	-1	-0.3		mA	

Notes:

a.

b.

tes: Refer to PROCESS OPTION FLOWCHART. Room = 25°C, Full = as determined by the operating temperature suffix. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet. Guaranteed by design, not subject to production test. V_{IN} = input voltage to perform proper function. c. d.

e. f.

DG201A_MIL/202_MIL

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TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)















DG201A_MIL/202_MIL Vishay Siliconix

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)









DG201A_MIL/202_MIL

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TEST CIRCUITS

Vo is the steady state output with switch on. Feedthrough via gate capacitance may result in spikes at leading and trailing edge of output waveform.









FIGURE 3. Off Isolation



FIGURE 4. Channel-to-Channel Crosstalk



 $[\]Delta V_O$ = measured voltage error due to charge injection The charge injection in coulombs is Q = C_L x ΔV_O





APPLICATION HINTS ^a				
V+ Positive Supply Voltage (V)	V– Negative Supply Voltage (V)	V _{IN} Logic Input Voltage V _{INH(min)} /V _{INL(max)} (V)	V _S or V _D Analog Voltage Range (V)	
15	-15	2.4/0.8	-15 to 15	
10	-12	2.4/0.8	-12 to 12	
12	-10	2.2/0.6	-10 to 10	
8 ^b	-8	2.0/0.5	-8 to 8	

Notes:

Application Hints are for DESIGN AID ONLY, not guaranteed and not subject to production testing.
Operation below ±8 V is not recommended.

APPLICATIONS





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APPLICATIONS



FIGURE 7. Active Low Pass Filter with Digitally Selected Break Frequency



FIGURE 8. A Precision Amplifier with Digitally Programable Input and Gains



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