

Low-Voltage Single SPDT Analog Switch

DESCRIPTION

The DG2002 is a single-pole/double-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed (t_{ON} : 8 ns, t_{OFF} : 6 ns), low on-resistance ($r_{DS(on)}$: 7 Ω) and small physical size (SC70), the DG2002 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2002 is built on Vishay Siliconix's low voltage J12 process. An epitaxial layer prevents latchup. Break-before-make is guaranteed for DG2002.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

FEATURES

- Low voltage operation (1.8 V to 5.5 V)
- Low on-resistance - $r_{DS(on)}$: 7 Ω
- Fast switching - t_{ON} : 8 ns, t_{OFF} : 6 ns
- Low charge injection - Q_{INJ} : 5 pC
- Low power consumption
- TTL/CMOS compatible
- 6-pin SC70 package



RoHS*
COMPLIANT

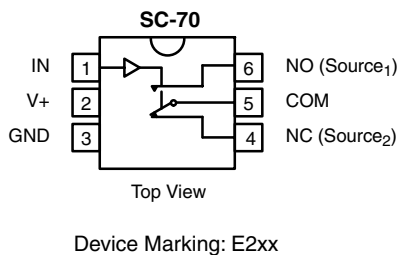
BENEFITS

- Reduced power consumption
- Simple logic interface
- High accuracy
- Reduce board space

APPLICATIONS

- Cellular phones
- Communication systems
- Portable test equipment
- Battery operated systems
- Sample and hold circuits

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



TRUTH TABLE

Logic	NC	NO
0	ON	OFF
1	OFF	ON

ORDERING INFORMATION

Temp Range	Package	Part Number
- 40 to 85 °C	SC70-6	DG2002DL-T1 DG2002DL-T1-E3

* Pb containing terminations are not RoHS compliant, exemptions may apply.

ABSOLUTE MAXIMUM RATINGS

Parameter	Limit	Unit
Referenced V+ to GND	- 0.3 to +6	V
IN, COM, NC, NO ^a	- 0.3 to (V+ + 0.3)	
Continuous Current (Any Terminal)	± 50	mA
Peak Current (Pulsed at 1 ms, 10 % duty cycle)	± 200	
Storage Temperature	- 65 to + 150	°C
Power Dissipation (Packages) ^b	6-Pin SC70 ^c	250
		mW

Notes:

a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC Board.

c. Derate 3.1 mW/°C above 70 °C.

SPECIFICATIONS V+ = 2.0 V

Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 2.0 V, ± 10 % VIN = 0.4 or 1.6 V ^e	Temp ^a	Limits - 40 to 85 °C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	VNO, VNC VCOM		Full	0		V+	V
On-Resistance	rON	V+ = 1.8 V, VCOM = 1.0 V, INO, INC = 10 mA	Room Full ^d		38 39.3	46.1 47.1	Ω
rON Flatness ^d	rON Flatness	V+ = 1.8 V, VCOM = 0 to V+, INO, INC = 10 mA	Room		21		
Switch Off Leakage Current ^f	INO(off) INC(off)	V+ = 2.2 V VNO, VNC = 0.5 V/1.5 V, VCOM = 1.5 V/0.5 V	Room Full ^d	- 250 - 3.0		250 3.0	pA nA
	ICOM(off)		Room Full ^d	- 250 - 3.0		250 3.0	pA nA
Channel-On Leakage Current ^f	ICOM(on)	V+ = 2.2 V, VNO, VNC = VCOM = 0.5 V/1.5 V	Room Full ^d	- 250 - 3.0		250 3.0	pA nA
Digital Control							
Input High Voltage	VINH		Full	1.6			V
Input Low Voltage	VINL		Full			0.4	
Input Capacitance ^d	Cin		Full		3		pF
Input Current	INL or INH	VIN = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time	tON	VNO or VNC = 1.5 V, RL = 300 Ω, CL = 35 pF Figures 1 and 2	Room Full ^d		22	31 32	ns
Turn-Off Time	tOFF		Room Full ^d		10	17 18	
Break-Before-Make Time	td		Room	1	12		
Charge Injection ^d	QINJ	CL = 1 nF, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation ^d	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		- 67		dB
Crosstalk ^d	XTALK		Room		- 71		
NO, NC Off Capacitance ^d	CNO(off) CNC(off)	VIN = 0 or V+, f = 1 MHz	Room		5		pF
Channel-On Capacitance ^d	CON		Room		29		
Power Supply							
Power Supply Range	V+			1.8		2.2	V
Power Supply Current ^d	I+	VIN = 0 or V+			0.01	1.0	μA
Power Consumption	PC					2.2	μW



SPECIFICATIONS V+ = 3.0 V							
Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 3 V, ±10 % VIN = 0.4 or 2.0 V ^e	Temp ^a	Limits - 40 to 85 °C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	V _{NO} , V _{NC} V _{COM}		Full	0		V+	V
On-Resistance ^d	r _{ON}	V+ = 2.7 V, V _{COM} = 1.5 V, I _{NO} , I _{NC} = 10 mA	Room Full		12.2 13	14.8 15.8	Ω
r _{ON} Flatness ^d	r _{ON} Flatness	V+ = 2.7 V, V _{COM} = 0 to V+, I _{NO} , I _{NC} = 10 mA	Room		5		
Switch Off Leakage Current ^f	I _{NO(off)} I _{NC(off)}	V+ = 3.3 V V _{NO} , V _{NC} = 1 V/3 V, V _{COM} = 3 V/1 V	Room Full	- 500 - 4.0		500 4.0	pA nA
	I _{COM(off)}		Room Full	- 500 - 4.0		500 4.0	pA nA
Channel-On Leakage Current ^f	I _{COM(on)}	V+ = 3.3 V, V _{NO} , V _{NC} = V _{COM} = 1 V/3 V	Room Full	- 500 - 4.0		500 4.0	pA nA
Digital Control							
Input High Voltage	V _{INH}		Full	2			V
Input Low Voltage	V _{INL}		Full			0.4	
Input Capacitance ^d	C _{in}		Full		3		pF
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time ^d	t _{ON}	V _{NO} or V _{NC} = 2.0 V, R _L = 300 Ω, C _L = 35 pF Figures 1 and 2	Room Full		12	21 22	ns
Turn-Off Time ^d	t _{OFF}		Room Full		7	14 15	
Break-Before-Make Time ^d	t _d		Room	1	6		
Charge Injection ^d	Q _{INJ}	C _L = 1 nF, V _{GEN} = 0 V, R _{GEN} = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation ^d	OIRR	R _L = 50 Ω, C _L = 5 pF, f = 1 MHz	Room		- 67		dB
Crosstalk ^d	X _{TALK}		Room		- 69		
NO, NC Off Capacitance ^d	C _{NO(off)} C _{NC(off)}	V _{IN} = 0 or V+, f = 1 MHz	Room		5		pF
Channel-On Capacitance ^d	C _{ON}		Room		29		
Power Supply							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	V _{IN} = 0 or V+			0.01	1.0	μA
Power Consumption	P _C						3.3

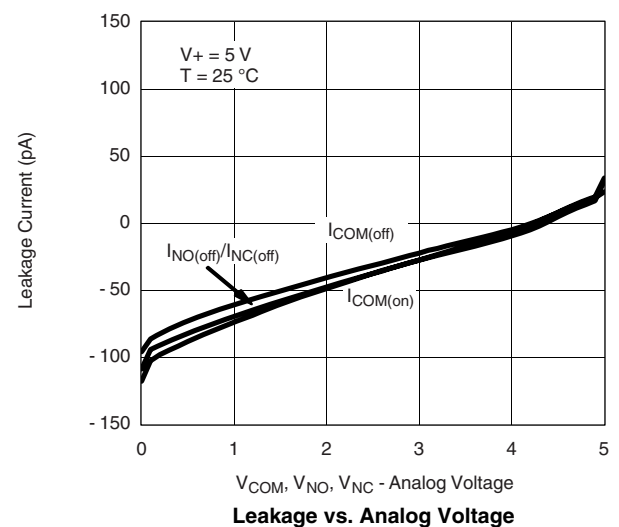
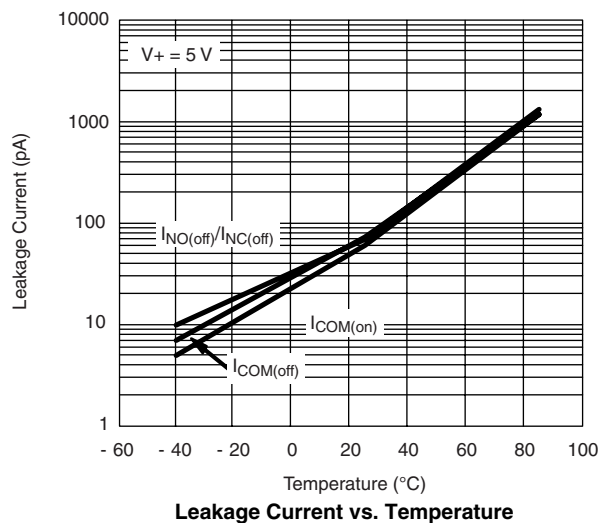
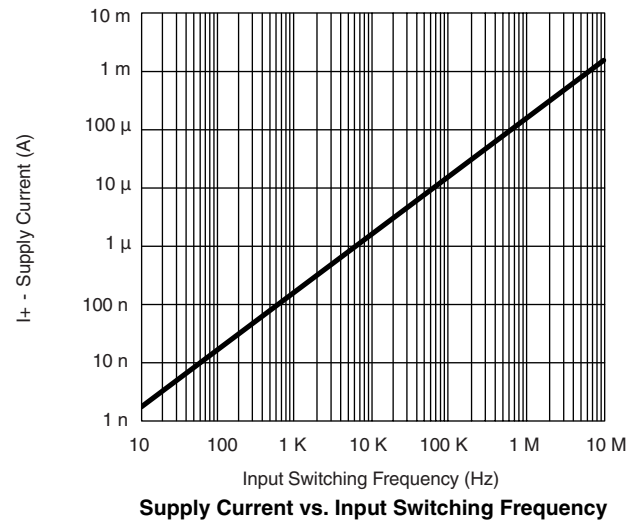
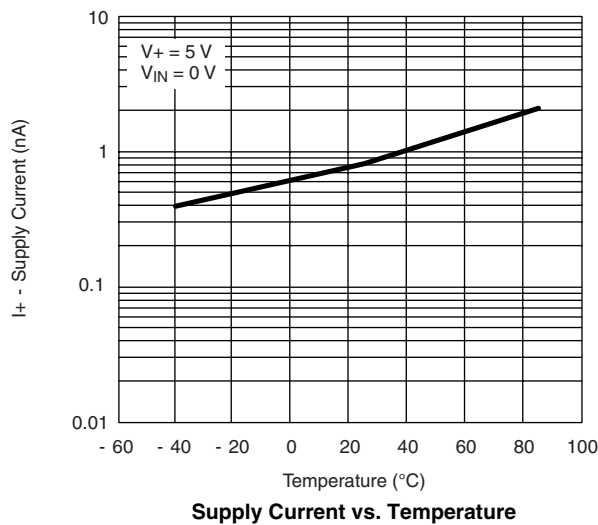
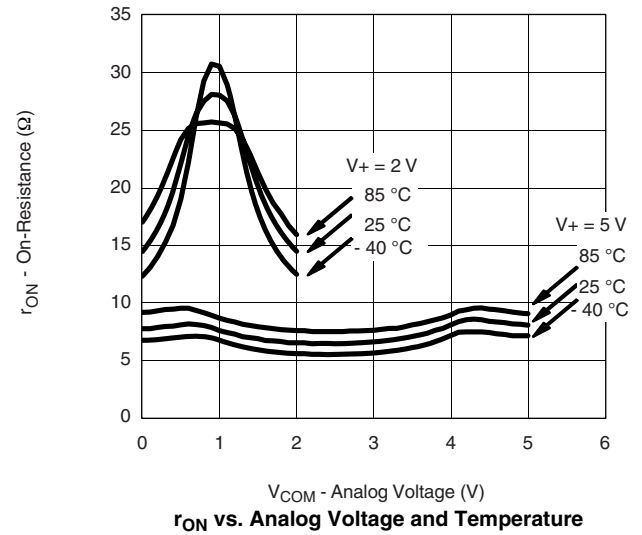
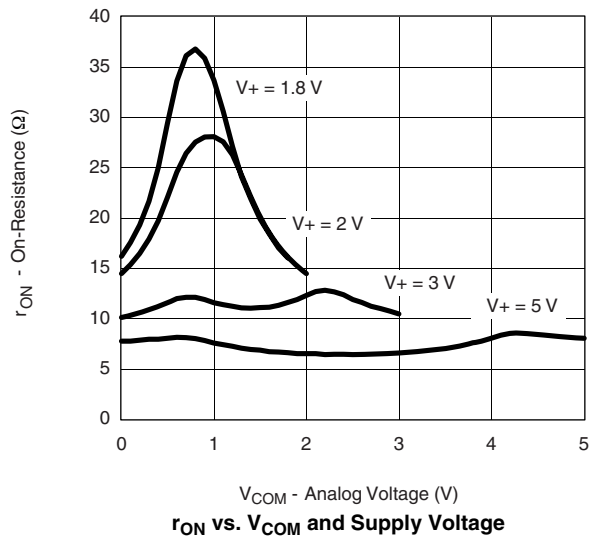
SPECIFICATIONS V+ = 5.0 V							
Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 5 V, ± 10 % VIN = 0.8 or 2.4 V ^e	Temp ^a	Limits - 40 to 85 °C			Unit
				Min ^b	Typ ^c	Max ^b	
Analog Switch							
Analog Signal Range ^d	VNO, VNC, VCOM		Full	0		V+	V
On-Resistance	rON	V+ = 4.5 V, VCOM = 3 V, INO, INC = 10 mA	Room Full		6.4 7.4	7.8 8.8	Ω
rON Flatness ^d	rON Flatness	V+ = 4.5 V, VCOM = 0 to V+, INO, INC = 10 mA	Room		3		
Switch Off Leakage Current	INO(off), INC(off)	V+ = 5.5 V VNO, VNC = 1 V/4.5 V, VCOM = 4.5 V/1 V	Room Full	- 1.0 - 4.0		1.0 4.0	nA
	ICOM(off)		Room Full	- 1.0 - 4.0		1.0 4.0	
Channel-On Leakage Current	ICOM(on)	V+ = 5.5 V, V+ = 5.5 V VNO, VNC = VCOM = 1 V/4.5 V	Room Full	- 1.0 - 4.0		1.0 4.0	
Digital Control							
Input High Voltage	VINH		Full	2.4			V
Input Low Voltage	VINL		Full			0.8	
Input Capacitance	Cin		Full		3		pF
Input Current	INL or INH	VIN = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time ^d	tON	VNO or VNC = 3 V, RL = 300 Ω, CL = 35 pF Figures 1 and 2	Room Full		8	15 16	ns
Turn-Off Time ^d	tOFF		Room Full		6	13 14	
Break-Before-Make Time ^d	td		Room	1	4		
Charge Injection ^d	QINJ	CL = 1 nF, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation ^d	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		- 69		dB
Crosstalk ^d	XTALK		Room		- 69		
Source-Off Capacitance ^d	CNO(off), CNC(off)	VIN = 0 or V+, f = 1 MHz	Room		5		pF
Channel-On Capacitance ^d	CON		Room		29		
Power Supply							
Power Supply Range	V+			4.5		5.5	V
Power Supply Current	I+	VIN = 0 or V+			0.01	1.0	μA
Power Consumption	PC						5.5

Notes:

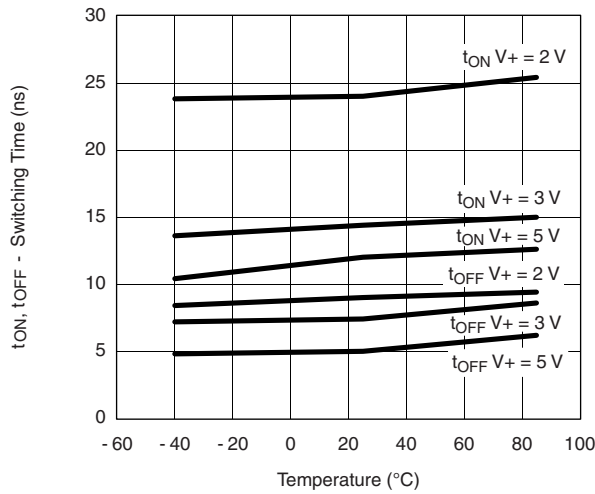
- Room = 25 °C, Full = as determined by the operating suffix.
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Typical values are for design aid only, not guaranteed nor subject to production testing.
- Guarantee by design, nor subjected to production test.
- V_{IN} = input voltage to perform proper function.
- Guaranteed by 5 V leakage testing, not production tested.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

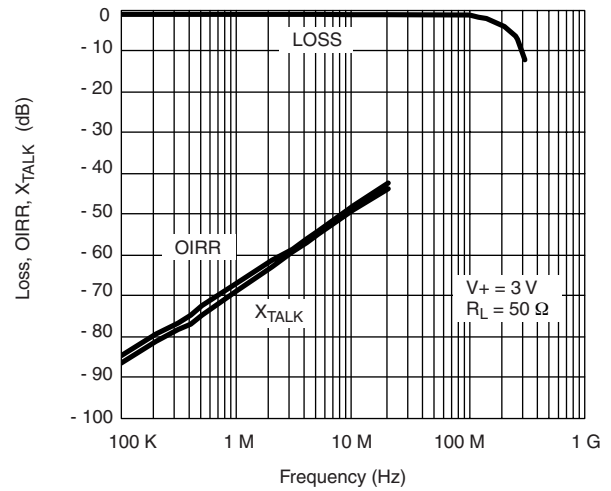
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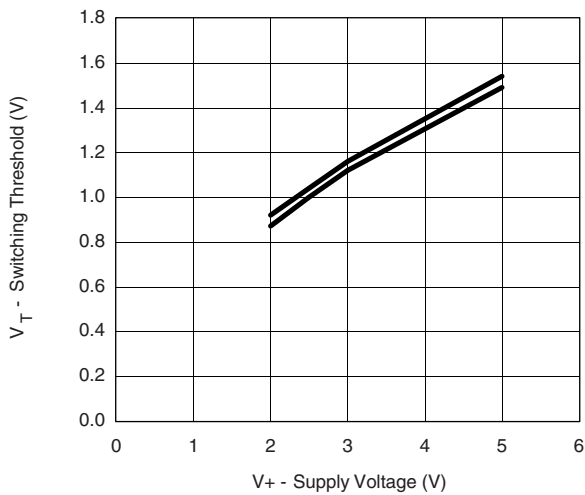
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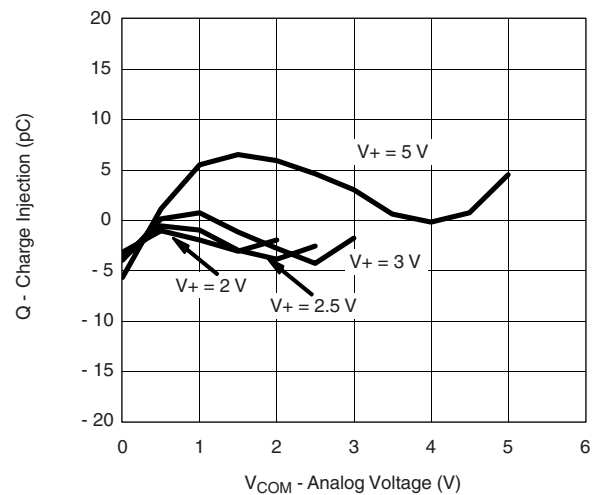
Switching Time vs. Temperature and Supply Voltage



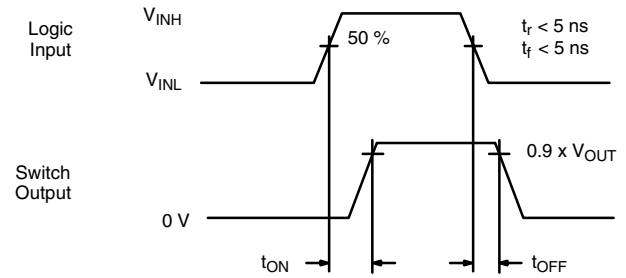
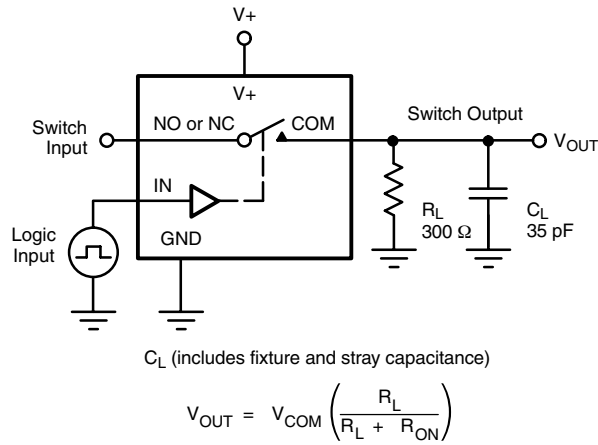
Insertion Loss, Off-Isolation Crosstalk vs. Frequency



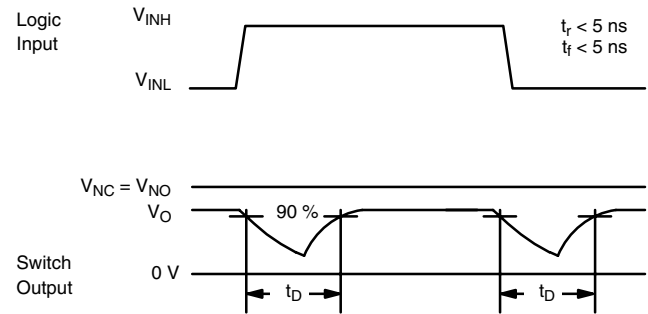
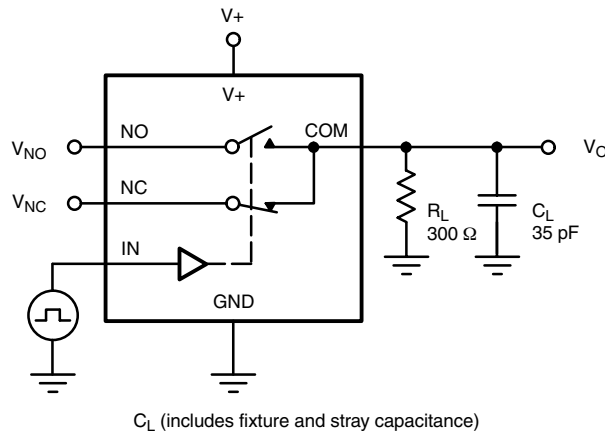
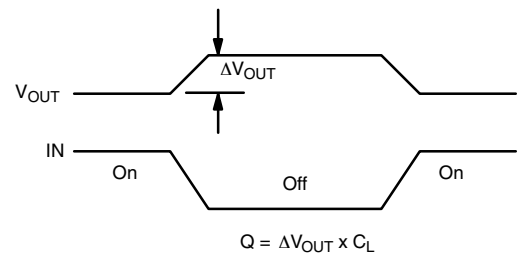
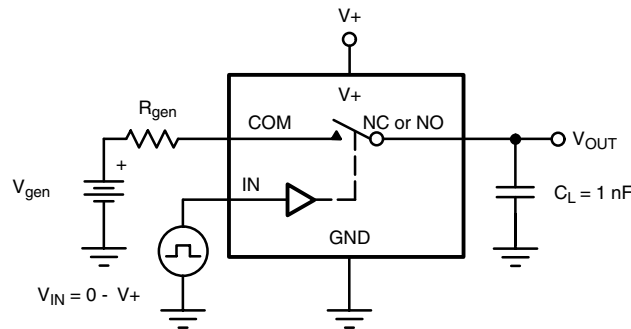
Switching Threshold vs. Supply Voltage



Charge Injection vs. Analog Voltage

TEST CIRCUITS


Logic "1" = Switch On
Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time

Figure 2. Break-Before-Make Interval


IN depends on switch configuration: input polarity determined by sense of switch.

Figure 3. Charge Injection

TEST CIRCUITS

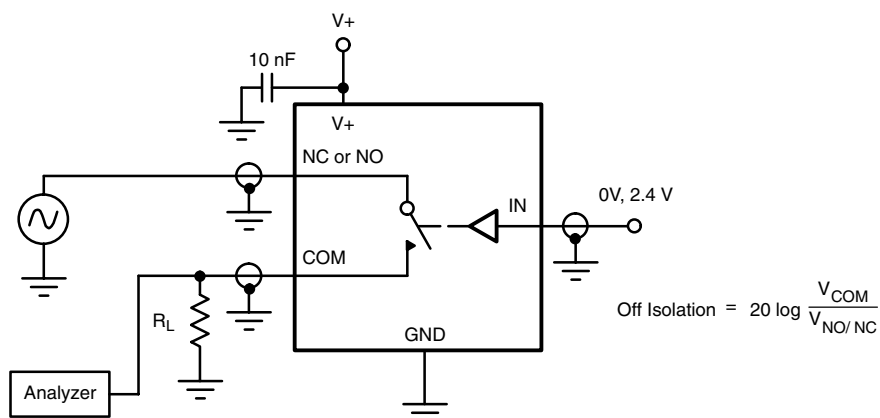


Figure 4. Off-Isolation

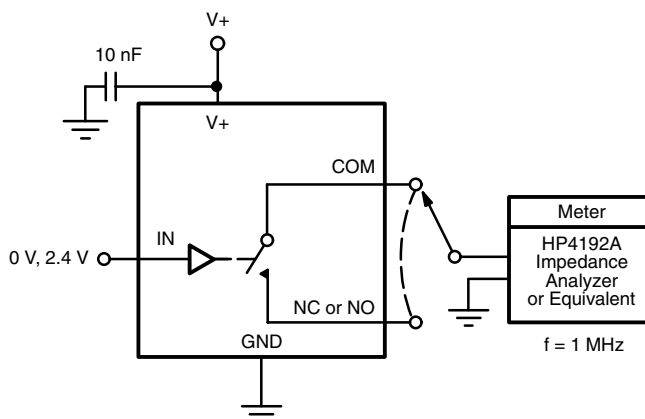
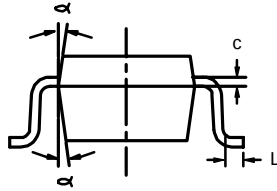
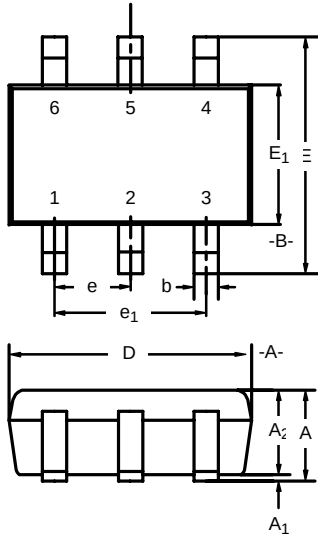


Figure 5. Channel Off/On Capacitance

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SC-70: 6-LEADS



Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.90	–	1.10	0.035	–	0.043
A ₁	–	–	0.10	–	–	0.004
A ₂	0.80	–	1.00	0.031	–	0.039
b	0.15	–	0.30	0.006	–	0.012
c	0.10	–	0.25	0.004	–	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65BSC			0.026BSC		
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
α	7°Nom			7°Nom		

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DWG: 5550



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