

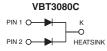
## Vishay General Semiconductor

# **Dual Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.46 \text{ V}$  at  $I_F = 5 \text{ A}$ 







### **DESIGN SUPPORT TOOLS**

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PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15 A			
$V_{RRM}$	80 V			
I <sub>FSM</sub>	150 A			
$V_F$ at $I_F = 15 A$	0.65 V			
T <sub>J</sub> max.	150 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Common cathode			

### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

High efficiency operation

ROHS COMPLIANT HALOGEN

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

FREE

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT3080C	UNIT	
Maximum repetitive peak reverse voltage	$V_{RRM}$	80	V		
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	30	А	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	150	А	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS SYMBO		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode <sup>(1)</sup>	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub>	0.52	-	V
	I <sub>F</sub> = 7.5 A			0.58	=	
	I <sub>F</sub> = 15 A			0.75	0.82	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.46	-	
	I <sub>F</sub> = 7.5 A			0.52	-	
	I <sub>F</sub> = 15 A			0.65	0.70	
Reverse current per diode (2)	V 80 V	T <sub>A</sub> = 25 °C	- I <sub>R</sub>	30	700	μΑ
	V <sub>R</sub> = 80 V	T <sub>A</sub> = 125 °C		20	35	mA

### **Notes**

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VBT3080C	UNIT		
Typical thermal resistance	per diode	$R_{ hetaJC}$	2.5	°C/W	
	per device		2.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT3080C-M3/4W	1.39	4W	50/tube	Tube	
TO-263AB	VBT3080C-M3/8W	1.39	8W	800/reel	Tape and reel	

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

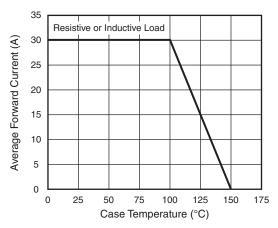


Fig. 1 - Maximum Forward Current Derating Curve

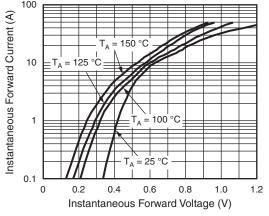


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

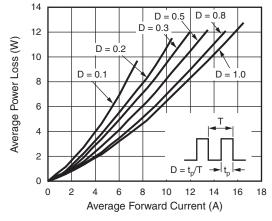


Fig. 2 - Forward Power Loss Characteristics Per Diode

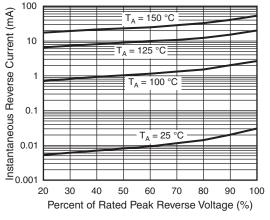


Fig. 4 - Typical Reverse Characteristics Per Diode



0.105 (2.67)

0.095 (2.41)

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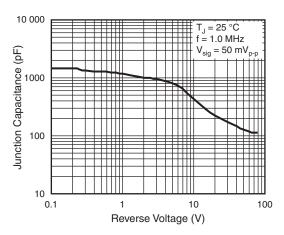


Fig. 5 - Typical Junction Capacitance Per Diode

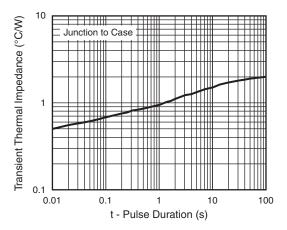


Fig. 6 - Typical Transient Thermal Impedance Per Device

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

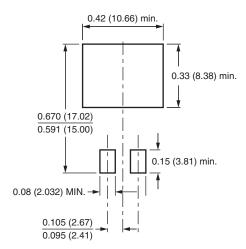
D<sup>2</sup>PAK (TO-263AB)

0.205 (5.20)

0.195 (4.95)

#### 0.411 (10.45) 0.190 (4.83) 0.380 (9.65) 0.055 (1.40) 0.160 (4.06) 0.245 (6.22) 0.045 (1.14) MIN. 0.055 (1.40) 0.360 (9.14) 0.047 (1.19) 0.320 (8.13) 0.624 (15.85) Κ 0.591 (15.00) -0 to 0.01 (0 to 0.254) 7 0.110 (2.79) 0.090 (2.29) 0.037 (0.940) 0.021 (0.53) 0.027 (0.686)

## **Mounting Pad Layout**



0.014 (0.36)

0.140 (3.56)

0.110 (2.79)



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