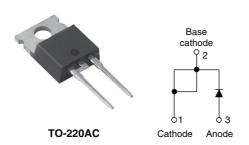


Vishay Semiconductors

Input Rectifier Diode, 10 A



PRODUCT SUMMARY				
V _F at 10 A	< 1.1 V			
I _{FSM}	200 A			
V_{RRM}	800 V/1200 V			

FEATURES

- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



RoHS

- APPLICATIONSInput rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

The 10ETS..PbF rectifier series has been optimized for very low forward voltage drop, with moderate leakage.

The glass passivation technology used has reliable operation up to 150 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	12.0	16.0	А			

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	10	А			
V _{RRM}		800/1200	V			
I _{FSM}		200	А			
V _F	10 A, T _J = 25 °C	1.1	V			
TJ		- 40 to 150	°C			

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
10ETS08PbF	800	900	0.5		
10ETS12PbF	1200	1300	0.5		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 105 °C, 180° conduction half sine wave	10			
Maximum peak one cycle	_	10 ms sine pulse, rated V _{RRM} applied	170	Α		
non-repetitive surge current	I _{FSM}	10 ms sine pulse, no voltage reapplied	200			
Maximum I ² t for fusing I ² t		10 ms sine pulse, rated V _{RRM} applied	130	A ² s		
		10 ms sine pulse, no voltage reapplied	145	A-5		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	1450	A²√s		

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



Input Rectifier Diode, 10 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	VALUES	UNITS					
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C	1.1	٧			
Forward slope resistance	r _t	T _{.1} = 150 °C	20	m $Ω$			
Threshold voltage	V _{F(TO)}	1J = 150 C	0.82	V			
Maximum reverse leakage current	I	T _J = 25 °C	V Patad V	0.05	mA		
Waxiiffuffi reverse leakage current	IRM	$V_R = Rated V_{RRM}$	0.50	IIIA			

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5			
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} (1)	(1) 62		°C/W		
Soldering temperature	T _S		240	°C		
Approximate weight			2	g		
Approximate weight			0.07	oz.		
Marking daving				TS08		
Marking device		Case style TO-220AC	10ET	TS12		

Note

⁽¹⁾ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 µm) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994





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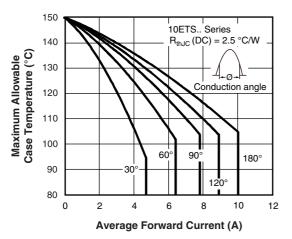


Fig. 1 - Current Rating Characteristics

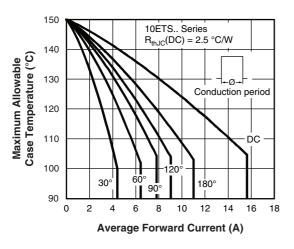


Fig. 2 - Current Rating Characteristics

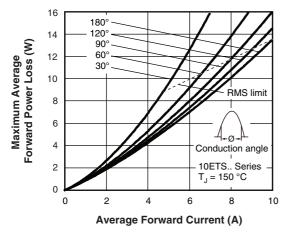


Fig. 3 - Forward Power Loss Characteristics

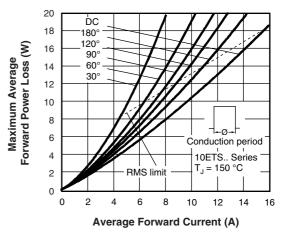


Fig. 4 - Forward Power Loss Characteristics

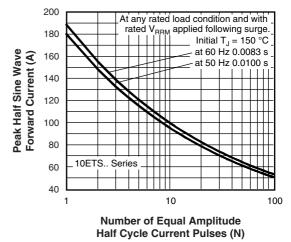


Fig. 5 - Maximum Non-Repetitive Surge Current

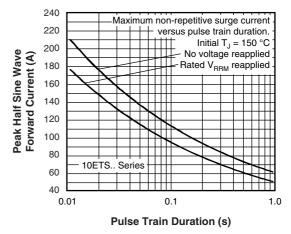


Fig. 6 - Maximum Non-Repetitve Surge Current

Vishay Semiconductors

Input Rectifier Diode, 10 A



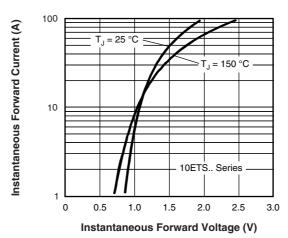


Fig. 7 - Forward Voltage Drop Characteristics

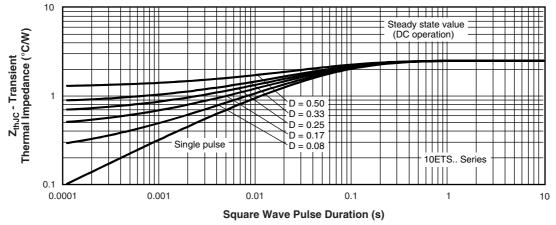


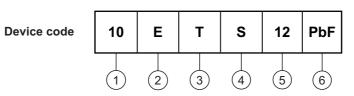
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 10 A

Vishay Semiconductors

ORDERING INFORMATION TABLE



- Current rating (10 = 10 A)
- 2 Circuit configuration:

E = Single diode

- 3 Package:
 - T = TO-220AC
- 4 Type of silicon:

S = Standard recovery rectifier

5 - Voltage code x 100 = V_{RRM} - 08 = 800 V 12 = 1200 V

6 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95221</u>				
Part marking information	www.vishay.com/doc?95224			

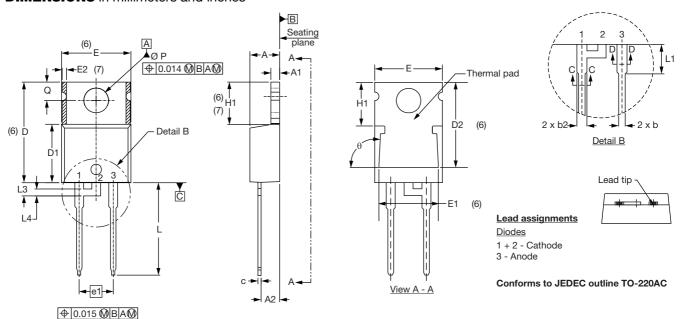
Document Number: 94337 Revision: 22-Jul-10



Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.56	2.92	0.101	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.25	0.585	0.600	3	
D1	8.38	9.02	0.330	0.355		
D2	11.68	12.88	0.460	0.507	6	
Е	10.11	10.51	0.398	0.414	3, 6	

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

Document Number: 95221 Revision: 07-Mar-11

Legal Disclaimer Notice



Vishay

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