### BYW52, BYW53, BYW54, BYW55, BYW56

### Vishay Semiconductors



HALOGEN

# **Standard Avalanche Sinterglass Diode**



#### **MECHANICAL DATA**

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

**Mounting position:** any **Weight:** approx. 369 mg

#### **FEATURES**

- Controlled avalanche characteristics
- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- High surge current loading
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



• Rectification, general purpose

PARTS TABLE			
PART	TYPE DIFFERENTIATION	PACKAGE	
BYW52	V <sub>R</sub> = 200 V; I <sub>FAV</sub> = 2 A	SOD-57	
BYW53	V <sub>R</sub> = 400 V; I <sub>FAV</sub> = 2 A	SOD-57	
BYW54	V <sub>R</sub> = 600 V; I <sub>FAV</sub> = 2 A	SOD-57	
BYW55	$V_R = 800 \text{ V}; I_{FAV} = 2 \text{ A}$	SOD-57	
BYW56	V <sub>R</sub> = 1000 V; I <sub>FAV</sub> = 2 A	SOD-57	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYW52	$V_R = V_{RRM}$	200	V	
		BYW53	$V_R = V_{RRM}$	400	V	
		BYW54	$V_R = V_{RRM}$	600	V	
		BYW55	$V_R = V_{RRM}$	800	V	
		BYW56	$V_R = V_{RRM}$	1000	V	
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	50	Α	
Repetitive peak forward current			I <sub>FRM</sub>	12	Α	
Average forward current	φ = 180 °		I <sub>FAV</sub>	2	А	
Pulse avalanche peak power	$t_p = 20 \mu s$ half sine wave, $T_j = 175  ^{\circ}C$		P <sub>R</sub>	1000	W	
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	I <sub>(BR)R</sub> = 1 A, T <sub>j</sub> = 175 °C		E <sub>R</sub>	20	mJ	
i <sup>2</sup> t-rating			i <sup>2</sup> t	8	A <sup>2</sup> s	
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C	

<b>MAXIMUM THERMAL RESISTANCE</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, T <sub>L</sub> = constant	$R_{thJA}$	45	K/W	
Junction ambient	On PC board with spacing 25 mm	$R_{thJA}$	100	K/W	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX	UNIT
Forward voltage	I <sub>F</sub> = 1 A	$V_{F}$	-	0.9	1	V
Reverse current	$V_R = V_{RRM}$	I <sub>R</sub>	ı	0.1	1	μΑ
neverse current	$V_R = V_{RRM}, T_j = 100  ^{\circ}C$	I <sub>R</sub>	ı	5	10	μΑ
Breakdown voltage	$I_R = 100 \mu A, t_p/T = 0.01, t_p = 0.3 \text{ ms}$	V <sub>(BR)</sub>	ı	1	1600	V
Diode capacitance	$V_R = 4 V$ , $f = 1 MHz$	$C_D$	ı	18	-	pF
Deverse receiver time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, i_R = 0.25 \text{ A}$	t <sub>rr</sub>	ı	ī	4	μs
Reverse recovery time	$I_F = 1 \text{ A}, \text{ dI/dt} = 5 \text{ A/}\mu\text{s}, V_R = 50 \text{ V}$	t <sub>rr</sub>	-	-	4	μs
Reverse recovery charge	$I_F = 1 A$ , $dI/dt = 5 A/\mu s$	Q <sub>rr</sub>	-	-	200	nC

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

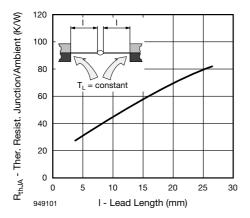


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

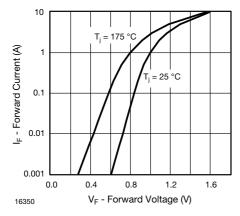


Fig. 2 - Forward Current vs. Forward Voltage

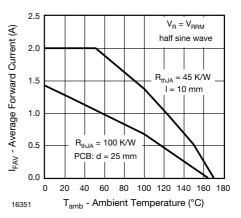


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

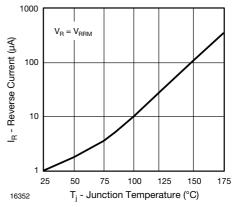


Fig. 4 - Reverse Current vs. Junction Temperature

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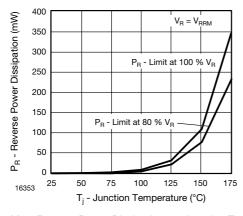


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

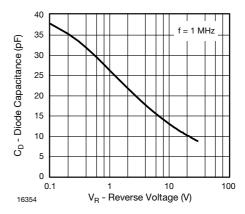


Fig. 6 - Diode Capacitance vs. Reverse Voltage

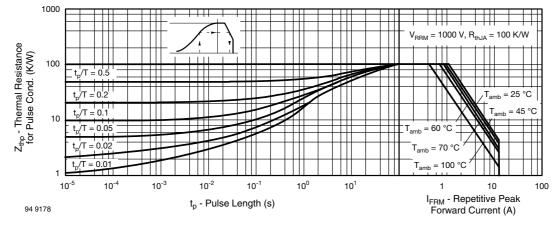
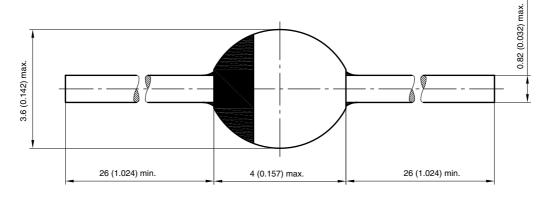


Fig. 7 - Thermal Response

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-57



20543 Rev. 3 - Date: 09.February 2005 Document no.:6.563-5006.3-4

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Revision: 12-Mar-12 Document Number: 91000