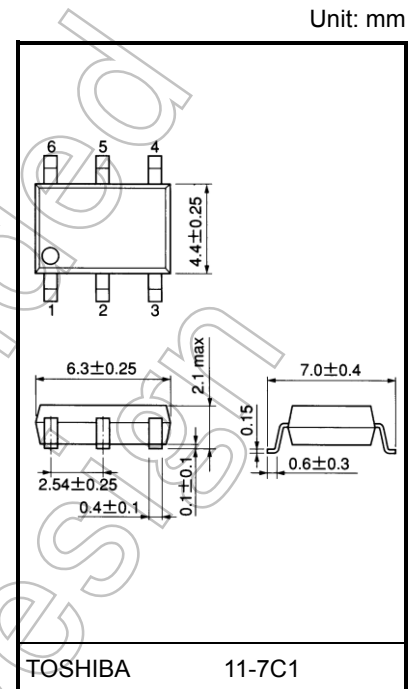


TLP3120

High-Speed Memory Tester
 High-Speed Logic Tester
 High-Frequency Measurement Equipment

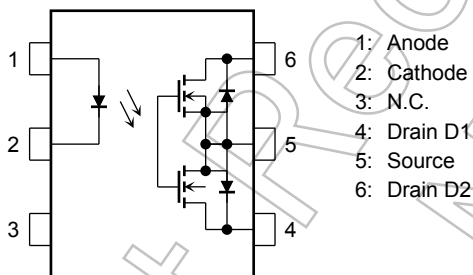
The Toshiba TLP3120 consists of an infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

- 6-pin SOP (2.54SOP6): 2.1 mm high, 2.54 mm pitch
- Normally opened (form A) device
- Peak OFF-state voltage: 80 V (min)
- Trigger LED current: 5 mA (max)
- ON-state current: 1.25 A (max)
- ON-state resistance: 0.15 Ω (max)
- Capacitance between output terminals: 1000 pF (max)
- Isolation voltage: 1500 V_{rms} (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
 File No.E67349



Weight: 0.13 g (typ.)

Pin Configuration (top view)



Start of commercial production
 2000-12

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|--|---|----------------------------------|------------|---------|
| Led | Forward current | I_F | 50 | mA |
| | Forward current derating (Ta ≥ 25°C) | $\Delta I_F / ^\circ\text{C}$ | -0.5 | mA/°C |
| | Reverse voltage | V_R | 5 | V |
| | Diode power dissipation | P_D | 50 | mW |
| | Diode power dissipation derating (Ta ≥ 25°C) | $\Delta P_D / ^\circ\text{C}$ | -0.5 | mW/°C |
| | Junction temperature | T_j | 125 | °C |
| Detector | OFF-state output terminal voltage | V_{OFF} | 80 | V |
| | ON-state current | I_{ON} | 1.25 | A |
| | ON-state current derating (Ta ≥ 25°C) | $\Delta I_{ON} / ^\circ\text{C}$ | -12.5 | mA/°C |
| | Output power dissipation | P_O | 234 | mW |
| | Output power dissipation derating (Ta ≥ 25°C) | $\Delta P_O / ^\circ\text{C}$ | -2.34 | mW / °C |
| | Junction temperature | T_j | 125 | °C |
| Storage temperature range | | T_{stg} | -40 to 125 | °C |
| Operating temperature range | | T_{opr} | -20 to 85 | °C |
| Lead soldering temperature (10 s) | | T_{sol} | 260 | °C |
| Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1) | | BVS | 1500 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device is considered as a two-terminal device. LED side pins are shorted together and detector side pins are shorted together.

Recommended Operating Conditions

| Characteristics | Symbol | Min | Typ. | Max | Unit |
|-----------------------|-----------|-----|------|------|------|
| Supply voltage | V_{DD} | — | — | 64 | V |
| Forward current | I_F | 5 | — | 30 | mA |
| ON-state current | I_{ON} | — | — | 1.25 | A |
| Operating temperature | T_{opr} | 25 | — | 60 | °C |

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Typ. | Max | Unit |
|-----------------|-------------------------------|-----------|---|-----|------|------|------|
| LED | Forward current | V_F | $I_F = 10 \text{ mA}$ | 1.0 | 1.15 | 1.3 | V |
| | Reverse current | I_R | $V_R = 5 \text{ V}$ | — | — | 10 | μA |
| | Capacitance between terminals | C_T | $V_F = 0 \text{ V}, f = 1 \text{ MHz}$ | — | 15 | — | pF |
| Detector | OFF-state current | I_{OFF} | $V_{OFF} = 20 \text{ V}, T_a = 50 \text{ }^\circ\text{C}$ | — | 1200 | 1500 | pA |
| | Capacitance between terminals | C_{OFF} | $V = 0 \text{ V}, f = 100 \text{ MHz}$ | — | 460 | 1000 | pF |

Coupled Electrical Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
|---------------------|----------|---|-----|------|------|----------|
| Trigger LED current | I_{FT} | $I_{ON} = 1.25 \text{ A}$ | — | 2 | 5 | mA |
| Return LED current | I_{FC} | $I_{OFF} = 10 \mu\text{A}$ | 0.2 | — | — | mA |
| ON-state resistance | R_{ON} | $I_{ON} = 1.25 \text{ A}, I_F = 5 \text{ mA}$ | — | 0.11 | 0.15 | Ω |

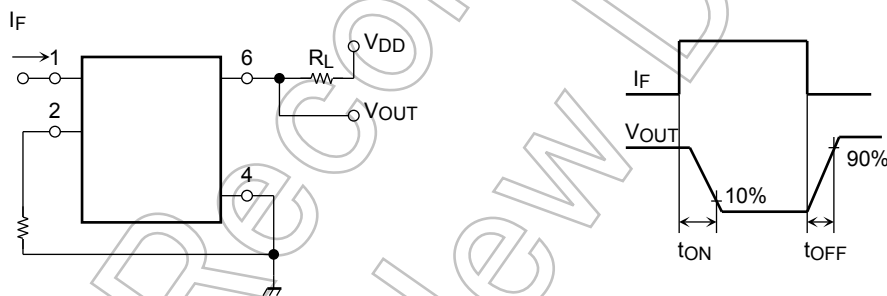
Isolation Characteristics (Ta = 25°C)

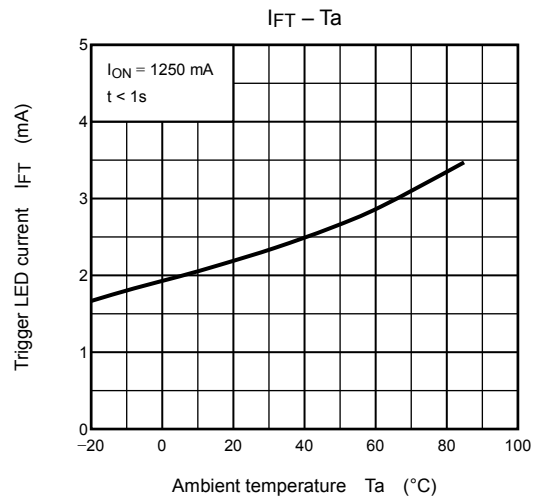
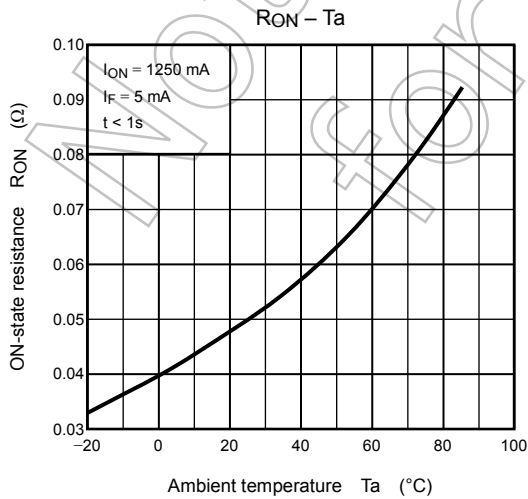
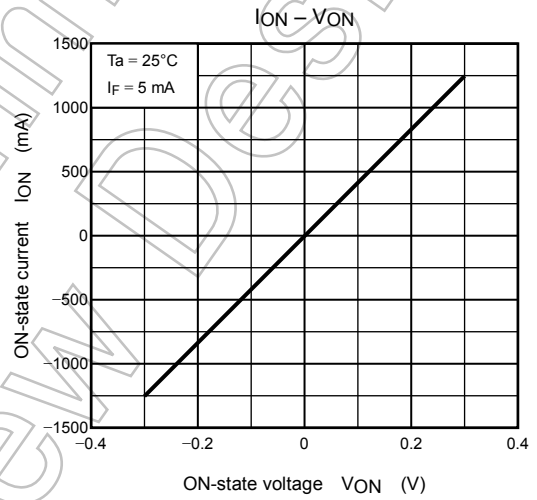
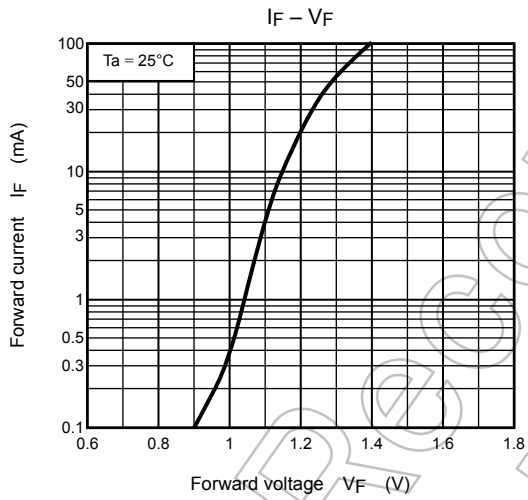
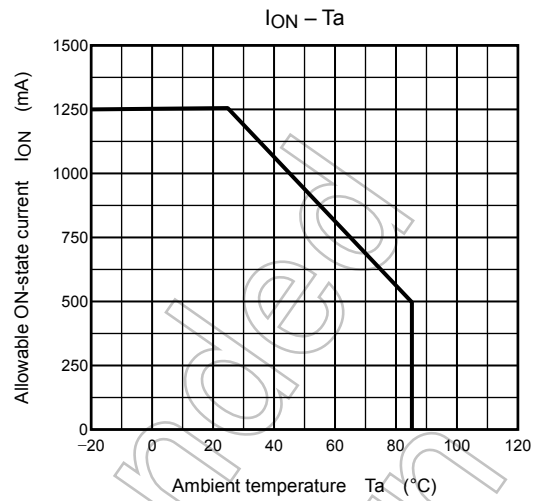
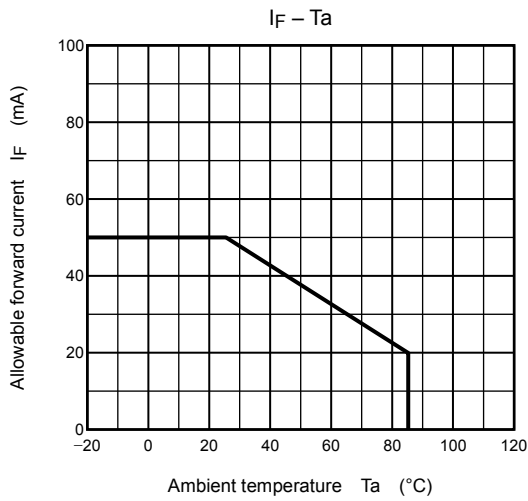
| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
|-----------------------------|--------|---|--------------------|-----------|-----|----------|
| Capacitance input to output | C_S | $V_S = 0 \text{ V}, f = 1 \text{ MHz}$ | — | 0.8 | — | pF |
| Isolation resistance | R_S | $V_S = 500 \text{ V}, \text{R.H.} \leq 60 \%$ | 5×10^{10} | 10^{14} | — | Ω |
| Isolation voltage | BV_S | AC, 60 s | 1500 | — | — | Vrms |

Switching Characteristics (Ta = 25°C)

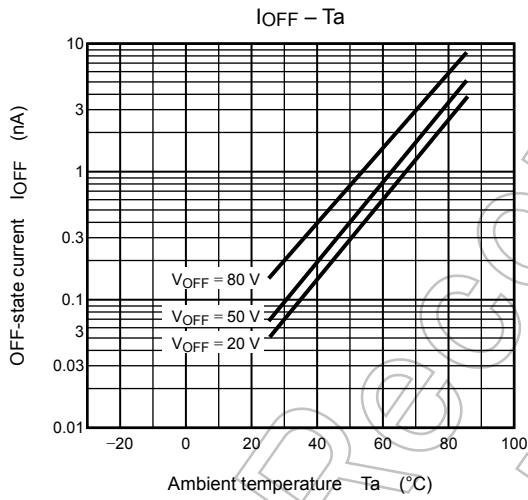
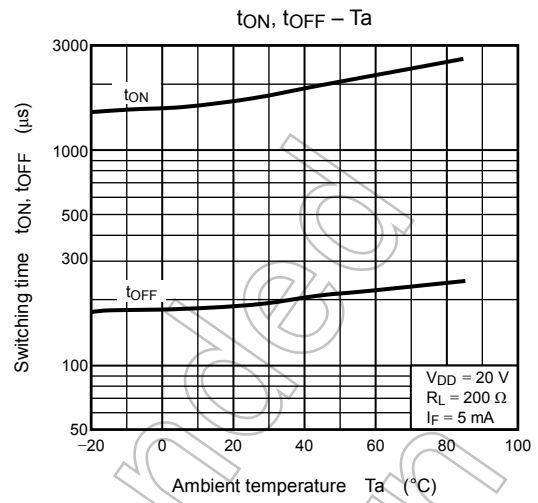
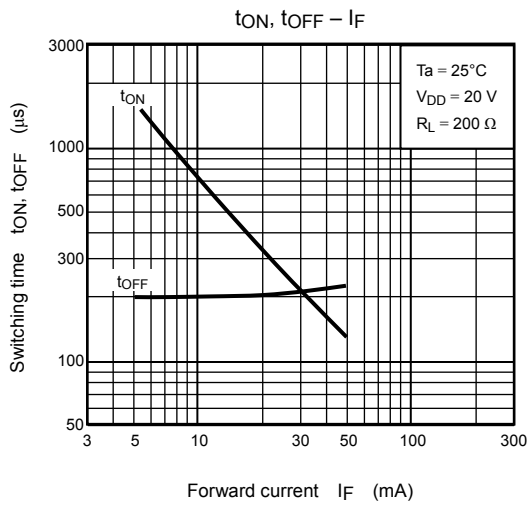
| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
|-----------------|-----------|--|-----|------|-----|------|
| Turn-ON time | t_{ON} | $R_L = 200 \Omega$ | — | 2.0 | 3.0 | ms |
| Turn-OFF time | t_{OFF} | $V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note 2) | — | 0.7 | 1.0 | |

Note 2: Switching time test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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