TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SGU04FU

Inverter (Unbuffered)

Features

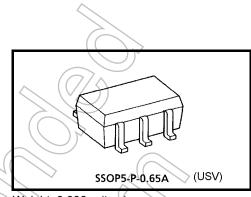
High output current : ±8 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 1.9 ns (typ.)

at V_{CC} = 3.3 V, 15pF

• Operating voltage range : V_{CC} = 0.9 to 3.6 V

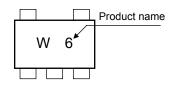
• 3.6-V tolerant input

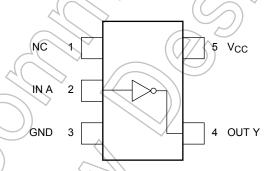


Weight: 0.006 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Rating (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V_{CC}	-0.5 to 4.6	V	
DC input voltage	\rangle V_{IN}	-0.5 to 4.6	٧	
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V	
Input diode current	l _{IK}	-20	mA	
Output diode current	lok <	±20 (Note 1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	lco)) ±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Vout < GND, Vout > Vcc

Start of commercial production 2005-02

IEC Logic Symbol

Truth Table



Α	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6
Input voltage	V _{IN}	0 to 3.6
Output voltage	V _{OUT}	0 to V _{CC} V
Output current	I _{OH} /I _{OL}	±8.0 (Note 2) ±4.0 (Note 3) ±3.0 (Note 4) ±1.7 (Note 5) ±0.3 (Note 6) ±0.02 (Note 7)
Operating temperature	T _{opr}	40 to 85

Note 2: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 5: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 6: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 7: $V_{CC} = 0.9 \text{ V}$



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Electrical Characteristics

DC Characteristics

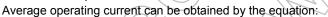
Characteristics	Symbol	ool Test Condition V _{CC} (V)		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level			0.9	V_{CC}	_	7	V _{CC}	_		
			1.1 to 1.3	V _{CC} × 0.8	_		V _{CC} ×0.8	_		
				1.4 to 1.6	V _{CC} × 0.8	-(V _{CC} × 0.8	_	
input voltage	V _{IH}	_		1.65 to 1.95	V _{CC} × 0.8			V _{CC} × 0.8	_	V
				2.3 to 2.7	V _{CC} × 0.8	$\widehat{\mathcal{D}}$		V _{CC} × 0.8	_	
			3.0		Vcc × 0.8	\rightarrow	_	V _{CC} × 0.8	>	
				0.9	7/~	> _	GND	(-//	GND	
				1.1 to 1.3		_ <	V _{CC} × 0.2	Z(A)	V _{CC} × 0.2	
Low-level				1.4 to 1.6		-(V _{CC} ×0.2		$\begin{array}{c} V_{CC} \\ \times \ 0.2 \end{array}$	
input voltage	V _{IL}		-	1.65 to 1.95	١		Vec × 0.2	_	$\begin{array}{c} V_{CC} \\ \times \ 0.2 \end{array}$	V
			2.3 to 2.7			V _{CC} × 0.2	_	$\begin{array}{c} V_{CC} \\ \times \ 0.2 \end{array}$		
				3.0 to 3.6	\/)	V _{CC} × 0.2	_	$\begin{array}{c} V_{CC} \\ \times \ 0.2 \end{array}$	
		$V_{IN} = V_{IL} \\$	I _{OH} = -0.02 mA	0.9	0.75	//-		0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75		_	V _{CC} × 0.75	_	
High-level	V _{OH}	(7)	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75		_	V _{CC} × 0.75	_	V
output voltage		V _{IN} = GND	$I_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
			$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
		$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_		
	>	$V_{IN} = V_{IH}$	$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		IOD = 0.3 mA	1.1 to 1.3	_		V _{CC} × 0.25	_	V _{CC} × 0.25		
Low-level output voltage	V _{OL}	V _{IN} = V _{CC}	I _{OL} = 1.7 mA	1.4 to 1.6		_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
Super rollage	(III = VCC	I _{OL} = 3.0 mA	1.65 to 1.95	_		0.45	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_		0.4	_	0.4	
		× //	I _{OL} = 8.0 mA	3.0 to 3.6			0.4	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μА

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symb	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		C_L = 10 pF, R_L = 1 M Ω	0.9	_	15.0	_	_	_	
			1.1 to 1.3	_	6.0	18.4	1.0	34.2	
			1.4 to 1.6	1	3.2	8.5	1.0	10.0	
			1.65 to 1.95	1	2.6	6.2	1.0	6.7	
			2.3 to 2.7		2.0	3.9	1.0	4.4	
			3.0 to 3.6	-<	1.7	3.1	1.0	3.7	
		C_L = 15 pF, R_L = 1 M Ω	0.9		18.8))	_		
Propagation delay time			1.1 to 1.3	_ (7.0	21.5	1.0	37.2	ns
	t _{PLH}		1.4 to 1.6	((3.5	9.3	1.0	11.2	
Topagation delay time			1.65 to 1.95	4	3.0	6.9	1.0	7.1	113
			2.3 to 2.7		2.3	4.4	1.0	5.0	
			3.0 to3.6	/	1.9	3.4	1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9))	33.0	4	(H)	_	
			1.1 to 1.3	>-	12.0	29.6	1.0	56.0	
			1.4 to 1.6	_	6.0	13.1	1.0	15.9	
			1.65 to 1.95	_	4.5	9.2	1.0	9.6	
			2.3 to 2.7	1(3.2)) 5.7	1.0	6.1	
			3.0 to 3.6		2.5	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 8)	0.9 to 3.6	7/	//8	_	_	_	pF

Note 8: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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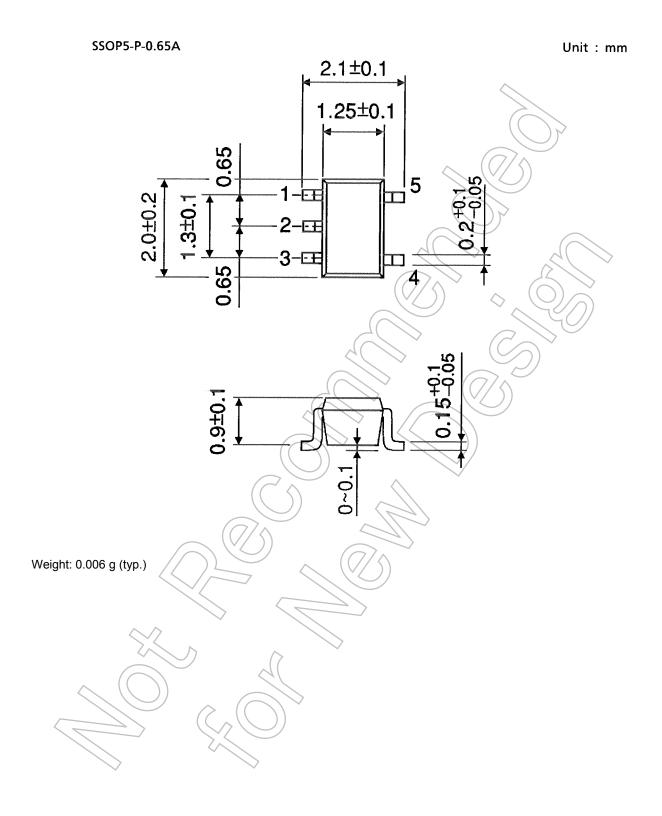


ICC (opr.) = CPD·VCC·fIN + ICC





Package Dimensions



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