TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG32FU

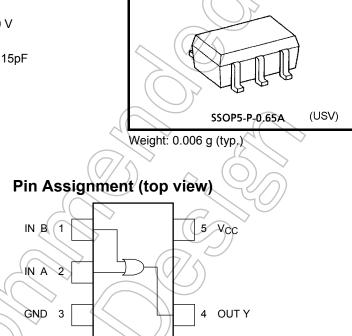
#### 2 Input OR Gate

#### Features

- High output current: ±8 mA (min) at V<sub>CC</sub> = 3.0 V
- High-speed operation: t<sub>pd</sub> = 2.4 ns (typ.)
  - at V<sub>CC</sub> = 3.3 V, 15pF

Product name

- Operating voltage range: V<sub>CC</sub> = 0.9 to 3.6 V
- 5.0-V tolerant inputs
- 3.6-V power down protection output.



### Marking

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit			
Supply voltage	Vec	-0,5 to 4,6	V			
DC input voltage	VIN	-0.5 to 7.0	V			
		-0.5 to 4.6 (Note 1)	V			
DC output voltage	VOUT	-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	v			
Input diode current	IIK	-20	mA			
Output diode current	Іок	-20 (Note 3)	mA			
DC output current	TUOL	±25	mA			
DC V <sub>CC</sub> /ground current	ICC	±50	mA			
Power dissipation	PD	200	mW			
Storage temperature	T <sub>stg</sub>	-65 to 150	°C			

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 
$$V_{CC} = 0V$$

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2005-02

Note 2: High or Low State. I<sub>OUT</sub> abusolute maximum rating must be observed.

# <u>TOSHIBA</u>

#### **IEC Logic Symbol**



T	ruth	Table	e	
	А	В	Y	
	L	L	L	
	L	Н	Н	
	Н	L	Н	$\langle$
	Н	Н	Н	
			$\langle$	
			6	

#### **Operating Rating**

perating Rating			$\langle \bigcirc \rangle$	_
Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	0.9 to 3.6	v	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	$\mathcal{A}( \ \ )$
Output voltage	Vout	0 to 3.6 (Note 4)	v	$\leq >$
	V001	0 to V <sub>CC</sub> (Note 5)		
		±8.0 (Note 6)		40
		±4.0 (Note 7)	$\mathcal{C}$	$>$ $\bigcirc$
Output Current	I <sub>OH</sub> /I <sub>OL</sub>	±3.0 (Note 8)	mA	
Output Gunenit	OHVOL	±1.7 (Note 9)		
		±0.3 (Note 10)	$\mathcal{O}$	
		±0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V	
		A		-

Note 4:  $V_{CC} = 0V$ Note 5: High or Low state. Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ Note 11:  $V_{CC} = 0.9 \text{ V}$ 

Note 12:  $V_{IN}$  = 0.8 to 2.0 V,  $V_{CC}$  = 3.0 V

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test	Test Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Cildiacteristics	Symbol	Circuit	Test	rest condition		Min	Тур.	Max	Min	Max	Unit
					0.9	V <sub>CC</sub>	_ <	X	V <sub>CC</sub>		
					1.1 to 1.3	$\begin{array}{c} V_{CC} \\ \times \ 0.7 \end{array}$	—		V <sub>CC</sub> ×0.7		
High-level input voltage	VIH	_		_	1.4 to 1.6	V <sub>CC</sub> × 0.65	-(0		V <sub>CC</sub> × 0.65	_	V
voltage					1.65 to 1.95	V <sub>CC</sub> × 0.65		$\mathcal{I}$	V <sub>CC</sub> × 0.65		
					2.3 to 2.7	1.7	(-)	> _	1.7	_	
					3.0 to 3.6	2.0			2.0		
					0.9			GND	$\mathcal{A}$	GND	
					1.1 to 1.3			V <sub>CC</sub> × 0.3		∨ <sub>CC</sub> × 0.3	
Low-level input voltage	VIL	_		_	1.4 to 1.6	2_	_	Vcc × 0.35	L)	V <sub>CC</sub> × 0.35	v
voltage				<	1.65 to 1.95	_	-((	V <sub>CC</sub> × 0.35	>_	V <sub>CC</sub> × 0.35	
				$\bigcirc$	2.3 to 2.7	_	$\overline{}$	0.7		0.7	
					3.0 to 3.6	1	VZ)	0.8		0.8	
				I <sub>OH</sub> =-0.02 mA	0.9	0.75	$\sim$		0.75	_	
				I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75	))_	_	V <sub>CC</sub> × 0.75	_	
High-level output voltage	Vон	_	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	V
oulput voltage				I⊖H = −3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45	_	
		$\frown$	(// s)	I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0			2.0	_	
		$\bigcirc$		I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48		_	2.48	_	
		$\sqrt{r}$		I <sub>OL</sub> = 0.02 mA	0.9	_		0.1		0.1	
			>	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
Low-level output	Vol	5	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6		_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
6		$\mathcal{I}$		I <sub>OL</sub> = 3.0 mA	1.65 to 1.95			0.45		0.45	
				I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_		0.4		0.4	
		$\bigcirc$		I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	—	0.4	—	0.4	
Input leakage current		$\mathcal{A}$	V <sub>IN</sub> = 0 to	5.5 V	0 to 3.6	_	_	±0.1		±1.0	μΑ
Power off leakage current	I <sub>OFF</sub>		V <sub>IN</sub> = 0 to V <sub>OUT</sub> = 0 t		0		_	1.0	_	10.0	μΑ
Quiescent supply current	Icc		$V_{IN} = V_{CC}$	or GND	3.6	_	—	1.0		10.0	μΑ

#### AC Electrical Characteristics (unless otherwise specified, input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
			C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	17.0			_	ns
			1.1 to 1.3	_	8.8	18.4	1.0	34.2	
			1.4 to 1.6	_	5.0	8.5	1.0	10.0	
			1.65 to 1.95	_	3.8	6.2	/1.0	6.7	
			2.3 to 2.7	$\geq$	2.7	3.9	1.0	4.4	
	tргн tрнг		3.0 to 3.6	-	2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_((	20.7	> —		_	
			1.1 to 1.3		10.6	21.5	1.0	37.2	
Propagation delay time			1.4 to 1.6		5.9	9.3	1.0	11.2	
Propagation delay time			1.65 to 1.95	X	4.5	6.9	2 1.0	7.1	
			2.3 to 2.7	A	3.0	4.4	1.0	5.0	
			3.0 to 3.6	Ľ	2.4	3.4	(1,0)	3.9	
		C <sub>L</sub> = 30 pF, R <sub>L</sub> = 1 MΩ	0.9		29.6	$\overline{2}$	$\leq$	_	
			1.1 to 1.3	_	14.8	29.6	1.0	56.0	
			1.4 to 1.6	_	8.0	43.1	1.0	15.9	
			1.65 to 1.95	1	6.0	9.2	1.0	9.6	
			2.3 to 2.7		3.9	5.7	1.0	6.1	
			3.0 to 3.6	-	3.0	4.4	1.0	4.8	
Input capacitance	C <sub>IN</sub>	((-))	3.6	$\langle \rangle$	3	_		_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9 to 3.6	_	6	_	—	—	pF

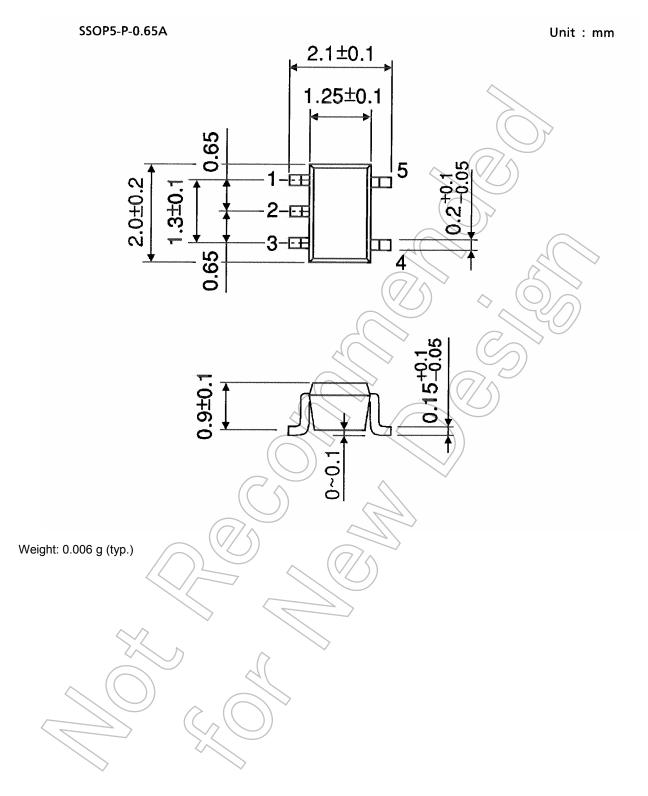
Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

## **TOSHIBA**

#### Package Dimensions



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