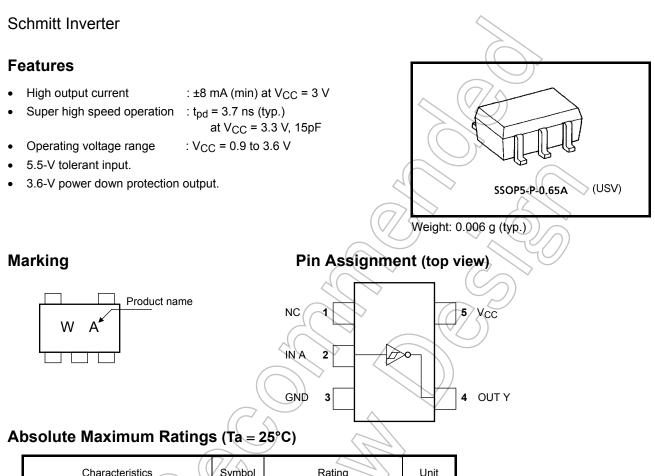
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG14FU



Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	-0.5 to 4.6	V	
DC input voltage	V _{IN}	-0.5 to 7.0	V	
DC output voltage	V _{OUT}	-0.5 to 4.6 (Note 1)	V	
		-0.5 to V _{CC} + 0.5 (Note 2)	•	
Input diode current	IIK 🔿	-20	mA	
Output diode current	IOK	-20 (Note 3)	mA	
DC output current	HOUT	±25	mA	
DC V _{CC} /ground current		±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	−65 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

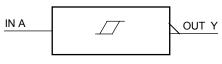
Note 3: V_{OUT} < GND

Start of commercial production 2005-08

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

TOSHIBA

IEC Logic Symbol



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							\sum
perating	Ranges						9
	Characteristics	Symbol	R	ating	\swarrow	Unit	
Supply volta	age	V _{CC}	0.9	to 3.6	()	V	
Input voltag		VIN	0 t	0 5.5	\sim	ע ל ע	
					(Note 4)		
Output volta	age	Vout	0 to	Vcc	(Note 5)	- V	\sum
		1		$+ \alpha + \lambda$	(Note 6)	(\supset
			Ê	4.0	(Note 7)		$\mathcal{C}(\mathcal{A})$
0.1.1.0			<u> </u>	3.0 ((Note 8)	C	
Output Curr	Output Current	I _{OH} /I _{OL}	$\langle \overline{\langle } \rangle$	1.7 ((Note 9)	(mA)	
			*	0.3 (N	Note 10)		
		C	<u>_()</u> +	0.02 (N	lote 11)	()	
Operating te	emperature	T _{opr}	_40	to 85	$\overline{)}$	°C	
Note 4: V	$V_{\rm CC} = 0 V$		$\langle \rangle$	\mathbb{Z}			-
Note 5: H	ligh or Low state.	$(\bigcirc$)		\leq		
Note 6: V	$V_{\rm CC} = 3.0 \text{ to } 3.6 \text{ V}$	\mathcal{C}	r ($\langle \rangle$	~		
	$V_{\rm CC} = 2.3 \text{ to } 2.7 \text{ V}$		5	$ \leq $			
	/ _{CC} = 1.65 to 1.95 V	$\overline{\overline{\partial A}}$		\sim			
	/ _{CC} = 1.4 to 1.6 V	\mathcal{V}		\searrow			
	/ _{CC} = 1.1 to 1.3 V						
Note 11: V	/ _{CC} = 0.9 V						
	\sim						
	~ 2		\geq				
		(7)	\checkmark				
~	()	41					
		(\bigcirc)					
	\supset	\searrow					

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Tost	Toot Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit	
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit			
					0.9	_		0.73	_	0.80	
					1.1	_	_	0.86		0.93	
	Positive				1.4	_	_	1.07)/	1.12	
	threshold voltage	VP		_	1.65	_	-6	1.23	_	1.25	
					2.3	_	7	1.66	_	1.68	
Threshold					3.0	- (2.14	_	2.15	v
voltage					0.9	0.18	\supset	_	0.07	_	v
					1.1	0.26	-	_	0.18		
	Negative threshold	Max			1.4	0.36	~_		0.31	\geq	
	voltage	V _N		_	1.65	0.45	—	-6	0.41	> _	
					2.3	0.69			0.64) —	
					3.0	0.96	_	$ \geq$	0.91	_	
					0.9	0.20	-((0.38	0.15	0.53	
					1,1	0.25		0.41	0.21	0.53	
		Max			1.4	0.35	(\mathcal{A})	0.48	0.34	0.57	V
Hysteresis volta	age	V _H		$-\langle \langle \rangle$	1.65	0.42	\mathbb{N}	0.56	0.40	0.60	v
					2.3	0.60	\backslash	0.74	0.61	0.76	
				$(\bigcirc)^{\vee}$	3.0	0.79	//	0.93	0.80	0.94	
				I _{OH} =-0.02 mA	0.9	0.75		_	0.75	_	
				I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
	High level	VOH		I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
		\sum		I _{OH} = −3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
		\sim		I _{OH} = -4.0 mA	2.3 to 2.7	2.0			2.0		
Output voltage				I _{OH} = -8.0 mA	-3.0 to 3.6	2.48	_		2.48		V
	$\langle \rangle$			l _{OL} = 0.02 mA	0.9		_	0.1	—	0.1	
		D	4	l _{OL} = 0.3 mA	1.1 to 1.3		—	V _{CC} × 0.25	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
	Low level	VOL	VIN=VIH	lo⊾⇒1.7 mA	1.4 to 1.6		_	V _{CC} × 0.25	_	V _{CC} × 0.25	
	•		1 (I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
				I _{OL} = 4.0 mA	2.3 to 2.7	_		0.4		0.4	
\rightarrow			\searrow	I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage c	urrent	I _{IN}	$V_{IN} = 0 \text{ to}$	5.5 V	0 to 3.6	_	_	±0.1	_	±1.0	μA
Power off leaka	ge current	I _{OFF}	V _{IN} = 0 to V _{OUT} = 0		0	_		1.0		10.0	μΑ
Quiescent supp	ly current	Icc	V _{IN} = V _C		3.6			1.0		10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$\begin{array}{l} C_L = 10 \ pF, \\ R_L = 1 \ M\Omega \end{array}$	0.9	_	27.3		—	_	
			1.1 to 1.3	_	13.0	22.6	1.0	35.9	
			1.4 to 1.6	_	7.5	10.5	1.0	11.3	
			1.65 to 1.95		6.0	7.8	1.0	8.2	
			2.3 to 2.7		4.3	5.4	1.0	5.8	
			3.0 to 3.6	$- \diamond$	3.5	4.4	1.0	4.6	ns
	^t рLН tрнL	C _L = 15 pF, R _L = 1 MΩ	0.9		29.5)	—		
			1.1 to 1.3		14.3	25.1	1.0	41.8	
Propagation delay time			1.4 to 1.6)0. 8.	11.5	1.0	12.6	
Propagation delay time			1.65 to 1.95	T,	6.3	8.4	1.0	8,7	
			2.3 to 2.7		4.6	5.7	2 1.0	6.1	
			3.0 to 3.6	//	3.7	4.6)1.0	5.0	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9)	40.5	Ĺ	~{})	/ _	
			1.1 to 1.3	\geq	19.6	35.7	1.0	58.1	
			1.4 to 1.6		10.7	15.8	1.0	17.6	
			1.65 to 1.95		7.8	10.7	1.0	11.7	
			2.3 to 2.7		5.4))6.9	1.0	8.1	
			3.0 to 3.6		4.3	5.2	1.0	6.1	
Input capacitance	C _{IN}		3.6	$\overline{\langle}$	3	_	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 12)	0.9 to 3.6		//7	—	—		pF

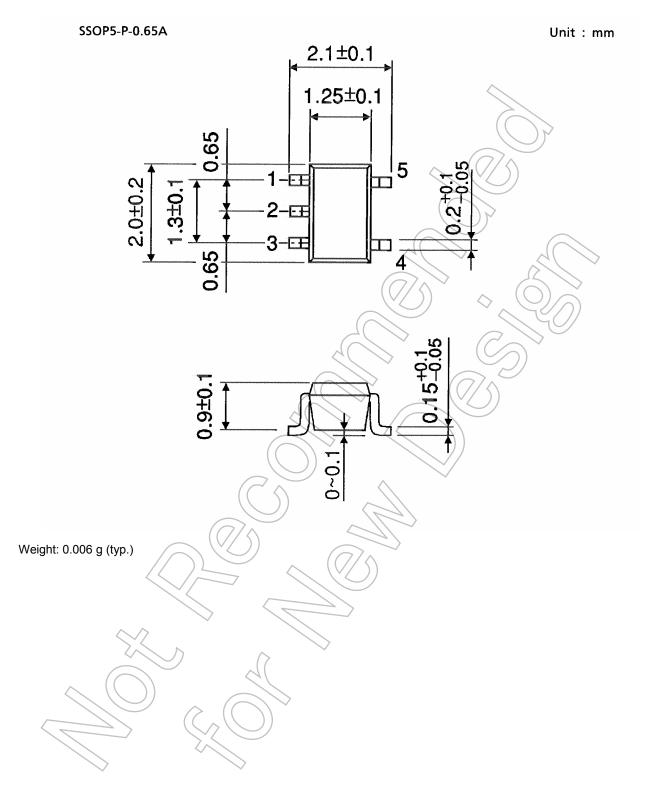
Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC}(opr) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions



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