TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SA34F,TC7SA34FU

Non-Inverter

Features

Low voltage operation : V_{CC} = 1.8 to 3.6 V

High speed operation : t_{pd} = 2.8 ns (max) (V_{CC} = 3.0 to 3.6 V)

: t_{pd} = 3.7 ns (max) (V_{CC} = 2.3 to 2.7 V)

 $: t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V)}$

High output current $: I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

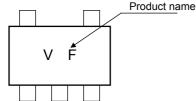
 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min)} (V_{CC} = 2.3 \text{ V})$

 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min)} (V_{CC} = 1.8 \text{ V})$

3.6-V tolerant input.

3.6-V power down protection output.

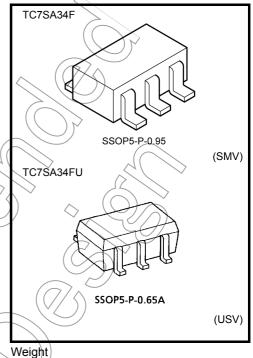
Marking





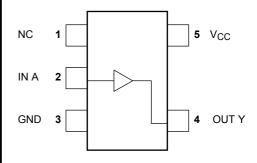


Characteristics	Symbol	Rating	
Supply voltage	Vcc	-0.5 to 4.6) V
DC input voltage	\/\v_IN	-0.5 to 4.6	V
DC output voltage	Vout	-0.5 to 4.6 (Note 1) -0.5 to V _{CC} +0.5 (Note 2)	V
Input diode current) lik	-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	lout	±50	mA
Power dissipation	₽ _D (200	mW
DC V _{CC} /ground current	lçc	±100	mA
Storage temperature range	T _{stg}	-65 to 150	°C



SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A: 0.006 g (typ.)

Pin Assignment (top view)



Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the Note: significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

IEC Logic Symbol

Truth Table



Α	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	1.8 to 3.6
	VCC	1.2 to 3.6 (Note 4)
Input voltage	V _{IN}	-0.3 to 3.6 V
Output voltage	V _{OUT}	0 to 3.6 (Note 5)
Output voltage		0 to Vec (Note 6)
	I _{OH} /I _{OL}	±24 (Note 7)
Output current		±18 (Note 8) mA
		±6 (Note 9)
Operating temperature range	T _{opr}	-40 to 85
Input rise and fall time	dt/dv	0 to 10 (Note 10) ns/V

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or low state

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$

Note 10: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C, 2.7 V < V_{CC} \le 3.6 V)

Charac	cteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
	High level	V _{IH}				2.0		.,
Input voltage	Low level	V _{IL}		_	2.7 to 3.6	_	0.8	V
			I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_		
Hiç Output voltage	High level	V _{OH}	V _{IN} = V _{IH}	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
	, and the second	GII		$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V
	Low level	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 100 μA	2.7 to 3.6		0.2	
				I _{OL} 12 mA	2.7	\mathcal{H}	0.4	
	Low level			10L = 18 mA	3.0		0.4	
				loL = 24 mA	3.0(0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 3.6 V		2.7 to 3.6	SH)	±5.0	μΑ
Power off leakage	current	l _{OFF}	V _{IN} , V _{OUT} = 0 to 3.6 V		0)	10.0	μΑ
Quiescent supply current		l lcc	V _{IN} = V _{CC} or GN	D (2.7 to 3.6	_	20.0	
			$V_{QC} \le (V_{IN}, V_{QUT}) \le 3.6 \text{ V}$		2.7 to 3.6		±20.0	μΑ
Increase in I _{CC} pe	r input	Δl _{CC}	V _{IH} =V _{CC} =0.6 V		2.7 to 3.6		750	

DC Characteristics (Ta = -40 to 85°C, 2.3 $V \le V_{CC} \le 2.7 \text{ V}$)

Characteristics		Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	High level	THE STATE OF THE S		=7/	2.3 to 2.7	1.6	_	V
input voltage	Low level	// \YıL			2.3 to 2.7	_	0.7	V
				I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	_	
	High level	VoH	VIN = AIH	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	
		1		I _{OH} = -12 mA	2.3	1.8	_	
Output voltage	\checkmark			I _{OH} = -18 mA	2.3	1.7	_	V
2		\wedge		I _{OL} = 100 μA	2.3 to 2.7	_	0.2	
	Low level	(NOF	$V_{IN} = V_{IL}$	I _{OL} = 12 mA	2.3	_	0.4	
				I _{OL} = 18 mA	2.3	_	0.6	
Input leakage curre	ent	(liju)	V _{IN} = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μА
Power off leakage	current	OFF	V_{IN} , $V_{OUT} = 0$ to 3	3.6 V	0	_	10.0	μА
		\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	V _{IN} = V _{CC} or GND		2.3 to 2.7	_	20.0	^
Quiescent supply c	urrent	^{>} lcc	$V_{CC} \le (V_{IN}, V_{OUT})$	r) ≤ 3.6 V	2.3 to 2.7		±20.0	μА

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DC Characteristics (Ta = -40 to 85°C, 1.8 V \leq V_{CC} < 2.3 V)

Charac	cteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	High level	V _{IH}	V _{IH} —		1.8 to 2.3	V _{CC} × 0.7		V
Input voltage	Low level	V _{IL}	-	_		_	V _{CC} × 0.2	V
	High level	V _{OH}	V _{IN} = V _{IH}	I _{OH} = -100 μA	1.8	Vcc 0.2	_	
Output voltage				$I_{OH} = -6 \text{ mA}$	7/1,8	1.4	_	V
	Low level	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 100 µA	1.8	_	0.2	
	Low level			I _{OL} = 6 mA	1.8		0.3	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 3.6 V		1.8		±5.0	μΑ
Power off leakage	current	I _{OFF} V _{IN} , V _{OUT} = 0 to 3.6 V		3.6 V	0		10.0	μΑ
Quiescent supply current		I _{CC}	V _{IN} = V _{CC} or GND		1.8	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	20.0	μΑ
		100	$V_{CC} \leq (V_{IN}, V_{OUT}) \leq 3.6 \text{Å}$		1.8)	±20.0	μΛ

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	Vcc (V)	Min	Max	Unit
	+		1.8	1.0	7.4	
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.5 ± 0.2	0.8	3.7	ns
	t _{pHL}		3.3 ± 0.3	0.6	2.8	

For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.

Capacitive Characteristics (Ta = 25°C)

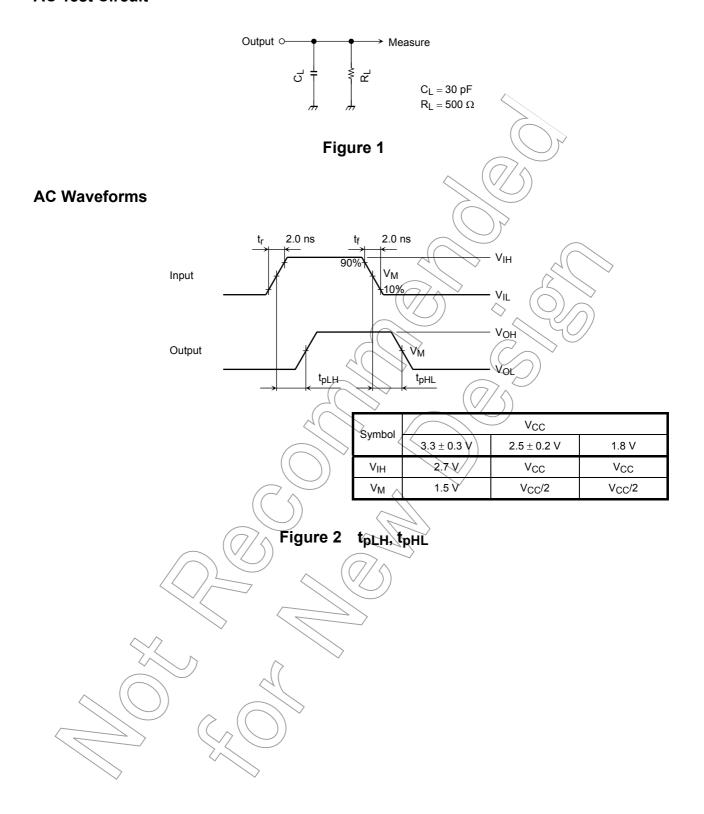
Characteristics	Symbol	Test Condition	Ī	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN} (1.8, 2.5, 3.3	4	pF
Power dissipation capacitance	C _{PD}	fin = 10 MHz ((Note 11)	1.8, 2.5, 3.3	12	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

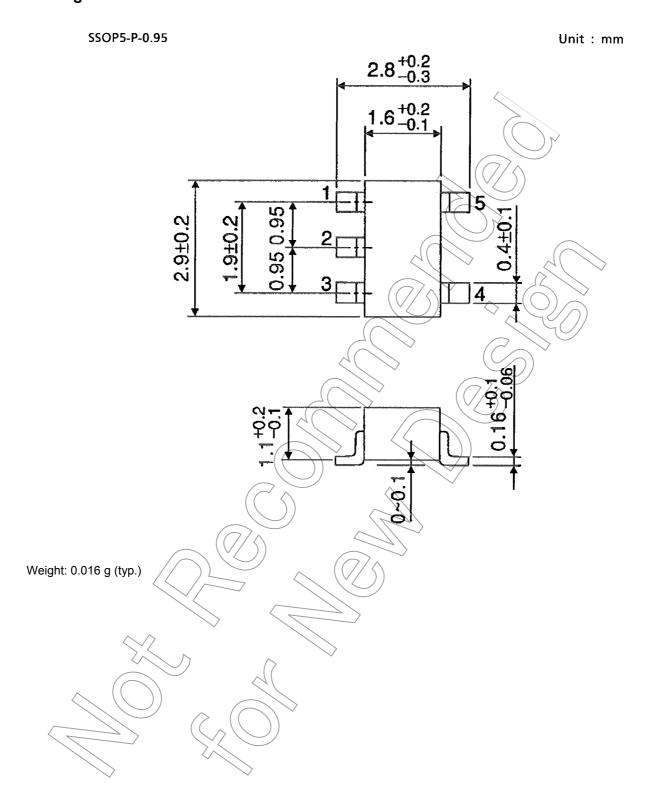
Average operating current can be obtained by the equation.

ICC (opr.) = CPD·VCC·fIN + ICC

AC Test Circuit

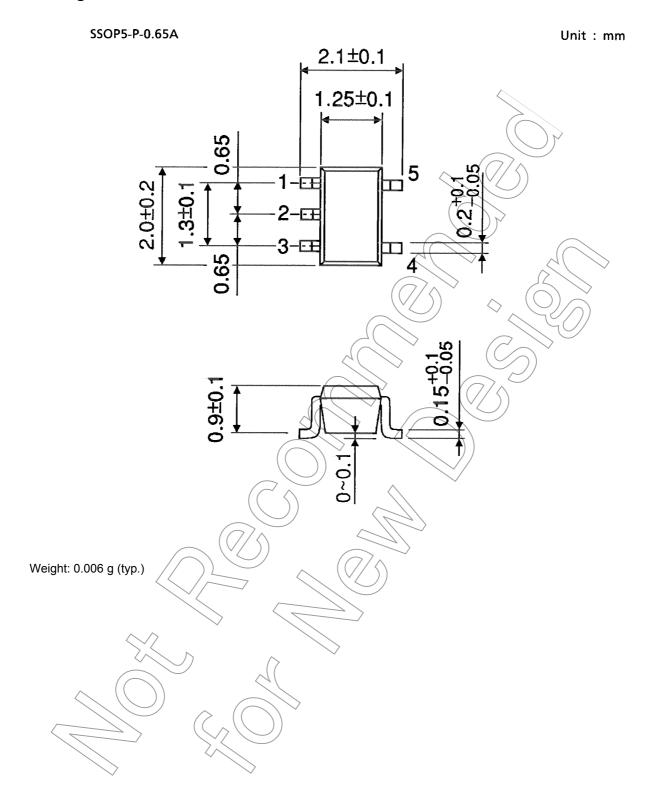


Package Dimensions





Package Dimensions



TC7SA34F/FU

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