TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA34FU

Dual Non-Inverter

Features

- Operating voltage range: V_{CC} = 1.8 to 3.6 V
- High-speed operation: t_{pd} = 3.5 ns (max) at V_{CC} = 3.0 to 3.6 V

 t_{pd} = 4.2 ns (max) at V_{CC} = 2.3 to 2.7 V

 t_{pd} = 8.4 ns (max) at V_{CC} = 1.8 V

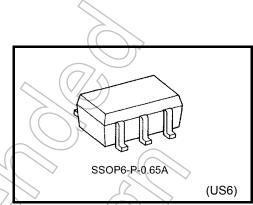
• High-level output current:

 I_{OH}/I_{OL} = ±24 mA (min) at V_{CC} = 3.0 V

 I_{OH}/I_{OL} = ±18 mA (min) at V_{CC} = 2.3 V

 $I_{OH}/I_{OL} = \pm 6$ mA (min) at $V_{CC} = 1.8$ V

- 3.6-V tolerant inputs.
- 3.6-V power down protection outputs.

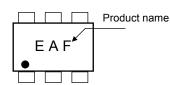


Weight: 0.0068 g (typ.)

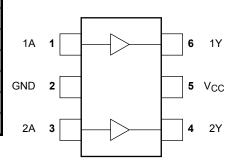
Absolute Maximum Ratings (Ta = 25°C)

			/
Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	٧
DC input voltage	V _{IN} (-0.5 to 4.6	V
DC output voltage	Your	-0.5 to 4.6 (Note 1)	1)
DC output voitage		-0.5 to V _{CC} + 0.5 (Note 2)	>
Input diode current	¥	-50	mA
Output diode current	lok	-50 (Note 3)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	−65 to 150	°C
		. '\	

Marking



Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Start of commercial production 2001-09

IEC Logic Symbol

Truth Table



А	Y
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	V _{CC}	1.8 to 3.6
ouppiy voitage	VCC .	1.2 to 3.6 (Note 4)
Input voltage	V _{IN}	-0.3 to 3.6 V
Output voltage	V _{OUT}	0 to 3.6 (Note 5)
Output voltage	VOU1	0 to V _{CC} (Note 6)
		±24 (Note 7)
Output Current	I _{OH} /I _{OL}	±18 (Note 8) mA
		±6 (Note 9)
Operating temperature	T _{opr}	-40 to 85 °C
Input rise and fall time	d _t /d _v	0 to 10 (Note 10) ns/V

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.8 \text{ V}$

Note 10: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C, 2.7 V < V_{CC}≤ 3.6 V)

Chara	acteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Innut valtage	High level	V _{IH}		_	2.7 to 3.6	2.0	_	V
Input voltage	Low level	V _{IL}		_	2.7 to 3.6	_	0.8	V
			I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_		
Output Voltage Low level	V _{OH}	V _{IN} = V _{IH}	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_		
		5		$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	V
				$I_{OH} = -24 \text{ mA}$	3.0	2.2		
				$I_{OL} = 100 \mu A$	2.7 to 3.6		0.2	
	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 12 mA	2.7	4	0.4		
	Low level	VOL	VIN - VIL	$I_{OL} = 18 \text{ mA}$	3.0		0.4	
				$I_{OL} = 24 \text{ mA}$	3.0(()	0.55	
Input Leakage Cu	ırrent	I _{IN}	V _{IN} = 0 to 3.6 V		2.7 to 3.6	4	±5.0	μΑ
Power-off Leakag	e Current	loff	V _{IN} , V _{OUT} = 0	to 3.6 V	0)	10.0	μΑ
Quiescent Supply Current		loo	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	20.0	
Quiescent Supply	Current	lcc	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		2.7 to 3.6	_	±20.0	μΑ
Increase in I _{CC} pe	er Input	Δlcc	V _{IH} = V _{CC} - 0.6 V		2.7 to 3.6	_	750	

DC Characteristics (Ta = -40 to 85°C, 2.3 V≤ V_{CC}≤ 2.7 V)

Charac	eteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High level		7/ _{(VIH}		7	2.3 to 2.7	1.6	_	V
Input voltage	Low level	₩ _{IL}	(7)		2.3 to 2.7	_	0.7	V
		7		I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2		
	High level	VoH	$V_{IN} = V_{IH}$	I _{OH} = -6 mA	2.3	2.0	_	V
	\frac{1}{2}			$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
Output Voltage	Output Voltage			I _{OH} = -18 mA	2.3	1.7	_	
		4		$I_{OL} = 100 \mu A$	2.3 to 2.7		0.2	V
	Low level	Vol	$V_{IN} = V_{IL}$	I _{OL} = 12 mA	2.3	_	0.4	V
			I _{OL} = 18 mA	I _{OL} = 18 mA	2.3	_	0.6	
Input Leakage Curr	rent	JIN	$V_{IN} = 0 \text{ to } 3.6 \text{ V}$		2.3 to 2.7		±5.0	μΑ
Power-off Leakage	Current	loff	V_{IN} , $V_{OUT} = 0$ to	3.6 V	0	_	10.0	μΑ
0.:	Current		V _{IN} = V _{CC} or GNI)	2.3 to 2.7		20.0	
Quiescent Supply (Junelil	Icc	V _{CC} ≤ (V _{IN} , V _{OUT})≤ 3.6 V	2.3 to 2.7	_	±20.0	μА

DC Characteristics (Ta = -40 to 85° C, $1.8 \text{ V} \le \text{V}_{CC} < 2.3 \text{ V}$)

Chara	cteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	High level	V _{IH}	_		1.8 to 2.3	V _{CC} × 0.7		V
input voitage	Low level	VIL	IL —		1.8 to 2.3	_	V _{CC} × 0.2	V
	High level	V _{OH} V _{IN} = V _{IH}		I _{OH} = -100 μA	1.8	VCC 0.2	_	
Output Voltage				I _{OH} = -6 mA	7/1,8	1.4	_	V
	Low level	Voi	$V_{IN} = V_{IL}$	$I_{OL} = 100 \mu A$	1.8	_	0.2	
	Low level	V _{OL}		I _{OL} = 6 mA	1.8		0.3	
Input Leakage Cui	rrent	I _{IN}	V _{IN} = 0 to 3.6 V		1.8	1	±5.0	μΑ
Power-off Leakage	e Current	l _{OFF}	V _{IN} , V _{OUT} = 0 to 3.6 V		0	A ()	10.0	μА
Quiescent Supply Current		loo	V _{IN} = V _{CC} or GND		1.8	173	20.0	μА
Quiescent Supply	Ourient	Icc	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		1.8)}-	±20.0	μΛ

AC Characteristics (Ta = -40 to 85°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	+		1.8	1.0	8.4	
Propagation delay time	t _{pLH}	(Figure 1 and 2)	2.5 ± 0.2	8.0	4.2	ns
	t _{pHL}		3.3 ± 0.3	0.6	3.5	

For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Cumbal	mbol Test Condition			T. m	Unit
Characteristics	Symbol			V _{CC} (V)	Тур.	Offic
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	1.8	0.25	
Quiet Output Maximum Dynamic V _{OL}	V_{OLP}	V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 11)	2.5	0.6	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	3.3	0.8	
	V _{OLV}	V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 11)	1,8	-0.25	
Quiet Output Minimum Dynamic V _{OL}		V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 11)	2.5	-0.6	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	3.3	-0.8	
		V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 11)	1.8	1.5	
Quiet Output Minimum Dynamic VOH	V_{OLP}	V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 11)	2.5	1.9	V
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	3.3	2.2	

Note 11: Characteristics guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	y _{cc} (V)	Тур.	Unit
Input Capacitance	C _{IN}		1.8, 2.5, 3.3	4	pF
Power Dissipation Capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 12) 1.8, 2.5, 3.3	12	pF

Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

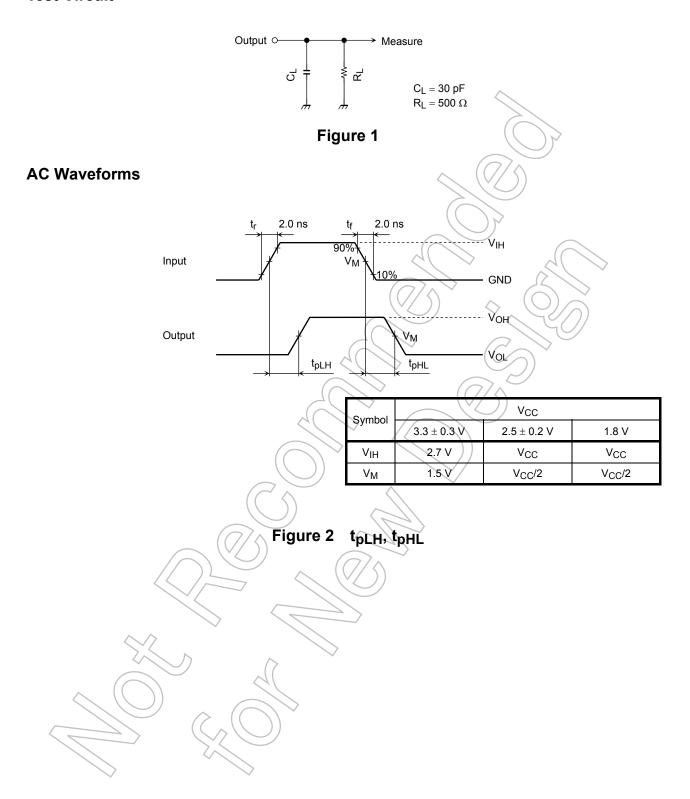
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Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

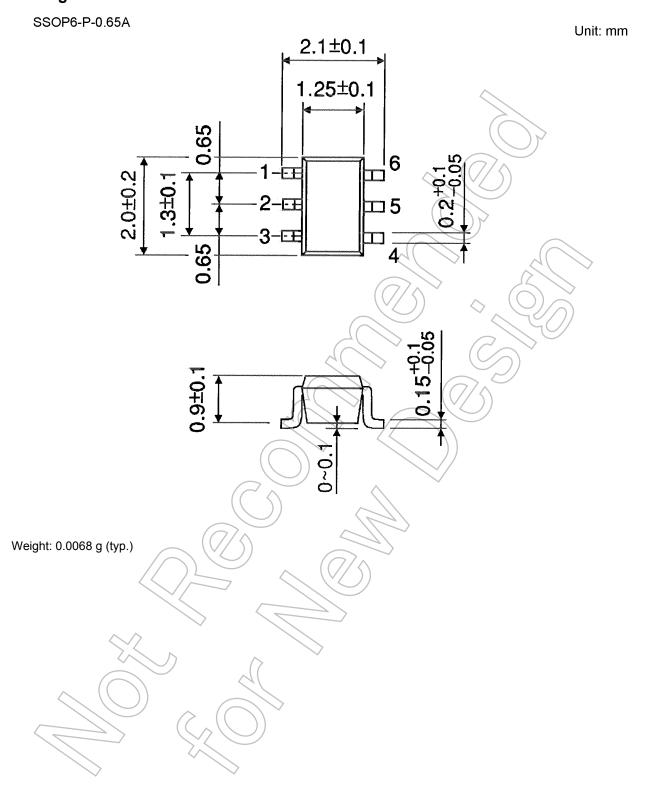
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Test Circuit





Package Dimensions



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