

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VCX32FT, TC74VCX32FK

Low-Voltage Quad 2-Input OR Gate with 3.6-V Tolerant Inputs and Outputs

The TC74VCX32FT/FK is a high-performance CMOS 2-input OR gate which is guaranteed to operate from 1.2 V to 3.6 V.

Designed for use in 1.5 V, 1.8 V, 2.5 V or 3.3 V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

It is also designed with overvoltage tolerant inputs and outputs up to 3.6 V. All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation: VCC = 1.2 to 3.6 V
- High-speed operation: tpd = 2.8 ns (max) (VCC = 3.0 to 3.6 V)

: tpd = 3.7 ns (max) (VCC = 2.3 to 2.7 V)

: tpd = 7.4 ns (max) (VCC = 1.65 to 1.95 V)

: tpd = 14.8 ns (max) (VCC = 1.4 to 1.6 V)

 $: t_{pd} = 37.0 \text{ ns (max) (VCC} = 1.2 \text{ V)}$ 

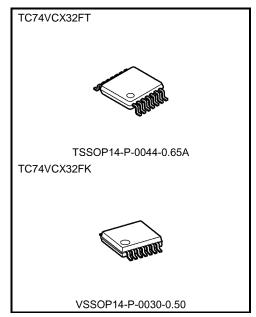
Output current : IOH/IOL = ±24 mA (min) (VCC = 3.0 V)

 $: IOH/IOL = \pm 18 \text{ mA (min) (VCC} = 2.3 \text{ V)}$ 

 $: IOH/IOL = \pm 6 \text{ mA (min) (VCC} = 1.65 \text{ V)}$ 

:  $IOH/IOL = \pm 2 \text{ mA (min) (VCC} = 1.4 \text{ V)}$ 

- Latch-up performance: -300 mA
- ESD performance: Machine model  $\geq \pm 200 \text{ V}$ Human body model  $\geq \pm 2000 \text{ V}$
- Package: TSSOP and VSSOP (US)
- 3.6 V tolerant function and power-down protection provided on all inputs and outputs



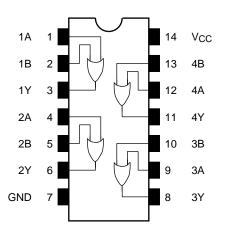
Weight

TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

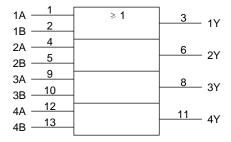
Start of commercial production 1998-11



### Pin Assignment (top view)



#### **IEC Logic Level**



#### **Truth Table**

Inp	uts	Outputs
А	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

#### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	Vcc	-0.5 to 4.6	V	
DC input voltage	VIN	-0.5 to 4.6	V	
		-0.5 to 4.6 (Note 2)	٧	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)		
Input diode current	lık	-50	mA	
Output diode current	lok	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	180	mW	
DC Vcc/ground current	ICC/IGND	±100		
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: VCC = 0 V

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC



### **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	Vcc	1.2 to 3.6	V	
Input voltage	VIN	-0.3 to 3.6	٧	
Output voltage	Vour	0 to 3.6 (Note 2)	V	
Output voltage	Vout	0 to VCC (Note 3)	V	
		±24 (Note 4)	Α	
Output ourrent	lov/lov	±18 (Note 5)		
Output current	IOH/IOL	±6 (Note 6)	mA	
		±6 (Note 7)		
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2: VCC = 0 V

Note 3: High or low state

Note 4: VCC = 3.0 to 3.6 V

Note 5: VCC = 2.3 to 2.7 V

Note 6: VCC = 1.65 to 1.95 V

Note 7: VCC = 1.4 to 1.6 V

Note 8: VIN = 0.8 to 2.0 V, VCC = 3.0 V

#### **Electrical Characteristics**

### DC Characteristics (Ta = -40 to $85^{\circ}$ C, $2.7 \text{ V} < \text{V}_{CC} \le 3.6 \text{ V}$ )

Characteristics		Symbol	Test Co	ondition		Min	Max	Unit
		,			Vcc (V)			
Input voltage	H-level	VIH	_	_	2.7 to 3.6	2.0	_	V
Input voltage	L-level	VIL	_	_	2.7 to 3.6	_	0.8	v
				I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>C</sub> C - 0.2	_	
Output voltage	H-level	Voн	VIN = VIH or VIL	I <sub>OH</sub> = -12 mA	2.7	2.2	_	
		<b></b>		I <sub>OH</sub> = -18 mA	3.0	2.4	_	V
				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
	L-level V		V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	2.7 to 3.6	_	0.2	
		V/		I <sub>OL</sub> = 12 mA	2.7	_	0.4	
	L-ievei	VoL		I <sub>OL</sub> = 18 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		2.7 to 3.6	_	±5.0	μА
Power-off leakage current		loff	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 \	/	0	_	10.0	μА
Quiescent supply current		laa	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7 to 3.6	_	20.0	
		Icc	V <sub>CC</sub> ≤ V <sub>IN</sub> ≤ 3.6 V		2.7 to 3.6	_	±20.0	μΑ
Quiescent supply current		Δlcc	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V (pe	r input)	2.7 to 3.6	_	750	μА



# DC Characteristics (Ta = -40 to $85^{\circ}$ C, 2.3 V $\leq$ V<sub>CC</sub> $\leq$ 2.7 V)

Characterist	Characteristics		Tost C		Min	Max	Unit	
Characteristics		Symbol	rest C	Test Condition			IVIIII	Offic
Input voltage	H-level	VIH	-	_	2.3 to 2.7	1.6	_	V
Input voltage	L-level	VIL	_	_	2.3 to 2.7		0.7	V
H-level			ΙΟΗ = -100 μΑ	2.3 to 2.7	V <sub>C</sub> C - 0.2	_		
	H-level	Voн	VIN = VIH or VIL	IOH = -6 mA	2.3	2.0	_	V
				IOH = -12 mA	2.3	1.8	_	
Output voltage				IOH = -18 mA	2.3	1.7	_	
			VIN = VIL	$IOL = 100 \mu A$	2.3 to 2.7	_	0.2	
	L-level	Vol		I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				I <sub>OL</sub> = 18 mA	2.3	_	0.6	
Input leakage current		liN	VIN = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μА
Power-off leakage cur	ower-off leakage current IOFF VIN, VOUT = 0 to 3.6 V		0	_	10.0	μА		
Quiaccent cumply our	Outros		V <sub>IN</sub> = V <sub>CC</sub> or GND		2.3 to 2.7	_	20.0	
Quiescent supply curre	HIIL	Icc	V <sub>CC</sub> ≤ V <sub>IN</sub> ≤ 3.6 V		2.3 to 2.7	_	±20.0	μА

### DC Characteristics (Ta = -40 to $85^{\circ}$ C, 1.65 V $\leq$ V<sub>CC</sub> < 2.3 V)

Characteristics		Symbol	Test Co	ondition		Min	Max	Unit
		-,			Vcc (V)			<b>5</b> 1
Input voltage	H-level	VIH	_	_	1.65 to 2.3	0.65 × V <sub>CC</sub>		V
input voltage	L-level	VIL	_	_	1.65 to 2.3	_	0.2 × VCC	V
H-level Output voltage	H-level	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.65 to 2.3	V <sub>C</sub> C - 0.2	-	V
				IOH = -6  mA	1.65	1.25	_	
	L-level	.,	V <sub>IN</sub> = V <sub>IL</sub>	IOL = 100 μA	1.65 to 2.3	_	0.2	
	L-ievei	VoL		I <sub>OL</sub> = 6 mA	1.65	_	0.3	
Input leakage current	ent I <sub>IN</sub> V <sub>IN</sub> = 0 to 3.6 V		1.65 to 2.3	_	±5.0	μΑ		
Power-off leakage curr	ent	IOFF	VIN, VOUT = 0 to 3.6 V		0	_	10.0	μΑ
O de constante de constante		laa	VIN = VCC or GND		1.65 to 2.3	_	20.0	^
Quiescent supply curre	er it	Icc	VCC ≤ V <sub>IN</sub> ≤ 3.6 V		1.65 to 2.3	_	±20.0	μА



# DC Characteristics (Ta = -40 to $85^{\circ}$ C, $1.4 \text{ V} \leq \text{V}_{CC} < 1.65 \text{ V}$ )

Characteristics		Symbol	Test Condition V <sub>C</sub>		V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	H-level	VIH	_	-	1.4 to 1.65	0.65 × VCC		V
Input voltage	L-level	VIL	_	_			0.05 × VCC	V
H-level Output voltage	H-level	I-level V <sub>OH</sub>	VIN = VIH or VIL	ΙΟΗ = -100 μΑ	1.4 to 1.65	Vcc - 0.2		V
				I <sub>OH</sub> = -2 mA	1.4	1.05	_	
	L-level	.,	Maria Mar	I <sub>OL</sub> = 100 μA	1.4 to 1.65		0.05	
	L-ievei	Vol	VIN = VIL	I <sub>OL</sub> = 2 mA	1.4		0.35	
Input leakage current	akage current I <sub>IN</sub> V <sub>IN</sub> = 0 to 3.6 V		1.4 to 1.65		±5.0	μΑ		
Power-off leakage curr	ent	IOFF	VIN, VOUT = 0 to 3.6 V		0		10.0	μΑ
Ovice continued to surrent		Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.4 to 1.65		20.0	^
Quiescent supply curre	Quiescent supply current		V <sub>CC</sub> ≤ V <sub>IN</sub> ≤ 3.6 V		1.4 to 1.65	_	±20.0	μА

# DC Characteristics (Ta = -40 to $85^{\circ}$ C, $1.2 \text{ V} \leq \text{V}_{CC} < 1.4 \text{ V}$ )

Characteristics		Symbol	Test Co	ndition	\/ (\/)	Min	Max	Unit
	1				V <sub>CC</sub> (V)			
Input voltage	H-level	VIH	_	-	1.2 to 1.4	0.8 × VCC	_	V
Input voltage	L-level	VIL	_	_		_	0.05 × V <sub>CC</sub>	V
Output voltage	H-level	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>OH</sub> = -100 μA		1.2	V <sub>C</sub> C - 0.1	_	V
	L-level	VoL	V <sub>IN</sub> = V <sub>IL</sub>	$I_{OL} = 100 \mu A$	1.2		0.05	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.2		±5.0	μΑ
Power-off leakage curr	ower-off leakage current IOFF V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		0	_	10.0	μΑ		
Quioscopt supply current		Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.2	_	20.0	
Quiescent supply curre	Quiescent supply current		V <sub>CC</sub> ≤ V <sub>IN</sub> ≤ 3.6 V		1.2	_	±20.0	μΑ



### AC Characteristics (Ta = -40 to $85^{\circ}$ C, input: $t_r = t_f = 2.0$ ns) (Note 1)

Characteristics	Symbol Test Condition			Min	Max	Unit	
Gridiacteristics	Symbol	163	rest condition		IVIIII	IVIAX	Offic
			CL 45 % F DL 2160	1.2	3.0	37.0	
Propagation delay time			$CL = 15 pF, RL = 2 k\Omega$	$1.5 \pm 0.1$	2.0	14.8	
	t <sub>pLH</sub> t <sub>pHL</sub>	Figure 1, Figure 2		1.8 ± 0.15	1.5	7.4	ns
	PITE		$CL = 30 \text{ pF}, RL = 500 \Omega$	$2.5\pm0.2$	0.8	3.7	
				$3.3 \pm 0.3$	0.6	2.8	
			CL = 15 pF, RL = 2 kΩ	1.2		1.5	
				$1.5\pm0.1$		1.5	
Output to output skew	t <sub>osLH</sub> t <sub>osHL</sub>	(Note 2)	CL = 30 pF, RL = 500 Ω	$1.8 \pm 0.15$		0.5	ns
	103I IL			$2.5 \pm 0.2$		0.5	
				$3.3 \pm 0.3$		0.5	

Note 1: For CL = 50 pF, add approximately 300 ps to the AC maximum specification.

Note 2: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition			Тур.	Unit
Characteristics	Cymbol	r oot Gernalden		Vcc (V)	ιyp.	Offic
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	0.25	
Quiet output maximum dynamic V <sub>OL</sub>	VOLP	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	3.3	0.8	
	Volv	$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	-0.25	
Quiet output minimum dynamic VoL		$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	-0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	3.3	-0.8	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	1.5	
Quiet output minimum dynamic V <sub>OH</sub>	Vohv	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	1.9	V
		V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note)	3.3	2.2	

Note: Parameter guaranteed by design.

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	CIN	_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz	(Note)	1.8, 2.5, 3.3	20	pF

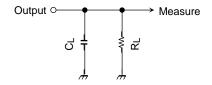
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)



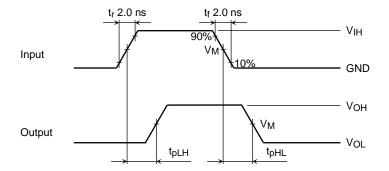
### **AC Test Circuit**



	Vcc	
Symbol	$\begin{array}{c} 3.3 \pm 0.3 \ \text{V} \\ 2.5 \pm 0.2 \ \text{V} \\ 1.8 \pm 0.15 \ \text{V} \end{array}$	1.5 ± 0.1 V 1.2V
RL	500 Ω	2 kΩ
CL	30 pF	15 pF

Figure 1

#### **AC Waveform**



Symbol	Vcc				
	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 ± 0.15 V	$1.5\pm0.1~\textrm{V}$	1.2 V
VIH	2.7 V	Vcc	Vcc	Vcc	Vcc
V <sub>M</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2

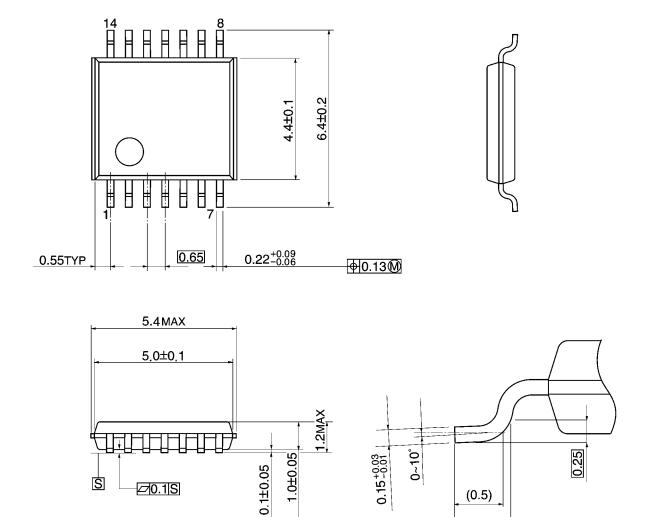
Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>



### **Package Dimensions**

TSSOP14-P-0044-0.65A

Unit: mm



8

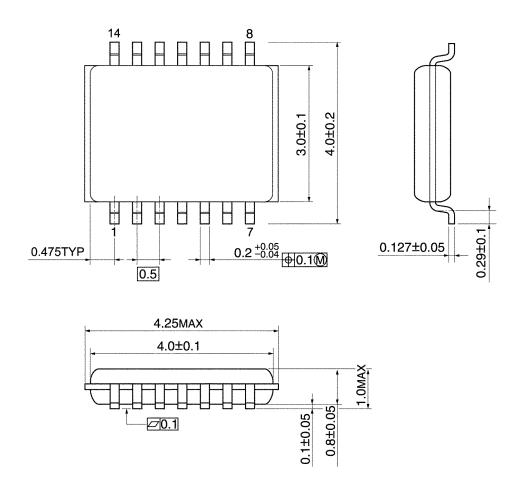
Weight: 0.06 g (typ.)

0.45~0.75



### **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

Downloaded from Arrow.com.



#### RESTRICTIONS ON PRODUCT USE

Toshiba Corporation and its subsidiaries and affiliates are collectively referred to as "TOSHIBA". Hardware, software and systems described in this document are collectively referred to as "Product".

- TOSHIBA reserves the right to make changes to the information in this document and related Product without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
  EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY
  CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
  ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation,
  equipment used in nuclear facilities, equipment used in the aerospace industry, lifesaving and/or life supporting medical equipment,
  equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or
  explosions, safety devices, elevators and escalators, and devices related to power plant. IF YOU USE PRODUCT FOR UNINTENDED USE,
  TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative or contact us via our
  website.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
  applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR
  PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER,
  INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING
  WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2)
  DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR
  INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE,
  ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for
  the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass
  destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations
  including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export
  and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and
  regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please
  use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including
  without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT
  OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

### TOSHIBA ELECTRONIC DEVICES & STORAGE CORPORATION

https://toshiba.semicon-storage.com/

Toshiba Electronic Devices & Storage Corporation