TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VCX2541FT, TC74VCX2541FK, TC74VCX2541FTG

Low-Voltage Octal Bus Buffer with 3.6-V Tolerant Inputs and Outputs

The TC74VCX2541 is a high-performance CMOS octal bus buffer. Designed for use in 1.8-V, 2.5-V or 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

It is also designed with overvoltage tolerant inputs and outputs up to $3.6\ V.$

This device is a non-inverting 3-state buffer having two active-low output enables. When either $\overline{OE}1$ or $\overline{OE}2$ are high, the terminal outputs are in the high-impedance state. This device is designed to be used with 3-state memory address drivers, etc. The $26\text{-}\Omega$ series resistor helps reducing output overshoot and undershoot without external resistor.

All inputs are equipped with protection circuits against static discharge.

Features (Note 1)

- 26-Ω series resistors on outputs.
- Low-voltage operation: VCC = 1.8 to 3.6 V
- High-speed operation: $t_{pd} = 4.4 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$

 $t_{pd} = 5.6 \text{ ns (max) (V}_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

 $: t_{pd} = 9.8 \text{ ns (max) (VCC} = 1.8 \text{ V)}$

Output current: I_{OH}/I_{OL} = ±12 mA (min) (V_{CC} = 3.0 V)

 $: I_{OH}/I_{OL} = \pm 8 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

 $: I_{OH}/I_{OL} = \pm 4 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$

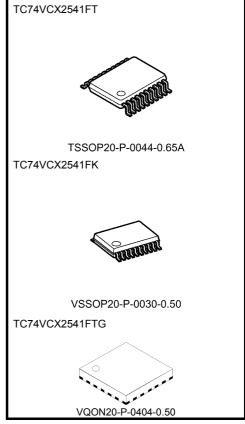
- Latch-up performance: -300 mA
- ESD performance: Machine model $\geq \pm 200 \text{ V}$ Human body model $\geq \pm 2000 \text{ V}$
- Package: TSSOP

VSSOP (US)

VQON

• 3.6-V tolerant function and power-down protection provided on all inputs and outputs

Note 1: When mounting VQON package, the type of recommended flux is RA or RMA.

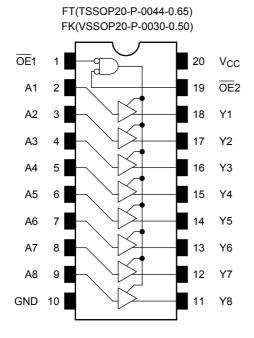


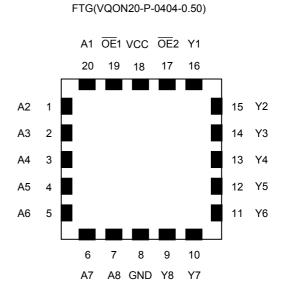
Weight

TSSOP20-P-0044-0.65A : 0.08 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.) VQON20-P-0404-0.50 : 0.0145g (typ.)

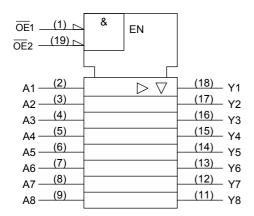
Start of commercial production 1998-06

Pin Assignment (top view)





IEC Logic Symbol



Truth Table

	Inputs		Outputs
OE1	OE2	An	Outputs
Н	Х	Х	Z
Х	Н	Х	Z
L	L	Н	Н
L	L	L	L

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN}	-0.5 to 4.6	V
		-0.5 to 4.6 (Note 2)	
DC output voltage	Vouт	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	l _{IK}	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	P_{D}	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Dower gunnly voltage	Vaa	1.8 to 3.6	V
Power supply voltage	V _{CC}	1.2 to 3.6 (Note 2)	V
Input voltage	V _{IN}	-0.3 to 3.6	V
Output voltage	V	0 to 3.6 (Note 3)	V
Output voltage	V _{OUT}	0 to V _{CC} (Note 4)	V
		±12 (Note 5)	
Output current	I _{OH} /I _{OL}	±8 (Note 6)	mA
		±4 (Note 7)	
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V

Note 1: The operating ranges are required to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either V_{CC} or GND. Please connect both bus inputs and the bus outputs with V_{CC} or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

Note 2: Data retention only

Note 3: OFF state

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 7: $V_{CC} = 1.8 \text{ V}$

Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C, 2.7 V < V_{CC} \leq 3.6 V)

Characteris	stics	Symbol	Test Co	Test Condition V _{CC}		Test Condition V _{CC}		Min	Max	Unit
Input voltage	H-level	V _{IH}	-	_	2.7 to 3.6	2.0	_	V		
input voitage	L-level	V _{IL}	_	_	2.7 to 3.6	_	0.8	V		
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_			
	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -6 \text{ mA}$	2.7	2.2	_			
				$I_{OH} = -8 \text{ mA}$	3.0	2.4	_			
Output voltage				I _{OH} = -12 mA	3.0	2.2	_	V		
				I _{OL} = 100 μA	2.7 to 3.6	_	0.2			
	L-level	.,	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	I _{OL} = 6 mA	2.7	_	0.4			
	L-ievei	VoL	$V_{IN} = V_{IH}$ or V_{IL}	I _{OL} = 8 mA	3.0	_	0.55			
				I _{OL} = 12 mA	3.0	_	0.8			
Input leakage curre	nt	I _{IN}	V _{IN} = 0 to 3.6 V		2.7 to 3.6	_	±5.0	μА		
3-state output OFF	state current	loz	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = 0$ to 3.6 V		2.7 to 3.6	_	±10.0	μА		
Power-off leakage of	urrent	loff	V _{IN} , V _{OUT} = 0 to 3.6 V		0	_	10.0	μА		
O de constant a constant		laa	V _{IN} = V _{CC} or GND		2.7 to 3.6	_	20.0			
Quiescent supply cu	an ent	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6$	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		_	±20.0	μΑ		
Increase in I _{CC} per	input	Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6		750			

DC Characteristics (Ta = -40 to 85° C, 2.3 V \leq V_{CC} \leq 2.7 V)

Character	istics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}	-	_	2.3 to 2.7	1.6	_	V
Input voltage	L-level	V _{IL}	-		2.3 to 2.7	_	0.7	V
				I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2		
	H-level	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -4 mA	2.3	2.0	_	
				$I_{OH} = -6 \text{ mA}$	2.3	1.8	_	
Output voltage				$I_{OH} = -8 \text{ mA}$	2.3	1.7	_	
				$I_{OL} = 100 \mu A$	2.3 to 2.7	_	0.2	
	L-level	V _{OL}	$V_{IN} = V_{IH}$ or V_{IL}	$I_{OL} = 6 \text{ mA}$	2.3	_	0.4	
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.6	
Input leakage curre	ent	I _{IN}	V _{IN} = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μΑ
3-state output OFF	state current	loz	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 3.6 \text{ V}$		2.3 to 2.7	_	±10.0	μА
Power-off leakage	current	loff	V _{IN} , V _{OUT} = 0 to 3.6 V		0	_	10.0	μΑ
Quiescent supply of	urrent	Icc	V _{IN} = V _{CC} or GND		2.3 to 2.7	_	20.0	μА
Quiescent supply o	.uii 6iit	icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.$	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V			±20.0	μΑ



DC Characteristics (Ta = -40 to 85° C, $1.8 \text{ V} \leq \text{V}_{CC} < 2.3 \text{ V}$)

Characteristics		Symbol	Test Co	andition		Min	Max	Unit				
Onaraciens	51103	Cymbol	1031 00	Test Condition		IVIIII	IVICA	Offic				
Input voltage	H-level	V _{IH}	_	-	1.8 to 2.3	0.7 × V _{CC}		V				
input voltage	L-level	V _{IL}	_	_	1.8 to 2.3		0.2 × V _{CC}	V				
	H-level	Voh	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -100 \mu A$	1.8	V _{CC} - 0.2						
Output voltage				$I_{OH} = -4 \text{ mA}$	1.8	1.4	_	V				
	L-level	V _{OL}	V _{IN} = V _{IH} or V _{II}	$I_{OL} = 100 \mu A$	1.8		0.2					
	L-IEVEI	VOL	VIN — VIH OI VIL	I _{OL} = 4 mA	1.8		0.3					
Input leakage currer	nt	I _{IN}	V _{IN} = 0 to 3.6 V		1.8		±5.0	μΑ				
3-state output OFF s	state current	loz	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = 0$ to 3.6 V				1.8		±10.0	μА		
Power-off leakage c	urrent	I _{OFF}	V _{IN} , V _{OUT} = 0 to 3.6 V		V _{IN} , V _{OUT} = 0 to 3.6 V		V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μА
Ouis a sent summit summer		loo	V _{IN} = V _{CC} or GND		1.8		20.0	μА				
Quiescent supply cu	iii Gill	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6$	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		_	±20.0	μА				

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω) (Note 1)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	4		1.8	1.5	9.8	
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.5 ± 0.2	8.0	5.6	ns
	t _{pHL}		3.3 ± 0.3	0.6	4.4	
	+		1.8	1.5	9.8	ns
3-state output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 3	2.5 ± 0.2	8.0	6.5	
			3.3 ± 0.3	0.6	5.0	
	+			1.5	7.7	
3-state output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	2.5 ± 0.2	8.0	4.3	ns
			3.3 ± 0.3	0.6	3.9	
Output to output skew	.		1.8		0.5	
	t _{osLH} t _{osHL}	(Note 2)	2.5 ± 0.2	_	0.5	ns
			3.3 ± 0.3	_	0.5	

Note 1: For $C_L = 50$ pF, add approximately 300 ps to the AC maximum specification.

Note 2: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$



Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition		.,	Тур.	Unit
				V _{CC} (V)		
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	0.15	
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	0.25	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	3.3	0.35	
	V _{OLV}	$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	-0.15	V
Quiet output minimum dynamic V _{OL}		$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	-0.25	
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	3.3	-0.35	
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	1.8	1.55	
Quiet output minimum dynamic V _{OH}	V _{OHV}	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	2.5	2.05	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note)	3.3	2.65	

Note: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition			Tun	Unit
Characteristics	Symbol			V _{CC} (V)	Тур.	Offic
Input capacitance	C _{IN}	_		1.8, 2.5, 3.3	6	pF
Output capacitance	CO	_		1.8, 2.5, 3.3	7	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note)	1.8, 2.5, 3.3	20	pF

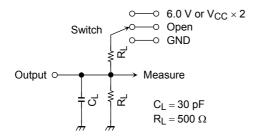
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$



AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	6.0 V V _{CC} × 2	$@V_{CC} = 3.3 \pm 0.3 \text{ V} \\ @V_{CC} = 2.5 \pm 0.2 \text{ V} \\ @V_{CC} = 1.8 \text{ V}$	
t _{pHZ} , t _{pZH}	GND		

Figure 1

AC Waveform

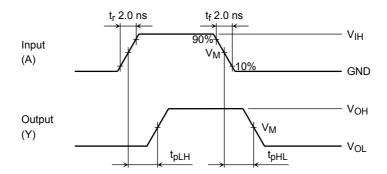


Figure 2 t_{pLH}, t_{pHL}

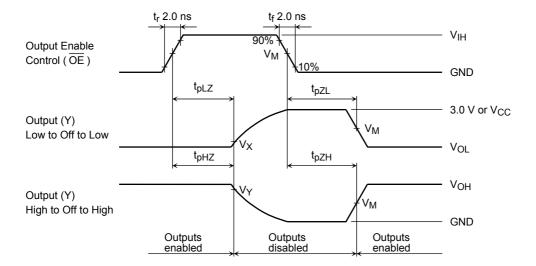


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

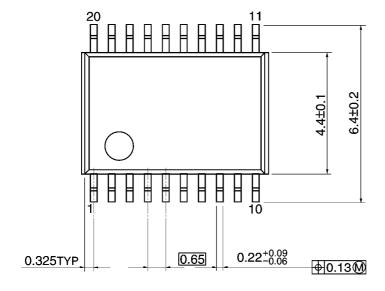
Symbol		V _{CC}	
Syllibol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V
V _{IH}	2.7 V	V _{CC}	V _{CC}
V _M	1.5 V	V _{CC} /2	V _{CC} /2
VX	V _{OL} + 0.3 V	V _{OL} + 0.15 V	V _{OL} + 0.15 V
VY	V _{OH} – 0.3 V	V _{OH} – 0.15 V	V _{OH} – 0.15 V

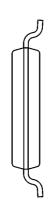
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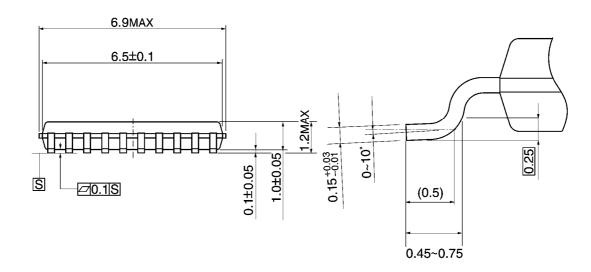
Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm



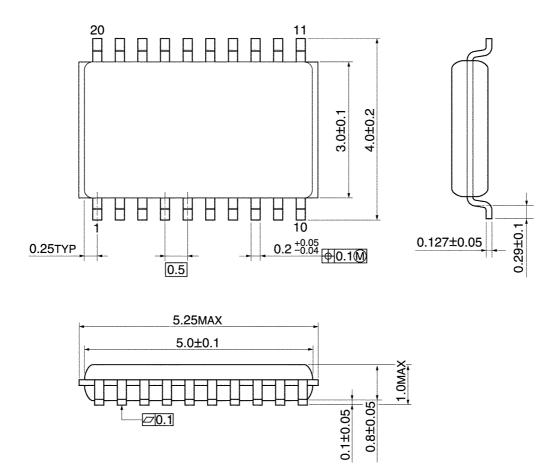




Weight: 0.08 g (typ.)

Package Dimensions

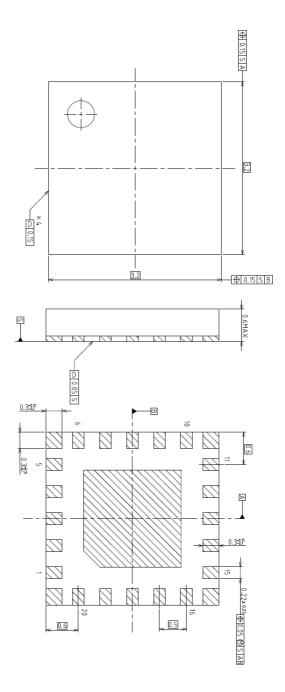
VSSOP20-P-0030-0.50 Unit: mm



Weight: 0.03 g (typ.)

Package Dimensions

VQON20-P-0404-0.50 Unit: mm



Weight: 0.0145 g (typ.)

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