TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LVX04F,TC74LVX04FN,TC74LVX04FT

Hex Inverter

The TC74LVX04F/FN/FT is a high-speed CMOS hex inverter fabricated with silicon gate CMOS technology. Designed for use in 3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

This device is suitable for low-voltage and battery operated systems.

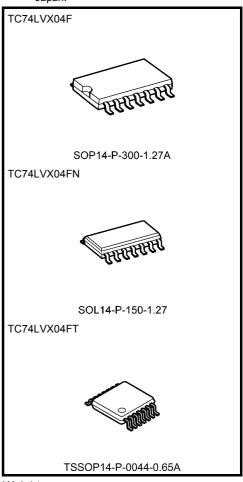
The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High-speed: $t_{pd} = 4.1 \text{ ns (typ.)} (V_{CC} = 3.3 \text{ V})$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max) (Ta} = 25 ^{\circ}\text{C)}$
- Input voltage level: $V_{IL} = 0.8 \text{ V (max)} (V_{CC} = 3 \text{ V})$ $V_{IH} = 2.0 \text{ V (min)} (V_{CC} = 3 \text{ V})$
- · Power-down protection provided on all inputs
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Low noise: $V_{OLP} = 0.5 \text{ V (max)}$
- Pin and function compatible with 74HC04

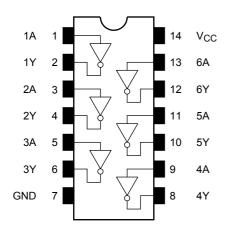
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

SOP14-P-300-1.27A : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol

1A	(1)	1	(2) 1Y
2A	(3)		(4) 2Y
	(5)		(6) 3Y
3A	(9)		(8)
4A	(11)		(10) 4Y
5A	(13)		(12) 5Y
6A	(- /		6Y

Truth Table

Inputs	Outputs
Α	Υ
L	Н
Н	L

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to $V_{CC} + 0.5$	٧
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit		
				V _{CC} (V)	Min	Тур.	Max	Min	Max		
					2.0	1.5	_	_	1.5	_	
	H-level	V _{IH}	_		3.0	2.0	_	_	2.0	_	
Input voltage					3.6	2.4	_	_	2.4	_	V
input voltage					2.0	_	_	0.5	_	0.5	v
	L-level	V _{IL}	_		3.0	_	_	0.8	_	0.8	
					3.6	_	_	0.8	_	0.8	
	H-level V ₀	V _{OH}		$I_{OH} = -50 \mu A$	2.0	1.9	2.0	_	1.9	_	
			$V_{IN} = V_{IL}$	$I_{OH} = -50 \mu A$	3.0	2.9	3.0	_	2.9	_	
Output voltage				I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	V
Output voltage	L-level V _O		$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$	2.0	_	0.0	0.1	_	0.1	v
		V _{OL}		$I_{OL} = 50 \mu A$	3.0	_	0.0	0.1	_	0.1	
				I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
Input leakage current		I _{IN}	V _{IN} = 5.5 V or GND		3.6	_	_	±0.1	_	±1.0	μА
Quiescent supply current		Icc	V _{IN} = V _{CC} or GND		3.6	_	_	2.0	_	20.0	μА

AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
	t _{pLH}		2.7	15	_	5.4	10.1	1.0	12.5	- ns
Propagation delay time				50	_	7.9	13.6	1.0	16.0	
Topagation delay time	t _{pHL}	_	3.3 ± 0.3	15	_	4.1	6.2	1.0	7.5	
				50	_	6.6	9.7	1.0	11.0	
Output to output skew	t _{osLH}	(Note 1)	2.7	50		_	1.5		1.5	ns
Output to output skew	t _{osHL}		3.3 ± 0.3	50	_	_	1.5	_	1.5	115
Input capacitance	C _{IN}			(Note 2)		4	10		10	pF
Power dissipation capacitance	C_{PD}			(Note 3)		18	_	_	_	pF

Note 1: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Note 2: Parameter guaranteed by design.

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

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Average operating current can be obtained by the equation:

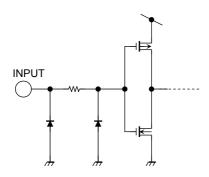
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$



Noise Characteristics (Ta = 25°C, input: $t_r = t_f = 3$ ns, $C_L = 50$ pF)

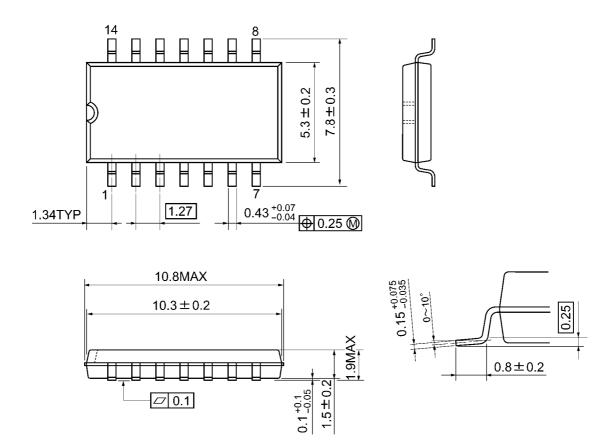
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	_	3.3	0.3	0.5	V
Quiet output minimum dynamic V _{OL}	V _{OLV}		3.3	-0.3	-0.5	V
Minimum high level dynamic input voltage V _{IH}	V _{IHD}		3.3	ı	2.0	V
Maximum low level dynamic input voltage V _{IL}	V _{ILD}	-	3.3		0.8	V

Input Equivalent Circuit



Package Dimensions

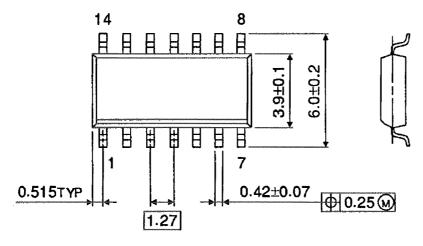
SOP14-P-300-1.27A Unit: mm

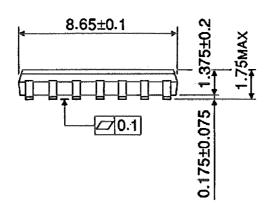


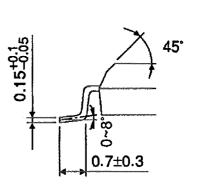
Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL14-P-150-1.27 Unit: mm







Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

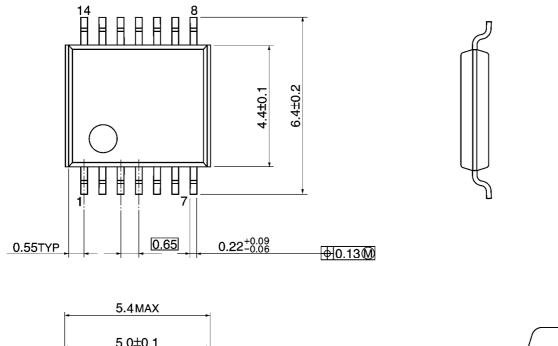
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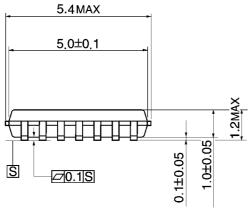
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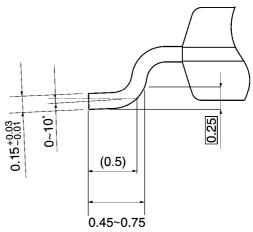
Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm







Weight: 0.06 g (typ.)

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