TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HCT32AP,TC74HCT32AF,TC74HCT32AFN

Quad 2-Input OR Gate

The TC74HCT32A is a high speed CMOS 2-INPUT OR GATE fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

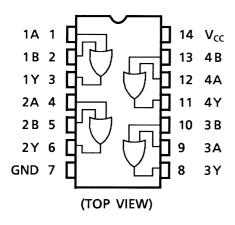
The internal circuit is composed of 4 stages including buffer output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

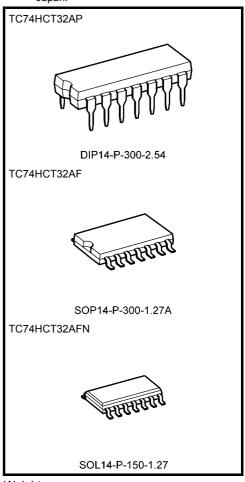
Features

- High speed: $t_{pd} = 12 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- Compatible with TTL outputs: V_{IH} = 2 V (min) V_{IL} = 0.8 V (max)
- Wide interfacing ability: LSTTL, NMOS, CMOS
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74LS32

Pin Assignment



Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.)

IEC Logic Symbol

1A	(1)	≥ 1	(3) 1Y
1 B	(2)		- 11
2A	(4) (5)		(6) 2Y
2 B			
3A	(9) (10)		(8) 3Y
3 B	(12)		
4A	(13)		(11) 4Y
4B	(13)		7'

Truth Table

Α	В	Υ
Н	Н	Н
L	Н	Н
Н	L	Н
L	L	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	V
DC output voltage	Vout	−0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	−65 ~ 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	t _r , t _f	0~500	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Cumbal	Test Condition V _{CC} (V)		-	Ta = 25°0		Ta = -40~85°C		Unit	
Characteristics	Symbol			Min	Тур.	Max	Min	Max	Offic	
High-level input voltage	V _{IH}	_		4.5~5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V _{IL}	_		4.5~5.5		_	0.8	_	0.8	V
High-level output	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	4.5	4.4	4.5	_	4.4	_	٧
voltage			I _{OH} = -4 mA	4.5	4.18	4.31	_	4.13	_	
Low-level output	V _{OL}	1 4 110	I _{OL} = 20 μA	4.5	_	0.0	0.1	_	0.1	V
voltage		= V _{IH} or V _{IL}	I _{OL} = 4 mA	4.5	_	0.17	0.26	_	0.33	V
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_	_	±0.1	_	±1.0	μА
	I _{CC}	$V_{IN} = V_{CC}$ or GND Per input: $V_{IN} = 0.5 \text{ V or } 2.4 \text{ V}$ Other input: V_{CC} or GND		5.5	_	_	1.0	_	10.0	μА
Quiescent supply current	IC			5.5	_	_	2.0	_	2.9	mA

AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t⊤LH •	_	_	6	12	ns
	t _{THL}					
Propagation delay time	t _{pLH}	_	_	10	16	ns
	t _{pHL}				,	

AC Characteristics ($C_L = 50$ pF, input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -4	- Unit	
Characteristics	Syllibol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Output transition time	t _{TLH}		4.5	_	8	15	_	19	ns
Output transition time	t _{THL}	_	5.5		7	13		16	
Propagation delay	t _{pLH}		4.5	_	13	20	_	25	ns
time	t_{pHL}		5.5		11	18		23	
Input capacitance	C _{IN}				5	10		10	pF
Power dissipation	C _{PD}				23				ηE
capacitance	(Note)		_	. 23				pF	

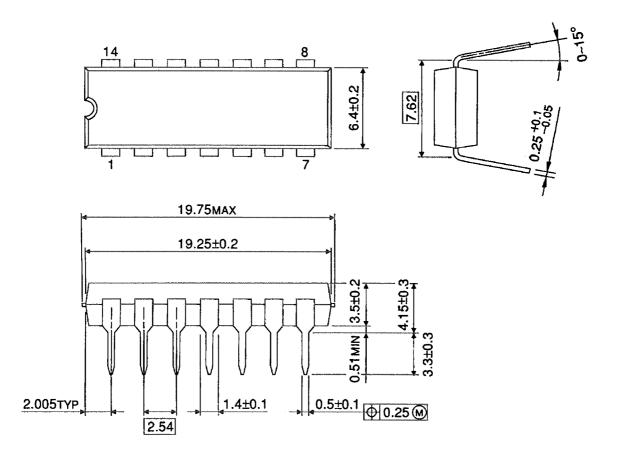
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Package Dimensions

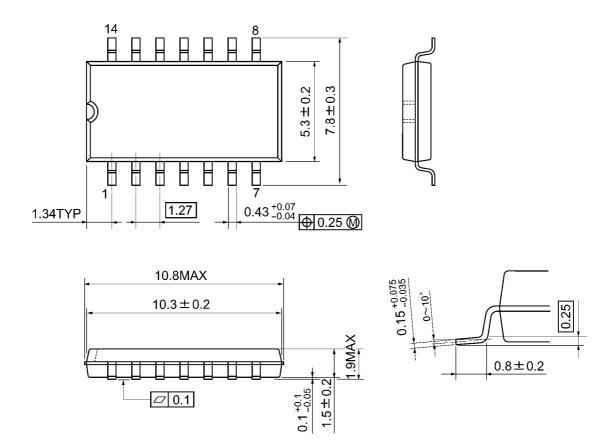
DIP14-P-300-2.54 Unit: mm



Weight: 0.96 g (typ.)

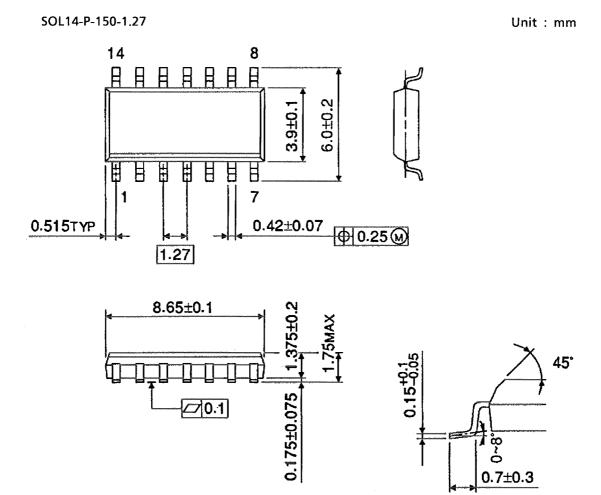
Package Dimensions

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



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Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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