TOSHIBA HIGH EFFICIENCY DIODE STACK (HED) SILICON EPITAXIAL TYPE

# 10JL2CZ47

SWITCHING MODE POWER SUPPLY APPLICATION **CONVERTER & CHOPPER APPLICATION** 

• Repetitive Peak Reverse Voltage  $: V_{RRM} = 600 \text{ V}$  Average Output Rectified Current  $I_{O} = 10 \text{ A}$ • Ultra Fast Reverse–Recovery Time  $: t_{rr} = 50 \text{ ns (Max)}$ 

Low Switching Losses and Output Noise

### **ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)**

| CHARACTERISTIC                                      | SYMBOL           | RATING                 | TIMU |
|---|------------------|------------------------|------|
| Repetitive Peak Reverse Voltage                     | $V_{RRM}$        | 600                    | (V)  |
| Average Output Rectified Current                    | Io               | 10                     | Ą    |
| Peak One Cycle Surge Forward<br>Current (Sine Wave) | I <sub>FSM</sub> | 50 (50Hz)<br>55 (60Hz) | ⇒ A  |
| Junction Temperature                                | Tj               | +40~150                | °C   |
| Storage Temperature Range                           | T <sub>stg</sub> | -40~150                | /°C  |
| Screw Torque  |                  | 0.6                    | N⋅m  |

Unit: mm ANODE ANODE **CATHODE** JEDEC JEITA TOSHIBA 12-10C1A

Weight: 2.0g

Note: Using continuously under heavy loads (e.g.) the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to

decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

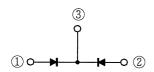
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# ELECTRICAL CHARACTERISTICS (Ta = 25°C

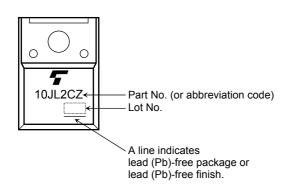
| CHARACTERISTIC                              | SYMBOL                | TEST CONDITION                           | MIN | TYP. | MAX | UNIT   |
|---|-----------------------|--|-----|------|-----|--------|
| Peak Forward Voltage (Note 1)               | VFM                   | I <sub>FM</sub> = 5A                     | _   | _    | 2.0 | V      |
| Repetitive Peak Reverse<br>Current (Note 1) | IRRM                  | V <sub>RRM</sub> = 600V                  | _   | _    | 50  | μΑ     |
| Reverse Recovery Time (Note 1)              | th.                   | I <sub>F</sub> = 2A, di / dt = -20A / μs | _   | _    | 50  | ns     |
| Forward Recovery Time (Note 1)              | <b>I</b> fr           | I <sub>F</sub> = 1A                      | _   | _    | 150 | ns     |
| Thermal Resistance                          | R <sub>th (j-c)</sub> | Total DC, Junction to Case               | _   | _    | 3.6 | °C / W |

Note 1: A value applied to one cell.

#### **POLARITY**



#### **MARKING**



| Abbreviation Code | Part No.  |  |  |  |
|-------------------|-----------|--|--|--|
| 10JL2CZ           | 10JL2CZ47 |  |  |  |

## **Handling Precaution**

The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

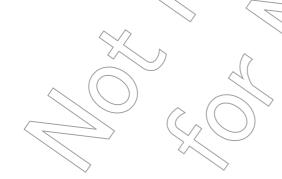
VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the absolute maximum rating of V<sub>RRM</sub> for a DC circuit and be no greater than 50% of that of V<sub>RRM</sub> for an AC circuit. V<sub>RRM</sub> has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

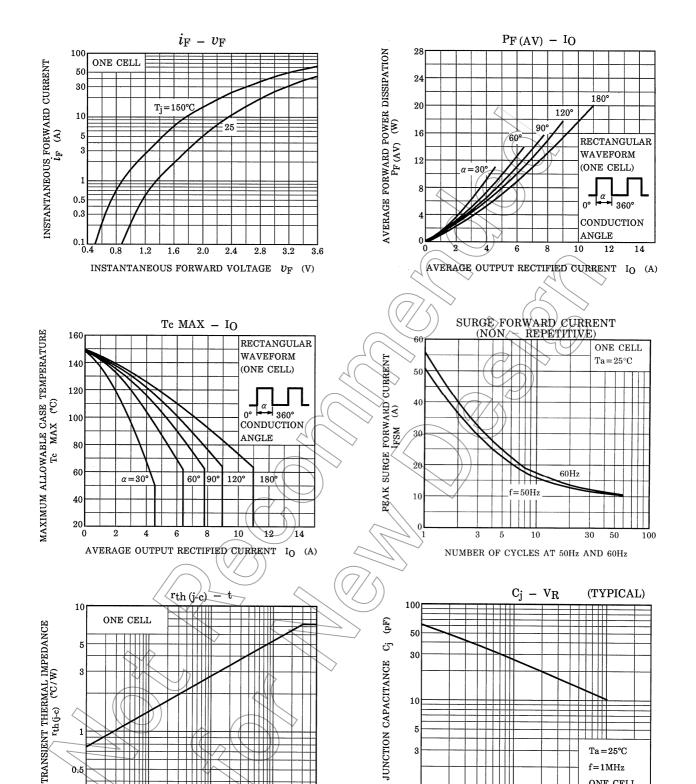
Io: We recommend that the worst case current be no greater than 80% of the absolute maximum rating of Io. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-Io curve.

This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

Please refer to the Rectifiers databook for further information.





1000

TIME t (ms)

. 10

REVERSE VOLTAGE  $V_R$  (V)

f=1MHz

100

ONE CELL

300

0.5

0.3



#### **RESTRICTIONS ON PRODUCT USE**

20070701-EN

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  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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