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DRV421 SBOS704 – MAY 2015

DRV421 Integrated Fluxgate Sensor for Closed-Loop Current Sensing

Technical

Documents

1 Features

- High-Precision Integrated Fluxgate Sensor
 - Offset and Drift: ±3 µT max, ±10 nT/°C typ
- Extended Current Measurement Range
 - H-Bridge Output Drive: ±250 mA typ at 5 V
- Precision Differential Amplifier
 - Offset and Drift (max): ±100 μV, ±2 μV/°C
 - Gain Error and Drift (max): ±0.3%, ±5 ppm/°C
- Precision Reference
 - Accuracy and Drift (max): ±2%, ±100 ppm/°C
 - Pin-Selectable Voltage: 2.5 V or 1.65 V
 - Selectable Ratiometric Mode: VDD/2
- Overrange and Error Flags
- Supply Voltage Range: 3.0 V to 5.5 V
- Package: 4-mm × 4-mm, Thin WQFN-20 with PowerPAD[™]
- Fully Specified Over the Extended Industrial Temperature Range

2 Applications

- Closed-Loop DC- and AC-Current Sensor Modules
- Leakage Current Sensors
- Industrial Monitoring and Control Systems

3 Description

Tools &

Software

The DRV421 is designed for magnetic closed-loop current sensing solutions, enabling isolated, precise dc- and ac-current measurements. The device provides both, a priopratery integrated fluxgate sensor, and the required analog signal conditioning, thus minimizing component count and cost. The low offset and drift of the fluxgate sensor, along with an optimized front-end circuit results in unrivaled measurement precision.

Support &

Community

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The DRV421 provides all the necessary circuit blocks to drive the current-sensing feedback loop. The sensor front-end circuit is followed by a filter that can be configured to work with common current-sensing modules. The integrated 250-mA H-Bridge drives the compensation coil, while doubling the current measurement range, comparing to conventional single-ended drive methods. The device also provides a precision voltage reference and differential amplifier to generate and drive the analog output signal.

The DRV421 is available in a thermally enhanced 4mm \times 4-mm, thin WQFN package with PowerPAD, and is specified for operation over the extended industrial temperature range of -40°C to +125°C.

Device Information⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)		
DRV421	WQFN (20)	4.00 mm × 4.00 mm		

(1) For all available packages, see the package option addendum at the end of the datasheet.



An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCT PREVIEW Information. Product in design phase of development. Subject to change or discontinuance without notice.



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4 Pin Configuration and Functions



Pin Functions

PIN		1/0	DESCRIPTION			
NAME	NO.	1/0	DESCRIPTION			
AINN	14	I	Inverting input of differential amplifier			
AINP	13	I	Noninverting input of differential amplifier			
DEMAG	18	I	Degauss control input			
ER	19	0	Error output flag, open-drain			
GND	7, 10, 16, 17	—	Ground reference			
GSEL0	1	I	Gain and bandwidth selection input 0			
GSEL1	20	I	Gain and bandwidth selection input 1			
ICOMP1	12	0	Output 1 of compensation coil driver			
ICOMP2	11	0	Output 2 of compensation coil driver			
OR	15	0	Overrange indication output of the differential amplifier, open-drain, low-active			
REFIN	5	I	Common-mode reference input for the differential amplifier			
REFOUT	4	0	Voltage reference output			
RSEL0	3	I	Voltage reference mode selection input 0			
RSEL1	2	I	Voltage reference mode selection input 1			
VDD	8, 9	—	Supply voltage, 3.0 V to 5.5 V. Both pins should be decoupled using $1-\mu F$ ceramic capacitors placed as close as possible to the device.			
VOUT	6	0	Differential amplifier output			
PowerPAD thermal pad —		_	Connect thermal pad to GND			



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5 Device and Documentation Support

5.1 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E[™] Online Community *TI's Engineer-to-Engineer (E2E) Community.* Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support TI's Design Support Quickly find helpful E2E forums along with design support tools and contact information for technical support.

5.2 Trademarks

PowerPAD, E2E are trademarks of Texas Instruments. All other trademarks are the property of their respective owners.

5.3 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

5.4 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.



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PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
DRV421RTJR	PREVIEW	QFN	RTJ	20	3000	TBD	Call TI	Call TI	-40 to 125	>	
										D421XXX	
DRV421RTJT	PREVIEW	QFN	RTJ	20	250	TBD	Call TI	Call TI	-40 to 125	>	
										D421XXX	
PDRV421RTJT	PREVIEW	QFN	RTJ	20	250	TBD	Call TI	Call TI	-40 to 125		

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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MECHANICAL DATA



- B. This drawing is subject to change without notice.
- C. QFN (Quad Flatpack No-Lead) package configuration.
- D. The package thermal pad must be soldered to the board for thermal and mechanical performance.
- E. See the additional figure in the Product Data Sheet for details regarding the exposed thermal pad features and dimensions.
- earroweak Check thermal pad mechanical drawing in the product datasheet for nominal lead length dimensions.



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