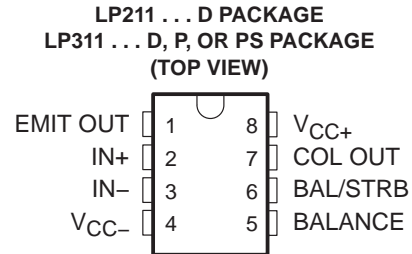


LP211, LP311 LOW-POWER DIFFERENTIAL COMPARATORS WITH STROBES

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- Low Power Drain . . . 900 μ W Typical With 5-V Supply
- Operates From ± 15 V or From a Single Supply as Low as 3 V
- Output Drive Capability of 25 mA
- Emitter Output Can Swing Below Negative Supply
- Response Time . . . 1.2 μ s Typ
- Low Input Currents:
Offset Current . . . 2 nA Typ
Bias Current . . . 15 nA Typ
- Wide Common-Mode Input Range:
–14.5 V to 13.5 V Using ± 15 -V Supply
- Offset Balancing and Strobe Capability
- Same Pinout as LM211, LM311
- Designed To Be Interchangeable With Industry-Standard LP311



description/ordering information

The LP211 and LP311 devices are low-power versions of the industry-standard LM211 and LM311 devices. They take advantage of stable, high-value, ion-implanted resistors to perform the same function as the LM311 series, with a 30:1 reduction in power consumption, but only a 6:1 slowdown in response time. They are well suited for battery-powered applications and all other applications where fast response times are not needed. They operate over a wide range of supply voltages, from ± 18 V down to a single 3-V supply with less than 300- μ A current drain, but are still capable of driving a 25-mA load. The LP211 and LP311 are quite easy to apply free of oscillation if ordinary precautions are taken to minimize stray coupling from the output to either input or to the trim pins. In addition, offset balancing is available to minimize input offset voltage. Strobe capability also is provided to turn off the output (regardless of the inputs) by pulling the strobe pin low.

The LP211 is characterized for operation from -25°C to 85°C . The LP311 is characterized for operation from 0°C to 70°C .

ORDERING INFORMATION

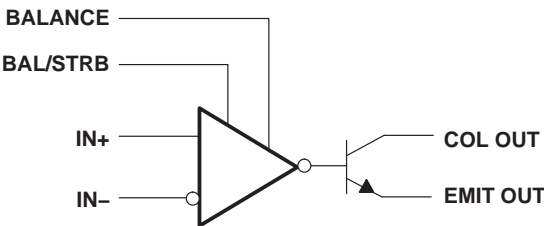
T_A	V_{IO} max AT 25°C	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-0°C to 70°C	7.5 mV	PDIP (P)	Tube of 50	LP311P	LP311P
		SOIC (D)	Tube of 75	LP311D	LP311
			Reel of 2500	LP311DR	
		SOP (PS)	Reel of 2000	LP311PSR	L311
-25°C to 85°C	7.5 mV	SOIC (D)	Tube of 75	LP211D	LP211
			Reel of 2500	LP211DR	

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

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functional block diagram



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage (see Note 1): V_{CC+}	18 V
V_{CC-}	-18 V
Differential input voltage, V_{ID} (see Note 2)	± 30 V
Input voltage, V_I (either input, see Notes 1 and 3)	± 15 V
Voltage from emitter output to V_{CC-}	30 V
Voltage from collector output to V_{CC-}	40 V
Voltage from collector output to emitter output	40 V
Duration of output short circuit (see Note 4)	40 V
Package thermal impedance, θ_{JA} (see Notes 5 and 6): D package	97°C/W
P package	85°C/W
PS package	95°C/W
Operating virtual junction temperature, T_J	150°C
Storage temperature range, T_{stg}	-65°C to 150°C

[†] Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the recommended operating conditions section of this specification is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES:
1. All voltage values, unless otherwise noted, are with respect to the midpoint between V_{CC+} and V_{CC-} .
 2. Differential input voltages are at $IN+$ with respect to $IN-$.
 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage of ± 15 V, whichever is less.
 4. The output may be shorted to ground or to either power supply.
 5. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A) / \theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
 6. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions

	MIN	MAX	UNIT
($ V_{CC\pm} \leq 15$ V) Input voltage	$V_{CC-} + 0.5$	$V_{CC+} - 1.5$	V
$V_{CC+} - V_{CC-}$ Supply voltage	3.5	30	V

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electrical characteristics at specified free-air temperature, $V_{CC\pm} = \pm 15$ V (unless otherwise noted)

PARAMETER		TEST CONDITIONS	T_A	MIN	TYP†	MAX	UNIT
V_{ID}	Input offset voltage	$R_S < 100$ k Ω , See Note 7	25°C		2	7.5	mV
			Full range			10	
V_{OL}	Low-level output voltage	$V_{ID} < -10$ mV, See Note 8	25°C		0.4	1.5	V
		$V_{CC} = 4.5$ V, $V_{ID} < -10$ mV, See Note 8	Full range		0.1	0.4	
I_{IO}	Input offset current	See Note 7	25°C		2	25	nA
			Full range			35	
I_{IB}	Input bias current		25°C		15	100	nA
			Full range			150	
	Low-level strobe current	$V_{(strobe)} = 0.3$ V, See Note 9	25°C		100	300	μ A
$I_{O(off)}$	Output off-state current	$V_{ID} > 10$ mV, $V_{CE} = 35$ V	25°C		0.2	100	nA
A_{VD}	Large-signal differential-voltage amplification	$R_L = 5$ k Ω	25°C	40	100		V/mV
I_{CC+}	Supply current from V_{CC+}	$V_{ID} = -50$ mV, $R_L = \infty$	Full range		150	300	μ A
I_{CC-}	Supply current from V_{CC-}	$V_{ID} = 50$ mV, $R_L = \infty$	Full range		- 80	- 180	μ A

† All typical values are at $V_{CC\pm} = \pm 15$ V, $T_A = 25^\circ\text{C}$.

NOTES: 7. The offset voltages and offset currents given are the maximum values required to drive the output within 1 V of either supply with a 1-mA load. Thus, these parameters define an error band and take into account the worst-case effects of voltage gain and input impedance.

8. Voltages are with respect to EMIT OUT and V_{CC-} tied together.

9. The strobe should not be shorted to ground; it should be current driven at 100 μ A to 300 μ A.

switching characteristics, $V_{CC\pm} = \pm 5$ V, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	TYP	UNIT
Response time	See Note 10	1.2	μ s

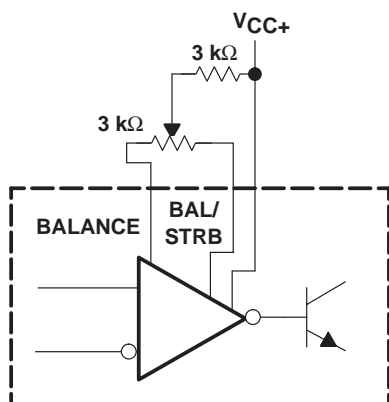
NOTE 10: The response time is specified for a 100-mV input step with 5-mV overdrive.



LP211, LP311 LOW-POWER DIFFERENTIAL COMPARATORS WITH STROBES

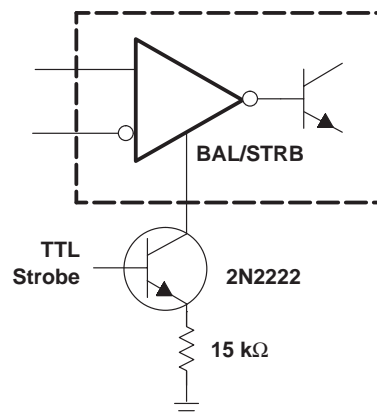
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TYPICAL APPLICATION CIRCUIT



NOTE: If offset balancing is not used, the BALANCE and BAL/STRB pins should be shorted together.

Figure 1. Offset Balancing



NOTE: Do not connect strobe pin directly to ground, because the output is turned off whenever current is pulled from the strobe pin.

Figure 2. Strobing

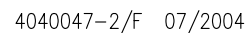
P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001

For the latest package information, go to http://www.ti.com/sc/docs/package/pkg_info.htm



A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
D. Falls within JEDEC MS-012 variation AA.

MECHANICAL DATA

PS (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



4040063/C 03/03

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-153

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