

# SN54ACT16244, 74ACT16244 16-BIT BUFFERS/LINE DRIVERS WITH 3-STATE OUTPUTS

SCAS116B – MARCH 1990 – REVISED APRIL 1996

- Members of the Texas Instruments *Widebus*™ Family
- Inputs Are TTL-Voltage Compatible
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Flow-Through Architecture Optimizes PCB Layout
- Distributed V<sub>CC</sub> and GND Pin Configurations Minimize High-Speed Switching Noise
- *EPIC*™ (Enhanced-Performance Implanted CMOS) 1-μm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic Shrink Small-Outline (DL) and Thin Shrink Small-Outline (DGG) Packages, and 380-mil Fine-Pitch Ceramic Flat (WD) Packages Using 25-mil Center-to-Center Pin Spacings

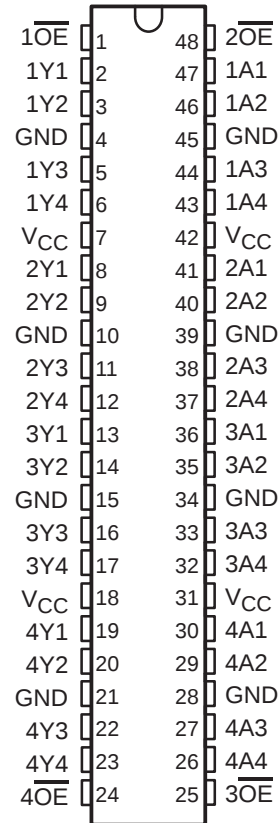
## description

The SN54ACT16244 and 74ACT16244 are 16-bit buffers/line drivers designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. They can be used as four 4-bit buffers, two 8-bit buffers, or one 16-bit buffer. The devices provide true outputs and symmetrical  $\overline{OE}$  (active-low) output-enable inputs.

The 74ACT16244 is packaged in TI's shrink small-outline package, which provides twice the I/O pin count and functionality of standard small-outline packages in the same printed-circuit-board area.

The SN54ACT16244 is characterized for operation over the full military temperature range of –55°C to 125°C. The 74ACT16244 is characterized for operation from –40°C to 85°C.

SN54ACT16244 . . . WD PACKAGE  
74ACT16244 . . . DGG OR DL PACKAGE  
(TOP VIEW)



FUNCTION TABLE  
(each driver)

| INPUTS          |   | OUTPUT |
|-----------------|---|--------|
| $\overline{OE}$ | A | Y      |
| L               | H | H      |
| L               | L | L      |
| H               | X | Z      |



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS  
INSTRUMENTS**

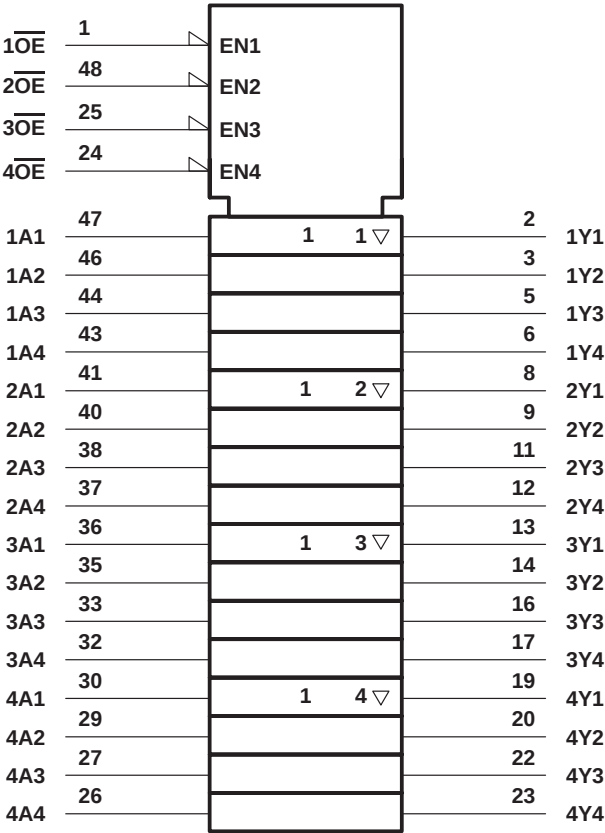
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16-BIT BUFFERS/LINE DRIVERS  
WITH 3-STATE OUTPUTS

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logic symbol†

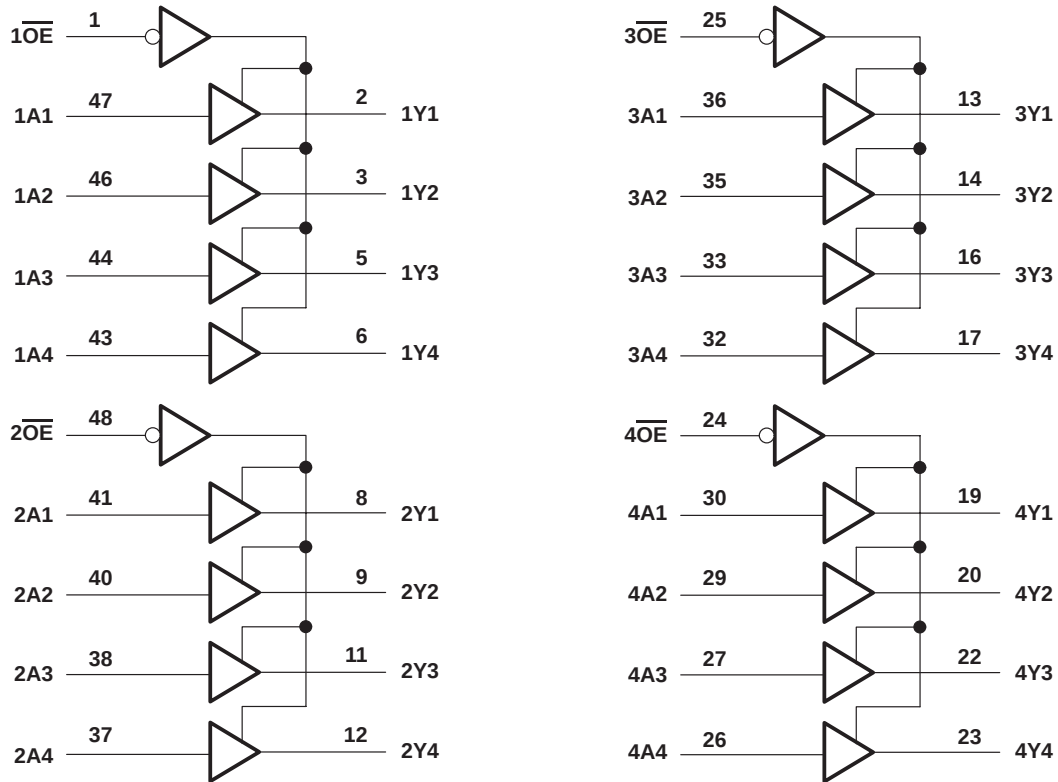


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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**16-BIT BUFFERS/LINE DRIVERS**  
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SCAS116B – MARCH 1990 – REVISED APRIL 1996

**logic diagram (positive logic)**



**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>**

|                                                                                                |                            |
|------------------------------------------------------------------------------------------------|----------------------------|
| Supply voltage range, $V_{CC}$                                                                 | –0.5 V to 7 V              |
| Input voltage range, $V_I$ (see Note 1)                                                        | –0.5 V to $V_{CC} + 0.5$ V |
| Output voltage range, $V_O$ (see Note 1)                                                       | –0.5 V to $V_{CC} + 0.5$ V |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ )                                  | ±20 mA                     |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ )                                 | ±50 mA                     |
| Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ )                                     | ±50 mA                     |
| Continuous current through $V_{CC}$ or GND                                                     | ±400 mA                    |
| Maximum power dissipation at $T_A = 55^\circ\text{C}$ (in still air) (see Note 2): DGG package | 0.85 W                     |
| DL package                                                                                     | 1.2 W                      |
| Storage temperature range, $T_{stg}$                                                           | –65°C to 150°C             |

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
 2. The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils.

# SN54ACT16244, 74ACT16244

## 16-BIT BUFFERS/LINE DRIVERS

### WITH 3-STATE OUTPUTS

SCAS116B – MARCH 1990 – REVISED APRIL 1996

#### recommended operating conditions (see Note 3)

|                     |                                    | SN54ACT16244 |          | 74ACT16244 |          | UNIT |
|---------------------|------------------------------------|--------------|----------|------------|----------|------|
|                     |                                    | MIN          | MAX      | MIN        | MAX      |      |
| $V_{CC}$            | Supply voltage (see Note 4)        | 4.5          | 5.5      | 4.5        | 5.5      | V    |
| $V_{IH}$            | High-level input voltage           | 2            |          | 2          |          | V    |
| $V_{IL}$            | Low-level input voltage            |              | 0.8      |            | 0.8      | V    |
| $V_I$               | Input voltage                      | 0            | $V_{CC}$ | 0          | $V_{CC}$ | V    |
| $V_O$               | Output voltage                     | 0            | $V_{CC}$ | 0          | $V_{CC}$ | V    |
| $I_{OH}$            | High-level output current          |              | –24      |            | –24      | mA   |
| $I_{OL}$            | Low-level output current           |              | 24       |            | 24       | mA   |
| $\Delta t/\Delta v$ | Input transition rise or fall rate | 0            | 10       | 0          | 10       | ns/V |
| $T_A$               | Operating free-air temperature     | –55          | 125      | –40        | 85       | °C   |

NOTES: 3. Unused inputs should be tied to  $V_{CC}$  through a pullup resistor of approximately 5 k $\Omega$  or greater to prevent them from floating.

4. All  $V_{CC}$  and GND pins must be connected to the proper voltage supply.

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER                | TEST CONDITIONS                                        | $V_{CC}$ | $T_A = 25^\circ\text{C}$ |     |           | SN54ACT16244 |          | 74ACT16244 |         | UNIT          |
|--------------------------|--------------------------------------------------------|----------|--------------------------|-----|-----------|--------------|----------|------------|---------|---------------|
|                          |                                                        |          | MIN                      | TYP | MAX       | MIN          | MAX      | MIN        | MAX     |               |
| $V_{OH}$                 | $I_{OH} = -50\ \mu\text{A}$                            | 4.5 V    | 4.4                      |     |           | 4.4          |          | 4.4        |         | V             |
|                          |                                                        | 5.5 V    | 5.4                      |     |           | 5.4          |          | 5.4        |         |               |
|                          | $I_{OH} = -24\ \text{mA}$                              | 4.5 V    | 3.94                     |     |           | 3.7          |          | 3.8        |         |               |
|                          |                                                        | 5.5 V    | 4.94                     |     |           | 4.7          |          | 4.8        |         |               |
|                          | $I_{OH} = -50\ \text{mA}^\dagger$                      | 5.5 V    |                          |     |           | 3.85         |          |            |         |               |
|                          | $I_{OH} = -75\ \text{mA}^\dagger$                      | 5.5 V    |                          |     |           |              |          | 3.85       |         |               |
| $V_{OL}$                 | $I_{OL} = 50\ \mu\text{A}$                             | 4.5 V    |                          |     | 0.1       |              | 0.1      |            | 0.1     | V             |
|                          |                                                        | 5.5 V    |                          |     | 0.1       |              | 0.1      |            | 0.1     |               |
|                          | $I_{OL} = 24\ \text{mA}$                               | 4.5 V    |                          |     | 0.36      |              | 0.5      |            | 0.44    |               |
|                          |                                                        | 5.5 V    |                          |     | 0.36      |              | 0.5      |            | 0.44    |               |
|                          | $I_{OL} = 50\ \text{mA}^\dagger$                       | 5.5 V    |                          |     |           | 1.65         |          |            |         |               |
|                          | $I_{OL} = 75\ \text{mA}^\dagger$                       | 5.5 V    |                          |     |           |              |          | 1.65       |         |               |
| $I_I$                    | $V_I = V_{CC}$ or GND                                  | 5.5 V    |                          |     | $\pm 0.1$ |              | $\pm 1$  |            | $\pm 1$ | $\mu\text{A}$ |
| $I_{OZ}$                 | $V_O = V_{CC}$ or GND                                  | 5.5 V    |                          |     | $\pm 0.5$ |              | $\pm 10$ |            | $\pm 5$ | $\mu\text{A}$ |
| $I_{CC}$                 | $V_I = V_{CC}$ or GND, $I_O = 0$                       | 5.5 V    |                          |     | 8         |              | 160      |            | 80      | $\mu\text{A}$ |
| $\Delta I_{CC}^\ddagger$ | One input at 3.4 V,<br>Other inputs at GND or $V_{CC}$ | 5.5 V    |                          |     | 0.9       |              | 1        |            | 1       | mA            |
| $C_i$                    | $V_I = V_{CC}$ or GND                                  | 5 V      |                          |     | 4.5       |              |          |            |         | pF            |
| $C_o$                    | $V_O = V_{CC}$ or GND                                  | 5 V      |                          |     | 13.5      |              |          |            |         | pF            |

$^\dagger$  Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

$^\ddagger$  This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V or  $V_{CC}$ .



**SN54ACT16244, 74ACT16244**  
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**WITH 3-STATE OUTPUTS**

SCAS116B – MARCH 1990 – REVISED APRIL 1996

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

| PARAMETER        | FROM<br>(INPUT)        | TO<br>(OUTPUT) | SN54ACT16244          |     |      |     |      | UNIT |
|------------------|------------------------|----------------|-----------------------|-----|------|-----|------|------|
|                  |                        |                | T <sub>A</sub> = 25°C |     |      | MIN | MAX  |      |
|                  |                        |                | MIN                   | TYP | MAX  |     |      |      |
| t <sub>PLH</sub> | A                      | Y              | 4                     | 6.5 | 8.5  | 3   | 10.3 | ns   |
| t <sub>PHL</sub> |                        |                | 3.4                   | 6.3 | 8.7  | 3.4 | 10.1 |      |
| t <sub>PZH</sub> | $\overline{\text{OE}}$ | Y              | 3                     | 5.8 | 8.1  | 3   | 10.5 | ns   |
| t <sub>PZL</sub> |                        |                | 3.7                   | 6.7 | 9.3  | 3.7 | 11   |      |
| t <sub>PHZ</sub> | $\overline{\text{OE}}$ | Y              | 5.4                   | 8.1 | 11.5 | 5.4 | 13   | ns   |
| t <sub>PLZ</sub> |                        |                | 5                     | 7.5 | 9.5  | 5   | 10.9 |      |

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

| PARAMETER        | FROM<br>(INPUT)        | TO<br>(OUTPUT) | 74ACT16244            |     |      |     |      | UNIT |
|------------------|------------------------|----------------|-----------------------|-----|------|-----|------|------|
|                  |                        |                | T <sub>A</sub> = 25°C |     |      | MIN | MAX  |      |
|                  |                        |                | MIN                   | TYP | MAX  |     |      |      |
| t <sub>PLH</sub> | A                      | Y              | 4                     | 6.5 | 8.5  | 4   | 9.4  | ns   |
| t <sub>PHL</sub> |                        |                | 3.4                   | 6.3 | 8.7  | 3.4 | 9.5  |      |
| t <sub>PZH</sub> | $\overline{\text{OE}}$ | Y              | 3                     | 5.8 | 8.1  | 3   | 8.9  | ns   |
| t <sub>PZL</sub> |                        |                | 3.7                   | 6.7 | 9.3  | 3.7 | 10.3 |      |
| t <sub>PHZ</sub> | $\overline{\text{OE}}$ | Y              | 5.4                   | 8.1 | 10.3 | 5.4 | 11.3 | ns   |
| t <sub>PLZ</sub> |                        |                | 5                     | 7.5 | 9.5  | 5   | 10.3 |      |

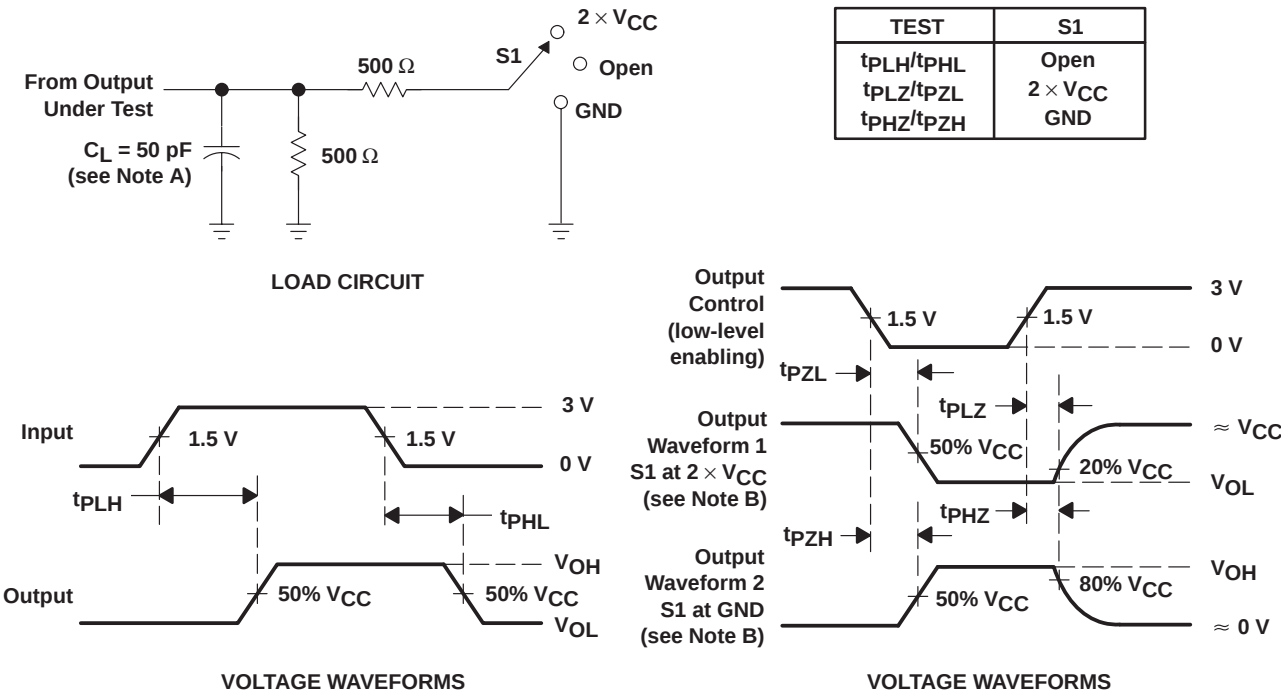
operating characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C

| PARAMETER       |                               | TEST CONDITIONS  |                                   | TYP | UNIT |
|-----------------|-------------------------------|------------------|-----------------------------------|-----|------|
| C <sub>pd</sub> | Power dissipation capacitance | Outputs enabled  | C <sub>L</sub> = 50 pF, f = 1 MHz | 39  | pF   |
|                 |                               | Outputs disabled |                                   | 11  |      |

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SCAS116B – MARCH 1990 – REVISED APRIL 1996

PARAMETER MEASUREMENT INFORMATION



- NOTES: A.  $C_L$  includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 1 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r = 3 \text{ ns}$ ,  $t_f = 3 \text{ ns}$ .
- D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup>    | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|------------------|-----------------------|--------------|-----------------|------|-------------|----------------------------|----------------------|------------------------------|-----------------------------|
| 5962-9202201MXA  | ACTIVE                | CFP          | WD              | 48   | 1           | TBD                        | Call TI              | Call TI                      |                             |
| 74ACT16244DGGR   | ACTIVE                | TSSOP        | DGG             | 48   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DGGRE4 | ACTIVE                | TSSOP        | DGG             | 48   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DGGRG4 | ACTIVE                | TSSOP        | DGG             | 48   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DL     | ACTIVE                | SSOP         | DL              | 48   | 25          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DLG4   | ACTIVE                | SSOP         | DL              | 48   | 25          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DLR    | ACTIVE                | SSOP         | DL              | 48   | 1000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| 74ACT16244DLRG4  | ACTIVE                | SSOP         | DL              | 48   | 1000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SNJ54ACT16244WD  | ACTIVE                | CFP          | WD              | 48   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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**TAPE AND REEL INFORMATION**
**REEL DIMENSIONS**

**TAPE DIMENSIONS**


|    |                                                           |
|----|-----------------------------------------------------------|
| A0 | Dimension designed to accommodate the component width     |
| B0 | Dimension designed to accommodate the component length    |
| K0 | Dimension designed to accommodate the component thickness |
| W  | Overall width of the carrier tape                         |
| P1 | Pitch between successive cavity centers                   |

**TAPE AND REEL INFORMATION**

\*All dimensions are nominal

| Device         | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|----------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| 74ACT16244DGGR | TSSOP        | DGG             | 48   | 2000 | 330.0              | 24.4               | 8.6     | 15.8    | 1.8     | 12.0    | 24.0   | Q1            |
| 74ACT16244DLR  | SSOP         | DL              | 48   | 1000 | 330.0              | 32.4               | 11.35   | 16.2    | 3.1     | 16.0    | 32.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS

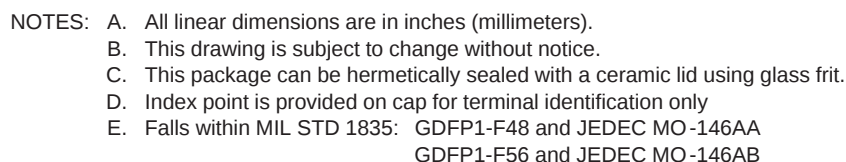


\*All dimensions are nominal

| Device         | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|----------------|--------------|-----------------|------|------|-------------|------------|-------------|
| 74ACT16244DGGR | TSSOP        | DGG             | 48   | 2000 | 367.0       | 367.0      | 45.0        |
| 74ACT16244DLR  | SSOP         | DL              | 48   | 1000 | 367.0       | 367.0      | 55.0        |

## CERAMIC DUAL FLATPACK

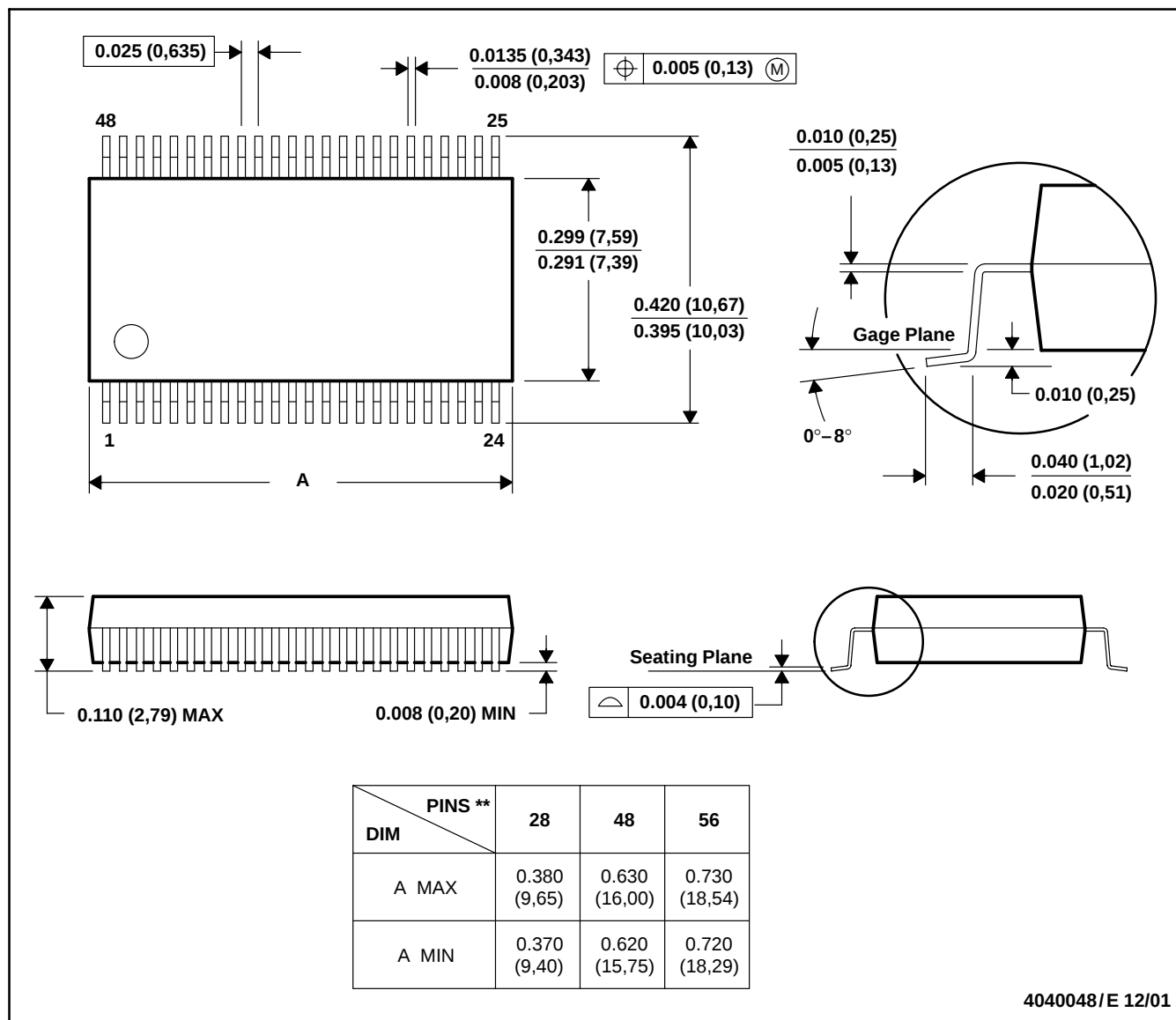
48 LEADS SHOWN



DL (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

48 PINS SHOWN



- NOTES: A. All linear dimensions are in inches (millimeters).  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).  
 D. Falls within JEDEC MO-118

## DGG (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE PACKAGE

48 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-153

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### Products

|                        |                                                                                      |
|------------------------|--------------------------------------------------------------------------------------|
| Audio                  | <a href="http://www.ti.com/audio">www.ti.com/audio</a>                               |
| Amplifiers             | <a href="http://amplifier.ti.com">amplifier.ti.com</a>                               |
| Data Converters        | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>                       |
| DLP® Products          | <a href="http://www.dlp.com">www.dlp.com</a>                                         |
| DSP                    | <a href="http://dsp.ti.com">dsp.ti.com</a>                                           |
| Clocks and Timers      | <a href="http://www.ti.com/clocks">www.ti.com/clocks</a>                             |
| Interface              | <a href="http://interface.ti.com">interface.ti.com</a>                               |
| Logic                  | <a href="http://logic.ti.com">logic.ti.com</a>                                       |
| Power Mgmt             | <a href="http://power.ti.com">power.ti.com</a>                                       |
| Microcontrollers       | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a>                   |
| RFID                   | <a href="http://www.ti-rfid.com">www.ti-rfid.com</a>                                 |
| OMAP Mobile Processors | <a href="http://www.ti.com/omap">www.ti.com/omap</a>                                 |
| Wireless Connectivity  | <a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a> |

### Applications

|                               |                                                                                          |
|-------------------------------|------------------------------------------------------------------------------------------|
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