

TPDV640 ---> TPDV1240

ALTERNISTORS

FEATURES

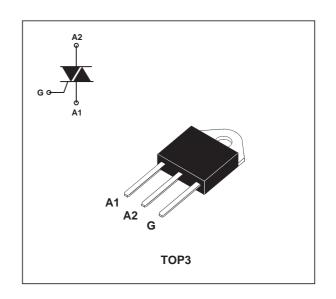
■ High commutation: > 142A/ms (400Hz)

■ Insulating voltage = 2500V_(RMS) (UL Recognized: EB81734)

■ High voltage capability: V_{DRM} = 1200V

DESCRIPTION

The TPDV640 ---> TPDV1240 use a high performance passivated glass alternistor technology. Featuring very high commutation levels and high surge current capability, this family is well adapted to power control on inductive load (motor, transformer...)



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter	Value	Unit	
I _{T(RMS)}	RMS on-state current (360° conduction angle)	Tc = 75°C	40	Α
I _{TSM}	Non repetitive surge peak on-state current			Α
	(Tj initial = 25°C)			
		tp = 10ms	350	
l ² t	I ² t value	tp = 10ms	610	A ² s
dl/dt	Critical rate of rise of on-state current Gate supply: I _G = 500mA dI _G /dt = 1A/µs			A/µs
	Non repetitive		100	
Tstg Tj	Storage and operating junction temperature range			°C
TI	Maximum lead soldering temperature during 10s at 4.5mm from	260	°C	

Symbol	Parameter	TPDV				l lmit
Symbol	Parameter	640	840	1040	1240	Unit
V _{DRM} V _{RRM}	Repetitive peak off-state voltage Tj = 125°C	600	800	1000	1200	V

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THERMAL RESISTANCES

Symbol	Parameter		Unit
Rth (j-a)	Contact to ambient	50	°C/W
Rth (j-c) DC	Junction to case for DC	1.2	°C/W
Rth (j-c) AC	Junction to case for 360° conduction angle (F = 50Hz)	0.9	°C/W

GATE CHARACTERISTICS (maximum values)

 $P_{G(AV)} = 1W \quad P_{GM} = 40W \; (tp = 20 \mu s) \quad I_{GM} = 8A \; (tp = 20 \mu s) \quad V_{GM} = 16V \; (tp = 20 \mu s)$

ELECTRICAL CHARACTERISTICS

Symbol	Test conditions		Quadrant		Value	Unit
I _{GT}	$V_D = 12V (DC)$ $R_L = 33\Omega$	Tj = 25°C	1 - 11 - 111	MAX.	200	mA
V _{GT}	$V_D = 12V (DC)$ $R_L = 33\Omega$	Tj = 25°C	1 - 11 - 111	MAX.	1.5	V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3k\Omega$	Tj =125°C	1 - 11 - 111	MIN.	0.2	V
tgt	$V_D = V_{DRM}$ $I_G = 500$ mA $dI_G/dt = 3$ A/ μ s	Tj = 25°C	1 - 11 - 111	TYP.	2.5	μs
IL	$I_G = 1.2I_{GT}$	Tj = 25°C	1 - 111	TYP.	100	mA
			II		200	
I _H *	I _T = 500mA Gate open	Tj = 25°C		TYP.	50	mA
V _{TM} *	I _{TM} = 60A tp = 380μs	Tj = 25°C		MAX.	1.8	V
I _{DRM}	V _{DRM} rated	Tj = 25°C		MAX.	0.02	mA
I _{RRM}	V _{RRM} rated	Tj = 125°C		MAX.	8	
dV/dt *	Linear slope up to $V_D = 67\% \ V_{DRM}$ gate open	Tj = 125°C		MIN.	500	V/µs
(dl/dt)c*	(dV/dt)c = 200V/μs	Tj = 125°C		MIN.	35	A/ms
	$(dV/dt)c = 10V/\mu s$				142	

^{*} For either polarity of electrode A₂ voltage with reference to electrode A₁.

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Fig. 1: Maximum RMS power dissipation versus RMS on-state current (F = 50Hz).(Curves are cut off by (dl/dt)c limitation)

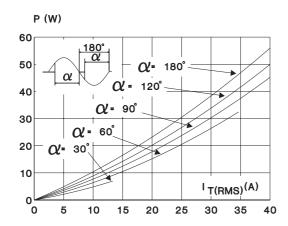


Fig. 3: RMS on-state current versus case temperature.

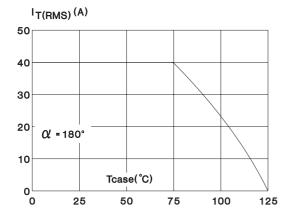


Fig. 5: Relative variation of gate trigger current and holding current versus junction temperature.

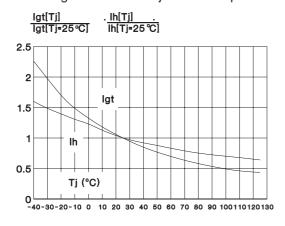


Fig. 2: Correlation between maximum RMS power dissipation and maximum allowable temperatures (Tamb and Tcase) for different thermal resistances heatsink + contact.

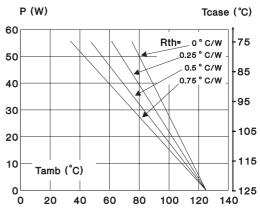


Fig. 4: Relative variation of thermal impedance versus pulse duration.

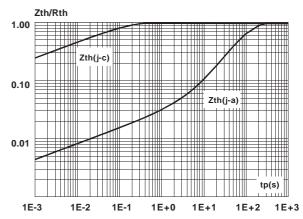
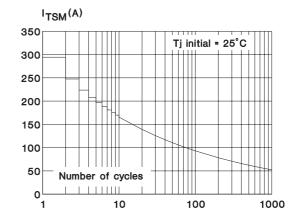


Fig. 6: Non repetitive surge peak on-state current versus number of cycles.



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Fig. 7: Non repetitive surge peak on-state current for a sinusoidal pulse with width: $t \le 10$ ms, and corresponding value of l^2t .

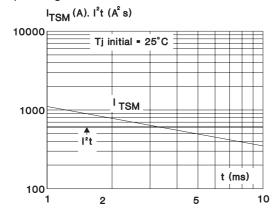


Fig. 8: On-state characteristics (maximum values).

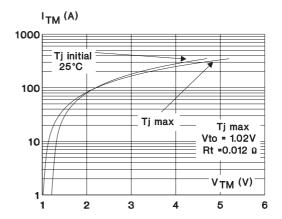
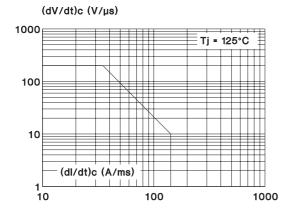


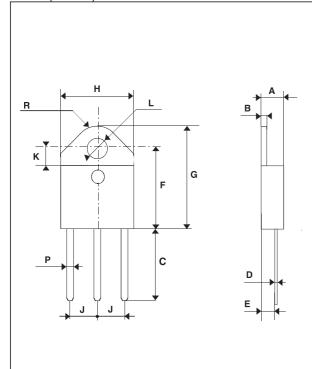
Fig. 9: Safe operating area.



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PACKAGE MECHANICAL DATA

TOP3 (Plastic)



	DIMENSIONS			
REF.	Millimeters		Inches	
	Min.	Max.	Min.	Max.
А	4.4	4.6	0.173	0.181
В	1.45	1.55	0.057	0.061
С	14.35	15.60	0.565	0.614
D	0.5	0.7	0.020	0.028
Е	2.7	2.9	0.106	0.114
F	15.8	16.5	0.622	0.650
G	20.4	21.1	0.815	0.831
Н	15.1	15.5	0.594	0.610
J	5.4	5.65	0.213	0.222
K	3.4	3.65	0.134	0.144
L	4.08	4.17	0.161	0.164
Р	2 1.20 1.4		0.047	0.055
R	4.60 Typ.		0.181 Typ.	

OTHER INFORMATION

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
TPDVx40	TPDVx40	TOP3	4.5 g	120	Bulk

- Epoxy meets UL94,V0
- Cooling method: C
- Recommended torque value: 0.8 m.N.
- Maximum torque value: 1 m.N.

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