

# Low Capacitance Protection Device

## RSB12Z

### ●Applications

ESD Protection

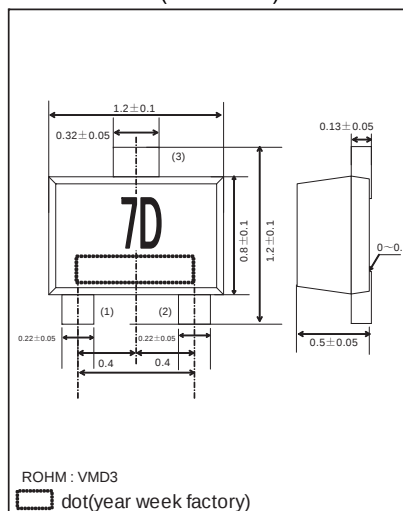
### ●Features

- 1) Ultra small mold type.(VMD3)
- 2) Low capacitance
- 3) Bi direction

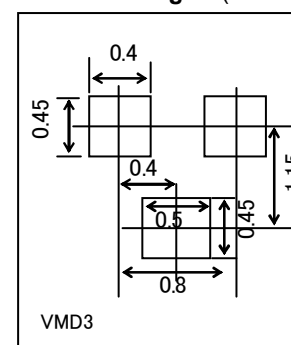
### ●Construction

Silicon epitaxial planer

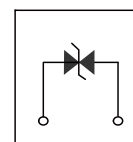
### ●Dimensions(Unit : mm)



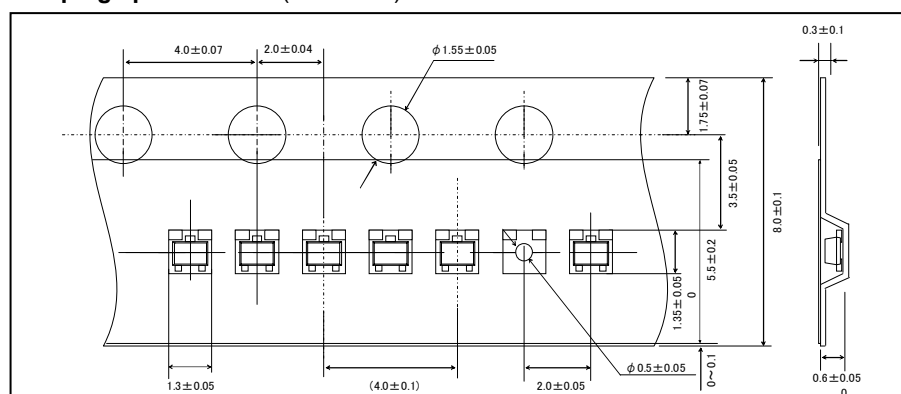
### ●Land size figure(Unit : mm)



### ●Structure



### ●Taping specifications(Unit : mm)



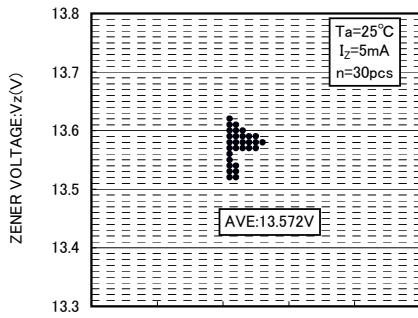
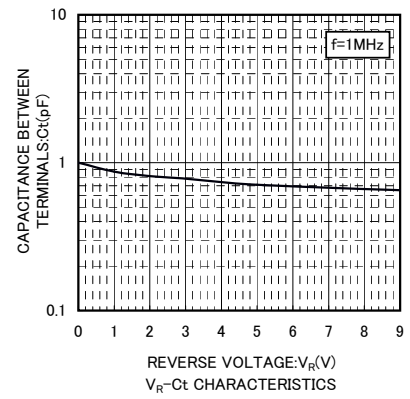
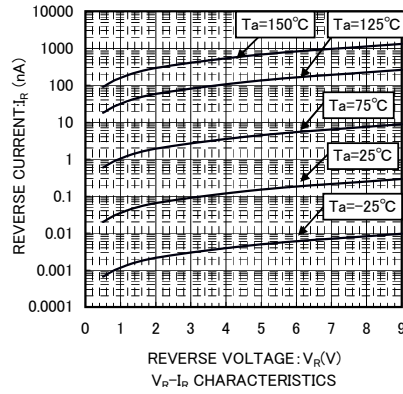
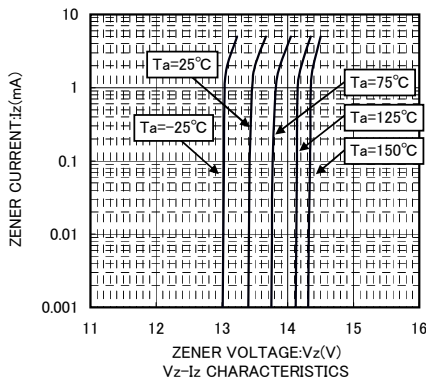
### ●Absolute maximum ratings(Ta=25°C)

| Parameter            | Symbol           | Limits      | Unit |
|----------------------|------------------|-------------|------|
| Power dissipation    | P                | 150         | mW   |
| Junction temperature | T <sub>j</sub>   | 150         | °C   |
| Storage temperature  | T <sub>stg</sub> | -55 to +150 | °C   |

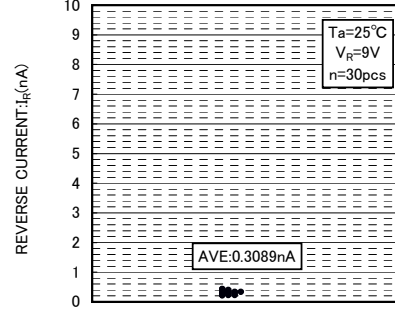
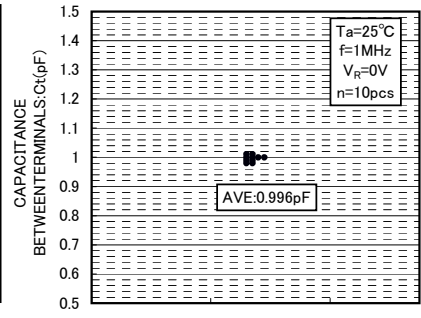
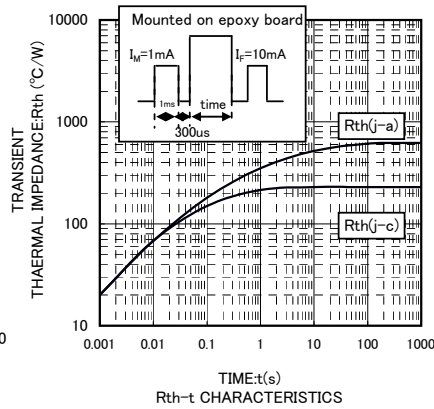
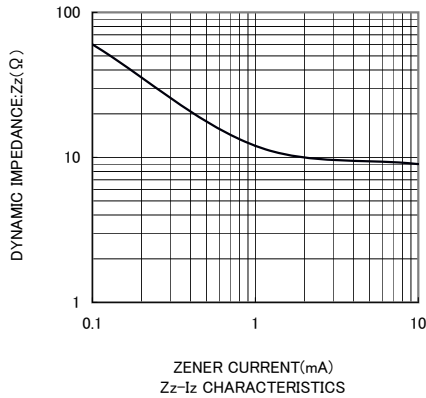
### ●Electical characteristics(Ta=25°C)

| Parameter                     | Symbol         | Min. | Typ. | Max. | Unit | Conditions                 |
|-------------------------------|----------------|------|------|------|------|----------------------------|
| Zener voltage                 | V <sub>Z</sub> | 9.6  | -    | 14.4 | V    | I <sub>Z</sub> =5mA        |
| Reverse current               | I <sub>R</sub> | -    | -    | 0.10 | μA   | V <sub>R</sub> =9V         |
| Capacitance between terminals | C <sub>t</sub> | -    | 1    | -    | pF   | f=1MHz, V <sub>R</sub> =0V |

# ●Electrical characteristics curve



Vz DISRESON MAP

 $I_R$  DISRESON MAP $C_t$  DISRESON MAP

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