-100mA / -50V Digital transistor (with built-in resistor)

DTA113TKA

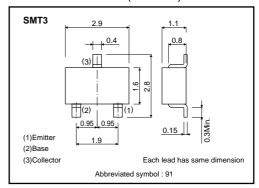
Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on/ off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

●External dimensions (Unit: mm)



●Structure

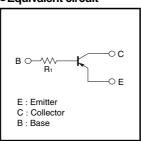
PNP epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTA113TKA		0

●Equivalent circuit

R₁=1kΩ



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	Vceo	-50	V
Emitter-base voltage	VEBO	−5 to +10	V
Collector current	lc	-100	mA
Collector Power dissipation	Pc	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	−55 to +150	°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	_	_	V	Ic= -50μA
Collector-emitter breakdown voltage	BVceo	-50	_	_	V	Ic=-1mA
Emitter-base breakdown voltage	ВУево	-5	_	-	V	I _E = -50μA
Collector cutoff current	Ісво	_	_	-0.5	μΑ	V _{CB} = -50V
Emitter cutoff current	Ієво	_	_	-0.5	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VcE(sat)	_	_	-0.3	V	Ic/I _B = -5mA / -0.25mA
DC current transfer ratio	hfe	100	250	600	_	Ic=-1mA , Vc==-5V
Input resistance	R ₁	0.7	1	1.3	kΩ	-
Transition frequency	f ⊤ *	_	250	_	MHz	Vcв= −10V , I∈=5mA , f=100MHz

^{*} Characteristics of built-in transistor

•Electrical characteristics curves

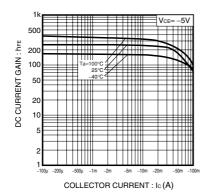


Fig.1 DC Current gain vs. Collector Current

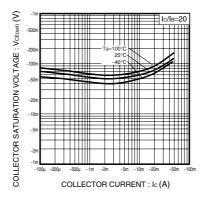


Fig.2 Collector-emitter saturation voltage vs. Collector Current

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