One Watt Amplifier Transistors

PNP Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector - Emitter Voltage	MPS6726 MPS6727	V _{CEO}	-30 -40	Vdc
Collector - Base Voltage	MPS6726 MPS6727	V _{CBO}	-40 -50	Vdc
Emitter – Base Voltage		V _{EBO}	-5.0	Vdc
Collector Current – Continuous		I _C	-1.0	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C		P _D	1.0 8.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C		P _D	2.5 20	W mW/°C
Operating and Storage Junction Temperature Range		T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

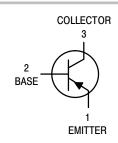
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



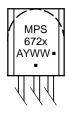
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MARKING DIAGRAM



MPS672x = Device Code

x = 6 or 7

A = Assembly Location

Y = Year
WW = Work Week
Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MPS6726	TO-92	5000 Units / Bulk
MPS6726G	TO-92 (Pb-Free)	5000 Units / Bulk
MPS6727	TO-92	5000 Units / Bulk
MPS6727G	TO-92 (Pb-Free)	5000 Units / Bulk

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage $(I_C = -10 \text{ mAdc}, I_B = 0)$	MPS6726 MPS6727	V _{(BR)CEO}	-30 -40	_ _	Vdc
Collector – Base Breakdown Voltage ($I_C = -100 \mu Adc$, $I_E = 0$)	MPS6726 MPS6727	V _{(BR)CBO}	-40 -50	_ _	Vdc
Emitter – Base Breakdown Voltage ($I_E = -100 \mu Adc, I_C = 0$)		V _{(BR)EBO}	-5.0	_	Vdc
Collector Cutoff Current $(V_{CB} = -40 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -50 \text{ Vdc}, I_E = 0)$	MPS6726 MPS6727	I _{CBO}	- -	-0.1 -0.1	μAdc
Emitter Cutoff Current (V _{EB} = -5.0 Vdc, I _C = 0)		I _{EBO}	_	-0.1	μAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain $ \begin{aligned} &(I_C = -100 \text{ mAdc, } V_{CE} = -1.0 \text{ Vdc}) \\ &(I_C = -1000 \text{ mAdc, } V_{CE} = -1.0 \text{ Vdc}) \end{aligned} $		h _{FE}	60 50	_ 250	-
Collector – Emitter Saturation Voltage (I _C = –1000 mAdc, I _B = –100 mAdc)		V _{CE(sat)}	_	-0.5	Vdc
Base – Emitter On Voltage (I _C = –1000 mAdc, V _{CE} = –1.0 Vdc)		V _{BE(on)}	_	-1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	•	•
Collector–Base Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz)		C _{cb}	_	30	pF
Small–Signal Current Gain (I _C = -50 mAdc, V _{CE} = -10 Vdc, f = 20 MHz)		h _{fe}	2.5	25	-

^{1.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.

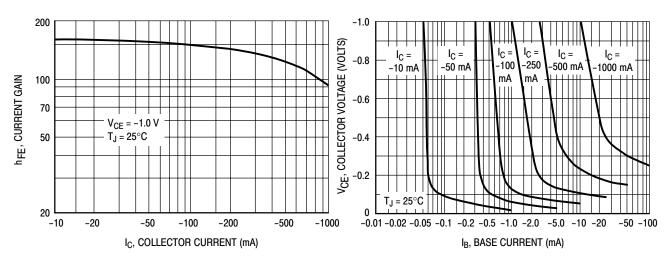


Figure 1. DC Current Gain

Figure 2. Collector Saturation Region

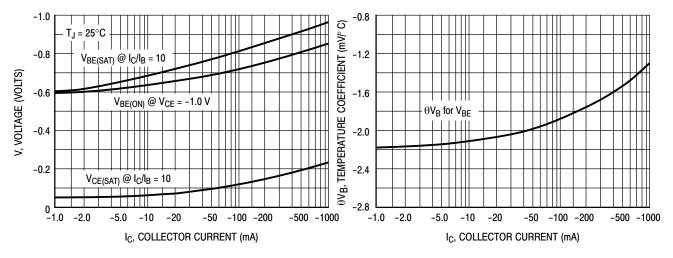


Figure 3. "ON" Voltages

Figure 4. Temperature Coefficient

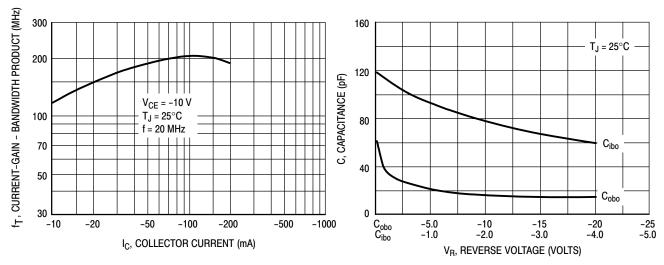


Figure 5. Current Gain — Bandwidth Product

Figure 6. Capacitance

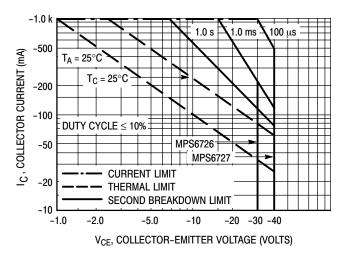
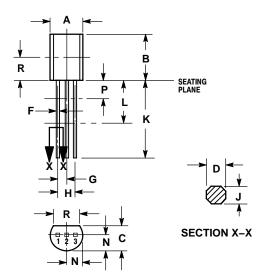


Figure 7. Active Region — Safe Operating Area

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L
 DIMENSIONS D AND J APPLY BETWEEN L AND K
 MIMIMUM. LEAD DIMENSION IS UNCONTROLLED
 IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.135		3 43	

STYLE 1:

PIN 1. EMITTER

2. BASE3. COLLECTOR

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