

CR05AS-8

Thyristor

Low Power Use

R07DS0135EJ0500

(Previous: REJ03G0348-0400)

Rev.5.00

Sep 15, 2010

Features

 $\begin{array}{ll} \bullet & I_{T \, (AV)} : 0.5 \; A \\ \bullet & V_{DRM} : 400 \; V \\ \bullet & I_{GT} : 100 \; \mu A \end{array}$

• Non-Insulated Type

• Planar Passivation Type

Surface Mounted type

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)



RENESAS Package code: PLZZ0004CB-A (Package name: SOT-89)





1. Cathode

Cathode
 Anode

Gate
 Anode

Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

Maximum Ratings

Parameter	Cumbal	Voltage class	Unit	
Parameter	Symbol	8 (Mark CD)		
Repetitive peak reverse voltage	V_{RRM}	400	V	
Non-repetitive peak reverse voltage	V_{RSM}	500	V	
DC reverse voltage	V _{R (DC)}	320	V	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	400	V	
DC off-state voltage ^{Note1}	V _{D (DC)}	320	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.79	Α	
Average on-state current	I _{T (AV)}	0.5	А	Commercial frequency, sine half wave 180° conduction, Ta = 57°C ^{Note2}
Surge on-state current	I _{TSM}	10	Α	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	0.1	W	
Average gate power dissipation	P _{G (AV)}	0.01	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.1	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \text{ k}\Omega$.

Electrical Characteristics

Parameter	Symbol	Rated value			Unit	Test conditions	
Farameter	Symbol	Min.	Тур.	Max.	Onit	rest conditions	
Repetitive peak reverse current	I _{RRM}	_	_	0.1	mA	Tj = 125°C, V _{RRM} applied	
Repetitive peak off-state current	I _{DRM}	_	_	0.1	mA	$Tj = 125$ °C, V_{DRM} applied, $R_{GK} = 1$ kΩ	
On-state voltage	V _{TM}	_	_	1.9	V	Ta = 25°C, I _{TM} = 1.5 A, instantaneous value	
Gate trigger voltage	V _{GT}	_	_	0.8	V	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A ^{Note4}	
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$, $R_{GK} = 1 k\Omega$	
Gate trigger current	I _{GT}	20	_	100 ^{Note3}	μΑ	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note4}$	
Holding current	I _H	_	_	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k Ω	
Thermal resistance	R _{th (j-a)}	_	_	70	°C/W	Junction to ambient ^{Note2}	

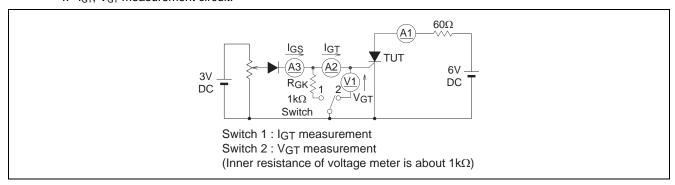
Notes: 2. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).

3. If special values of I_{GT} are required, choose item E from those listed in the table below if possible.

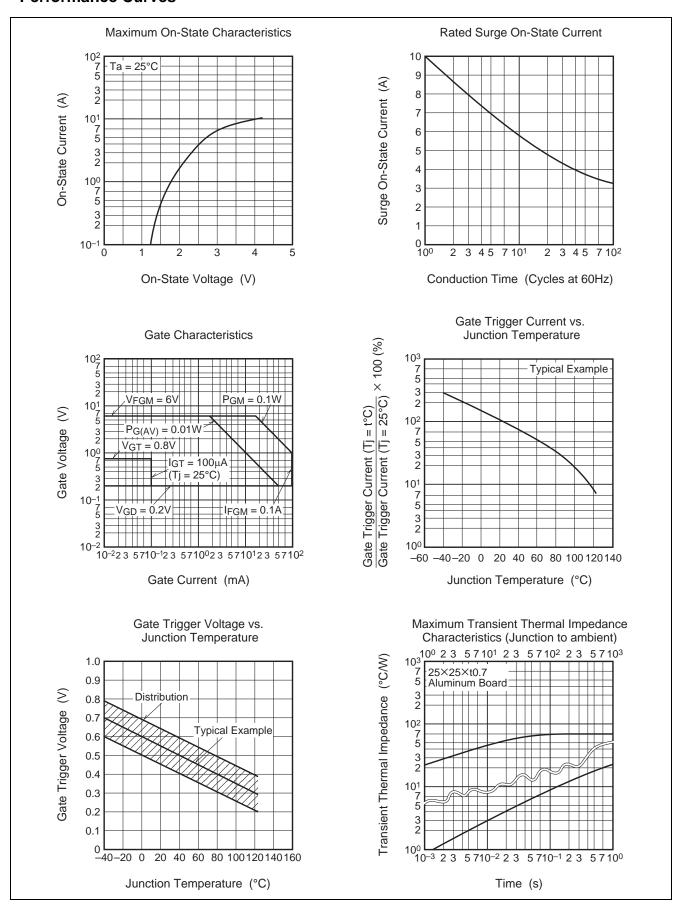
Item	В	E
I _{GT} (μA)	20 to 50	20 to 100

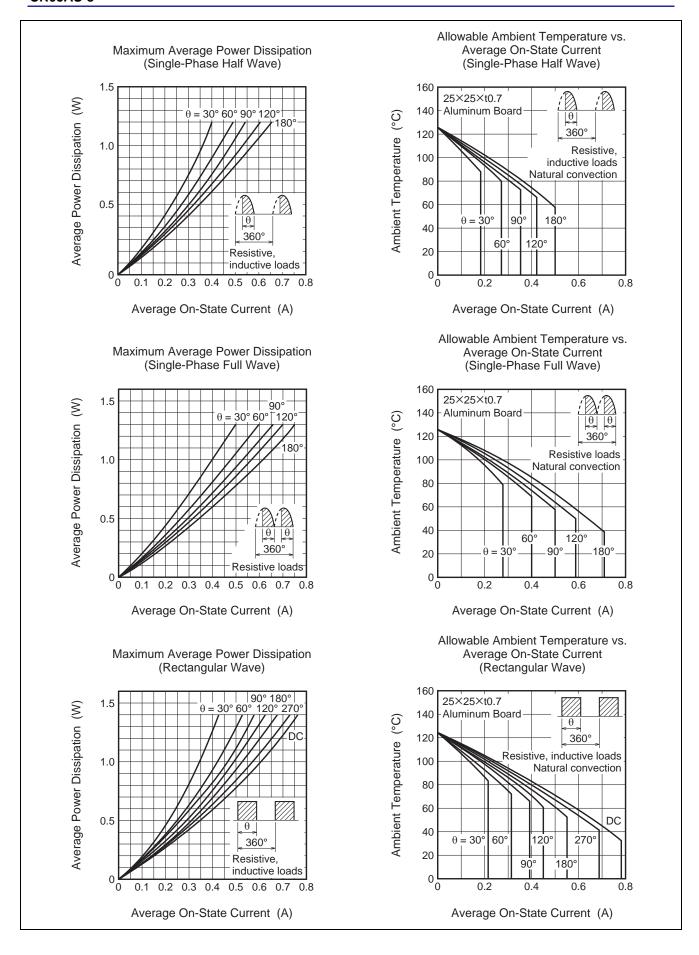
The above values do not include the current flowing through the 1 k Ω resistance between the gate and cathode.

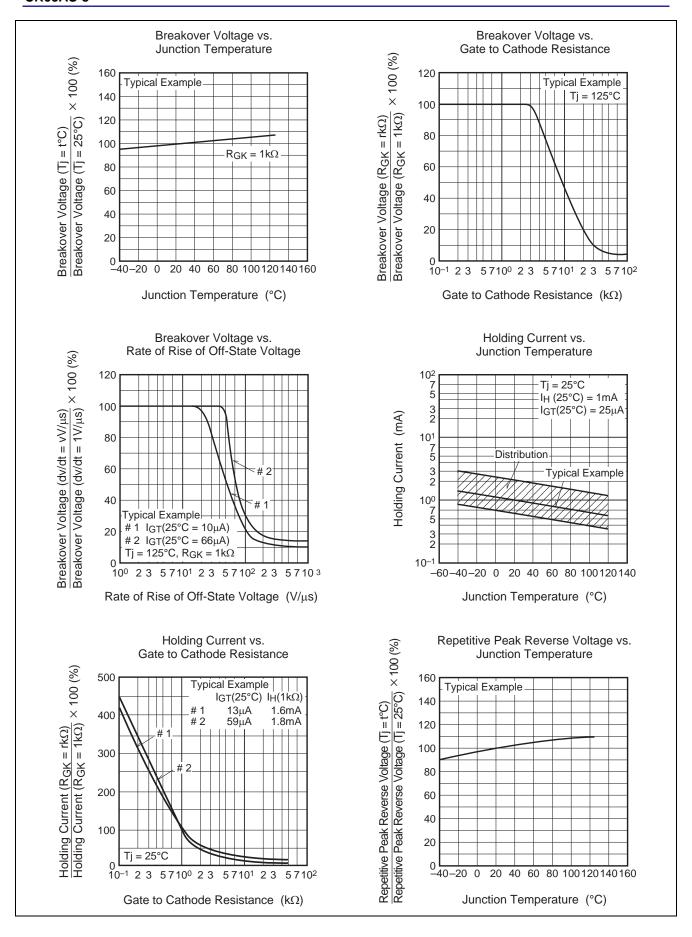
4. I_{GT} , V_{GT} measurement circuit.

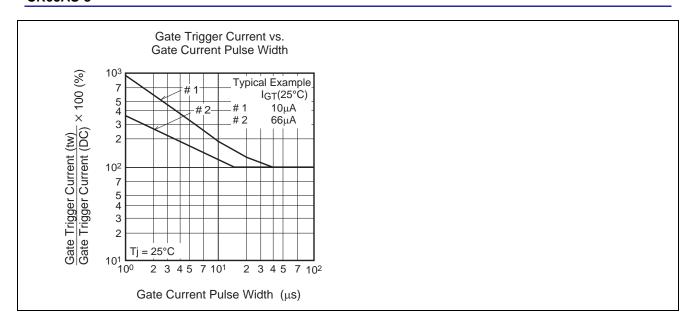


Performance Curves

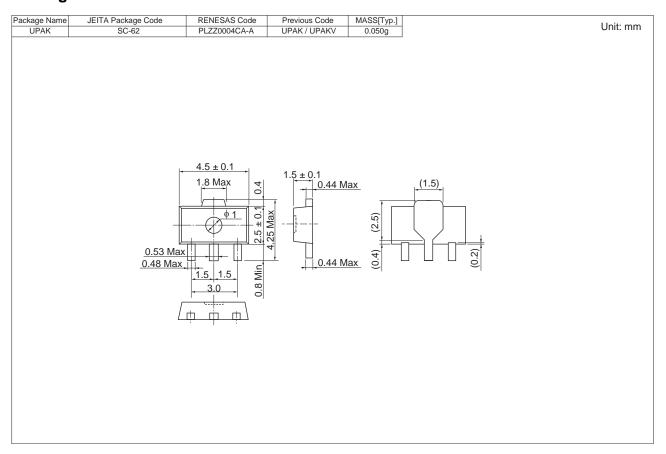


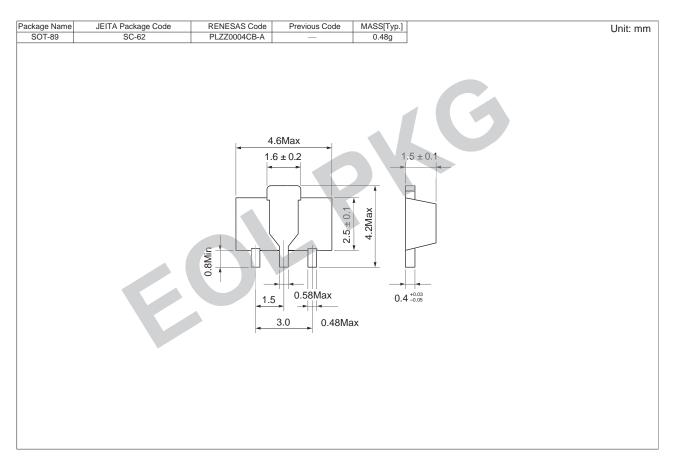






Package Dimensions





Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	4000	Type name – ET +Direction (1 or 2) + 4	CR05AS-8-ET14

Note: Please confirm the specification about the shipping in detail.

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