R07DS1422EJ0200

Rev.2.00

Dec. 12, 2018

(Previous: REJ03G1806-0100)



# BCR3AM-14B

700V - 3A - Triac

Low Power Use

#### **Features**

•  $I_{T (RMS)}$ : 3 A (non-continuous)

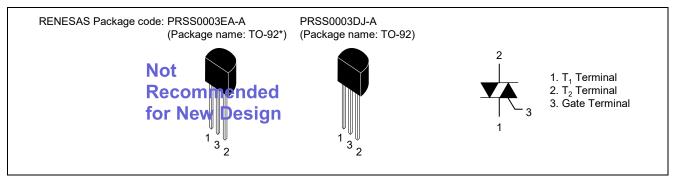
•  $V_{DRM}$ : 800 V (Tj = 125 °C)

 $\bullet \quad I_{FGTI},\,I_{RGTI},\,I_{RGT\,III}{:}\,\,30\,\,mA$ 

• Tj: 150 °C

• Planar Passivation Type

#### **Outline**



#### **Application**

Non-continuous Motor control and other general purpose non-continuous AC control applications.

#### **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit	Conditions
		14		
Repetitive peak off-state voltage <sup>Note1</sup>	$V_{DRM}$	800	V	Tj=125°C
		700	V	Tj=150°C
Non-repetitive peak off-state voltage <sup>Note1</sup>	$V_{DSM}$	840	V	

Notes: 1. Gate open.

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T (RMS)</sub>	3	Α	Commercial frequency, sine full wave
				360°conduction, non-continuous
Surge on-state current	I <sub>TSM</sub>	30	Α	60 Hz sinewave 1 full cycle, peak value,
				non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	3.7	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave
				60 Hz, surge on-state current
Peak gate power dissipation	P <sub>GM</sub>	3	W	
Average gate power dissipation	P <sub>G (AV)</sub>	0.3	W	
Peak gate voltage	V <sub>GM</sub>	6	V	
Peak gate current	l <sub>GM</sub>	0.5	Α	
Junction Temperature	Tj	-40 to +150	°C	
Storage temperature	Tstg	-40 to +150	°C	

#### **Electrical Characteristics**

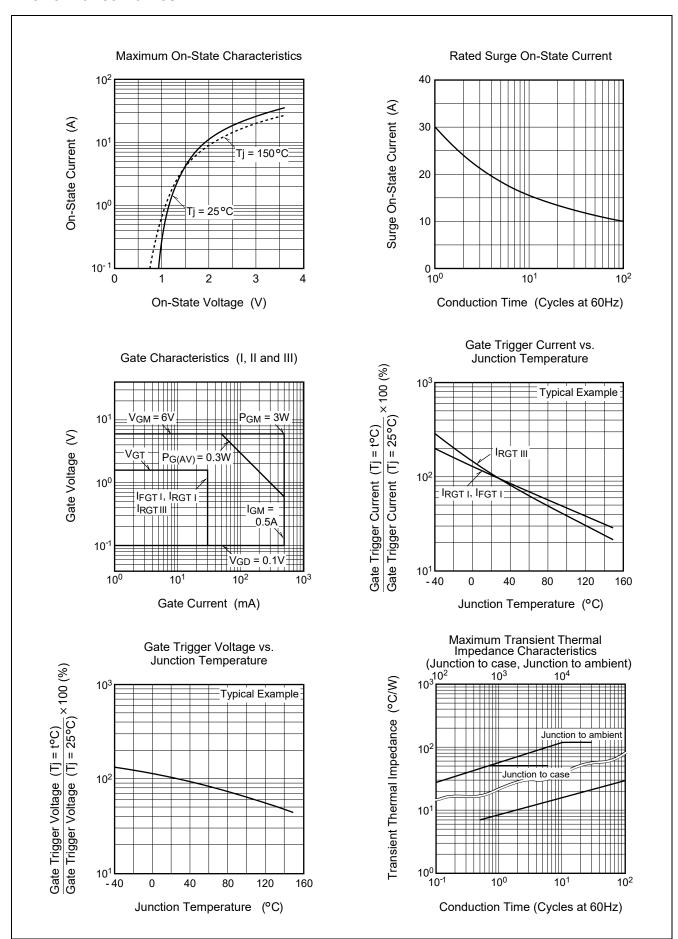
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state cur	rent	I <sub>DRM</sub>	_	_	2.0	mA	Tj = 150°C, V <sub>DRM</sub> applied
On-state voltage		V <sub>TM</sub>	_	_	1.6	٧	Tc = 25°C, I <sub>TM</sub> = 4.5 A, instantaneous measurement
Gate trigger voltageNote2	I	V <sub>FGTI</sub>	_	_	1.5	V	Tj = 25°C, $V_D$ = 6 V, $R_L$ = 6 Ω,
	II	$V_{RGTI}$	_	_	1.5	V	$R_G = 330 \Omega$
	III	$V_{RGTIII}$	_	_	1.5	V	
Gate trigger currentNote2	I	I <sub>FGTI</sub>	_	_	30	mA	Tj = 25°C, $V_D$ = 6 V, $R_L$ = 6 Ω,
	II	I <sub>RGTI</sub>	_	_	30	mA	$R_G = 330 \Omega$
	III	I <sub>RGTIII</sub>	_	_	30	mA	
Gate non-trigger voltage		$V_{GD}$	0.2	_	_	V	Tj = 125°C, V <sub>D</sub> = 1/2 V <sub>DRM</sub>
		•	0.1	_	_		Tj = 150°C, V <sub>D</sub> = 1/2 V <sub>DRM</sub>
Thermal resistance		Rth (j-c)	_	_	50	°C/W	Junction to case <sup>Note3</sup>
Critical-rate of rise of off-state		(dv/dt)c	5	_	_	V/μs	Tj = 125°C
commutating voltage <sup>Note4</sup>			1	_	_		Tj = 150°C

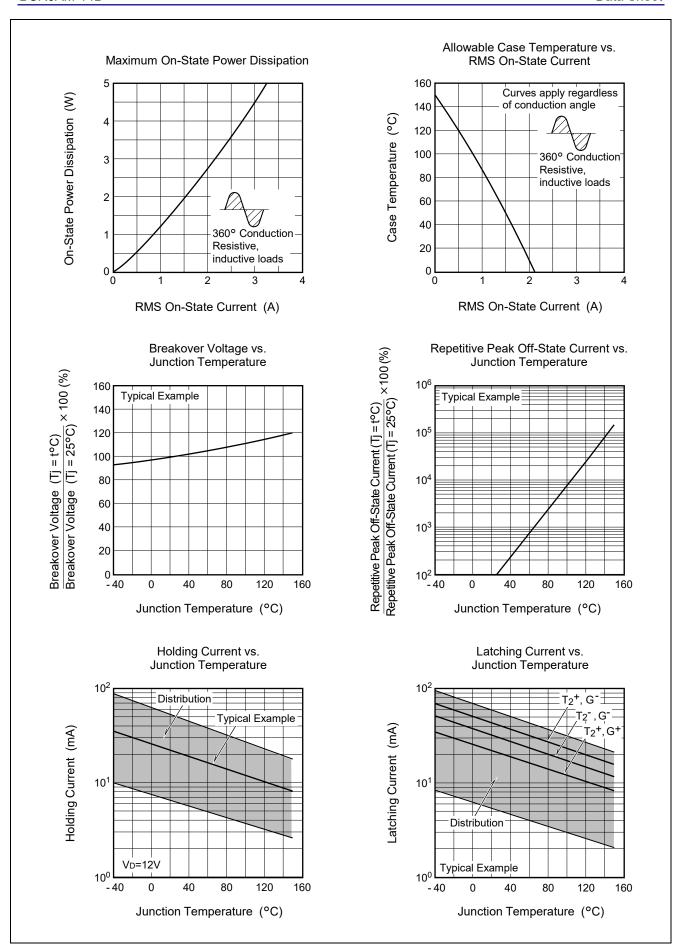
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

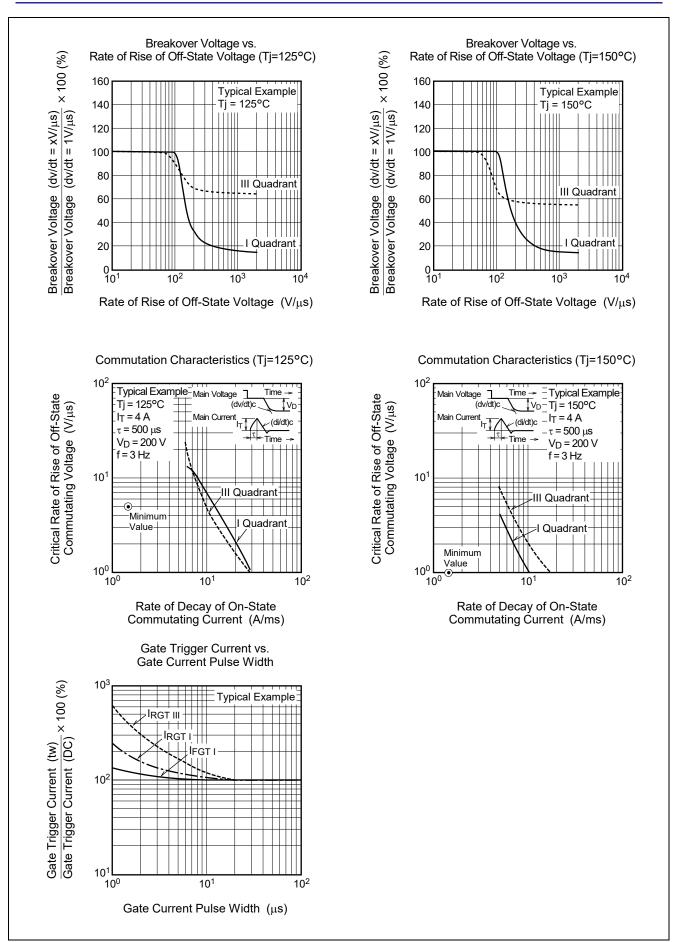
- 3. Case temperature is measured at the  $T_2$  terminal 1.5 mm away from the molded case.
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
<ol> <li>Junction temperature</li> <li>Tj = 125°C/150°C</li> <li>Rate of decay of on-state commutating current (di/dt)c = - 1.5 A/ms</li> <li>Peak off-state voltage</li> <li>V<sub>D</sub> = 400 V</li> </ol>	Supply Voltage  Main Current  Main Voltage  (di/dt)c  Time  Main Voltage  (dv/dt)c

#### **Performance Curves**



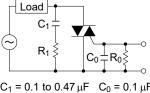




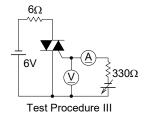
#### Gate Trigger Characteristics Test Circuits

## 6Ω 330Ω 330Ω Test Procedure I Test Procedure II

#### Recommended peripheral components for Triac



 $\begin{array}{ll} C_1 = 0.1 \text{ to } 0.47 \, \mu\text{F} & C_0 = 0.1 \, \mu\text{F} \\ R_1 = 47 \text{ to } 100 \Omega & R_0 = 100 \, \Omega \end{array}$ 

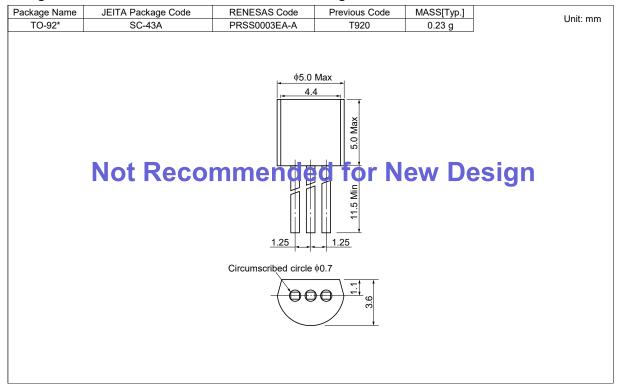


6Ω

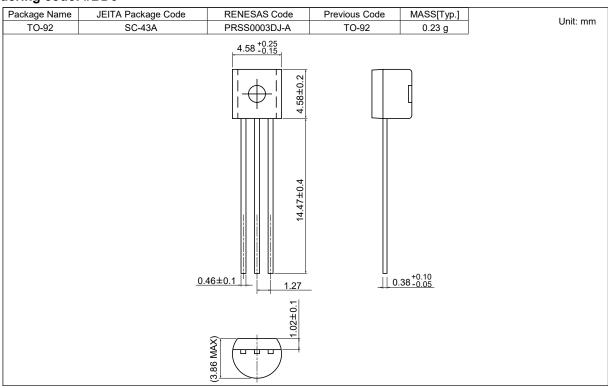
6V

### **Package Dimensions**

#### Ordering code: #B00 <Not Recommended for New Design>



#### Ordering code: #BD0



### **Ordering Information**

Orderable Part Number	Package	Packing Note5	Quantity	Remark
BCR3AM-14B#B00	TO-92*	Plastic Bag	500 pcs.	Straight type, NRND
BCR3AM-14B-A6#B00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form, NRND
BCR3AM-14B#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, Halogen-free
BCR3AM-14B-A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form, Halogen-free

Note: 5. Please confirm the specification about the shipping in detail.

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