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April 1st, 2010 Renesas Electronics Corporation

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HD74HC540, HD74HC541

Octal Buffers/Line Drivers (with 3-state outputs)

REJ03D0628-0200 (Previous ADE-205-508) Rev.2.00 Mar 30, 2006

Description

The HD74HC540 is an inverting buffer and the HD74HC541 is a non-inverting buffer. The 3-state control gate operates as a two-input NOR such that if either G_1 or G_2 are high, all eight outputs are in the high-impedance state.

Features

High Speed Operation: t_{pd} = 11.5 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC540P HD74HC541P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74HC540FPEL HD74HC541FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HC540RPEL HD74HC541RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)
HD74HC540TELL HD74HC541TELL	TSSOP-20 pin	PTSP0020JB-A (TTP-20DAV)	Т	ELL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

	Inputs	Output Y				
G ₁	G ₂	Α	A HD74HC540			
L	L	L	Н	L		
L	L	Н	L	Н		
Н	X	X	Z	Z		
Х	Н	Х	Z	Z		

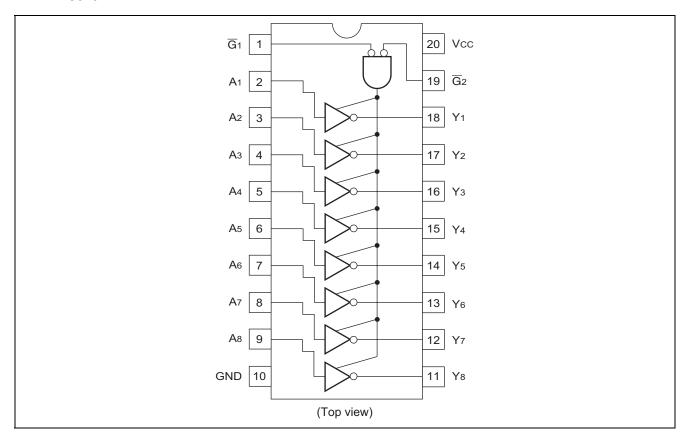
H: high levelL: low levelX: irrelevant

Z : off (high-impedance) state of a 3-state output

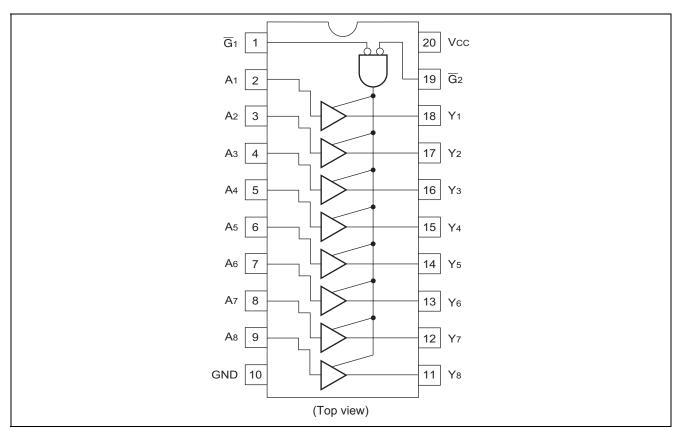


Pin Arrangement

HD74HC540



HD74HC541



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	–0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	Io	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		$V_{CC} = 2.0 \text{ V}$
Input rise / fall time*1	t _r , t _f	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

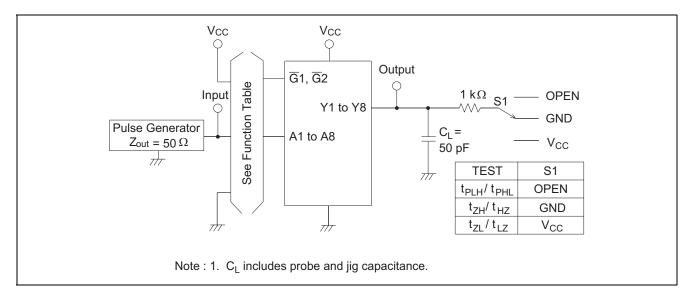
Electrical Characteristics

			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	V _{IL}	2.0	_	_	0.3	_	0.3	V		
		4.5	_	_	1.35	_	1.35			
		6.0	_	_	1.8	_	1.8			
Hysteresis voltage	V _H	2.0	_	0.1	_	_	_	V		
		4.5	_	0.4	_	_	_			
		6.0	_	0.4	_	_	_			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	_	_	4.13	_			$I_{OH} = -6 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -7.8 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	1	_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$
		6.0	_	_	0.26	_	0.33			$I_{OL} = 7.8 \text{ mA}$
Off-state output current	l _{OZ}	6.0	-	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} \text{ or } V_{IL},$ $Vout = V_{CC} \text{ or } G$	ND
Input current	lin	6.0		_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0	_		4.0	_	40	μΑ	$Vin = V_{CC}$ or GN	ID, lout = $0 \mu A$

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

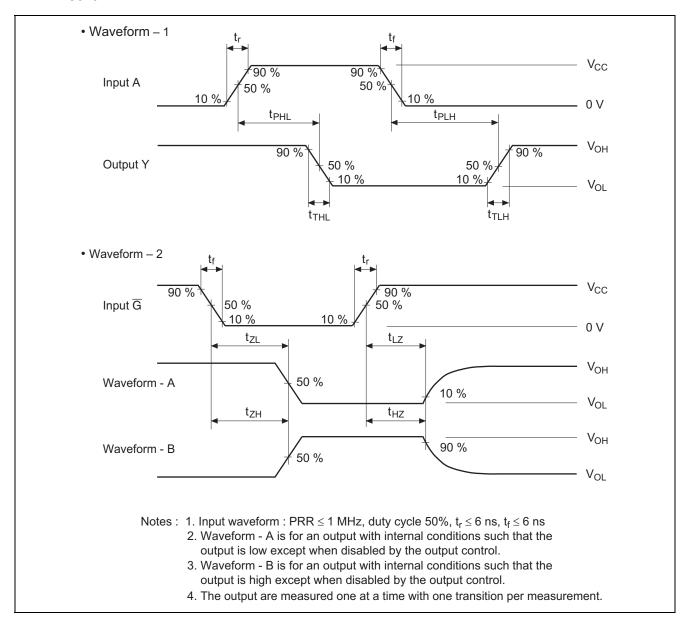
			Т	a = 25°	С	Ta = -40	to +85°C		
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	100	_	125	ns	(HD74HC540 only)
time	t _{PHL}	4.5	_	11	20	_	25		
		6.0	_	_	17	_	21		
	t _{PLH}	2.0	_	_	115	_	145	ns	(HD74HC541 only)
	t _{PHL}	4.5	_	12	23	_	29		
		6.0	_	_	20	_	25		
Output enable	t _{ZH}	2.0	_	_	150	_	190	ns	
time	t_{ZL}	4.5	_	14	30	_	38		
		6.0	_	_	26	_	33		
Output disable	t _{HZ}	2.0	_	_	150	_	190	ns	
time	t_{LZ}	4.5	_	16	30	_	38		
		6.0	_	_	26	_	33		
Output rise/fall	t _{TLH}	2.0		_	60		75	ns	
time	t _{THL}	4.5		4	12		15		
		6.0		_	10		13		
Input capacitance	Cin	_	_	5	10	_	10	pF	

Test Circuit

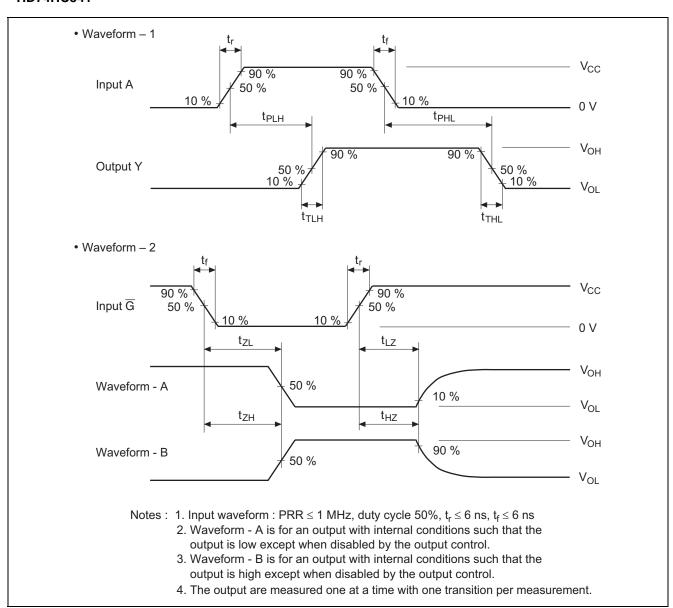


Waveforms

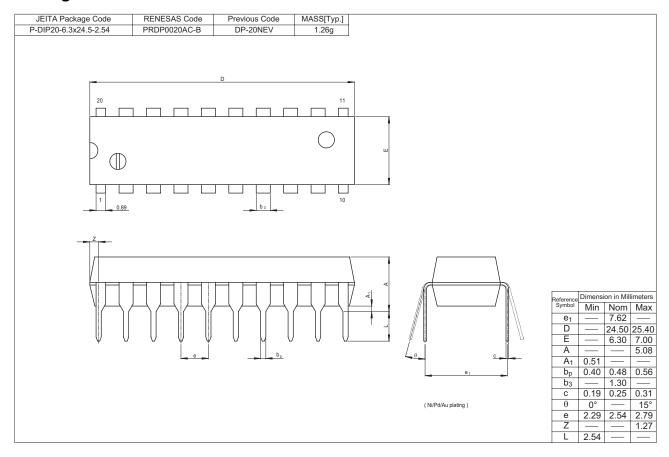
HD74HC540

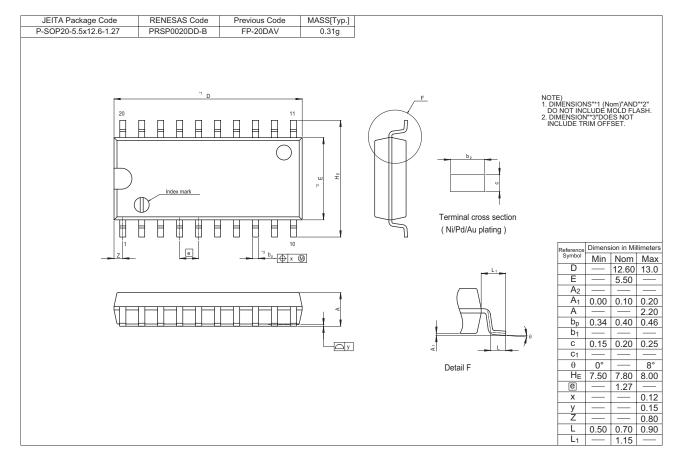


HD74HC541

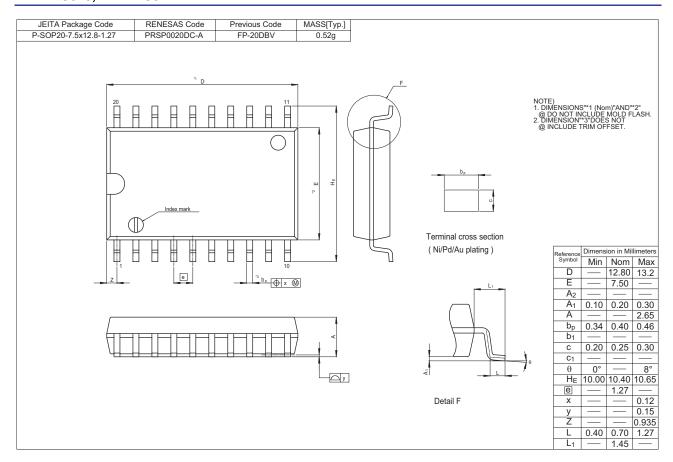


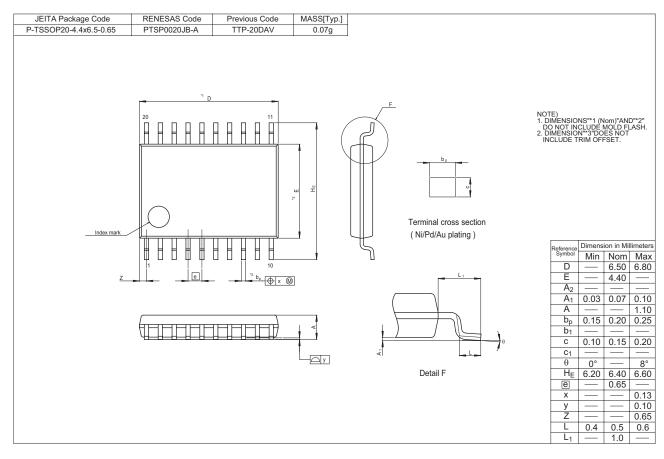
Package Dimensions





HD74HC540, HD74HC541





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