

BCR3AS-12A

Triac

Low Power Use

REJ03G0288-0400 Rev.4.00 Dec 19, 2008

Features

I_{T(RMS)}: 3 A
 V_{DRM}: 600 V

 $\bullet \quad I_{FGT\ I},\,I_{RGT\ I},\,I_{RGT\ III}:15\ mA$

- Non-Insulated Type
- Planar Passivation Type

Outline

RENESAS Package code: PRSS0004ZG-A

(Package name: MP-3A)





- 1. T₁ Terminal
- 2. T₂ Terminal
- 3. Gate Terminal
- 4. T₂ Terminal

Applications

Hybrid IC, solid state relay, switching mode power supply, light dimmer, electric fan, electric blanket, control of household equipment such as washing machine, and other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Farameter	Syllibol	12		
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600	V	
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	720	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T(RMS)}	3	А	Commercial frequency, sine full wave 360° conduction, Tc = 108°C ^{Note3}
Surge on-state current	I _{TSM}	30	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	3.7	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	3	W	
Average gate power dissipation	$P_{G(AV)}$	0.3	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I _{GM}	0.3	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.26	g	Typical value

Notes: 1. Gate open.

Electrical Characteristics

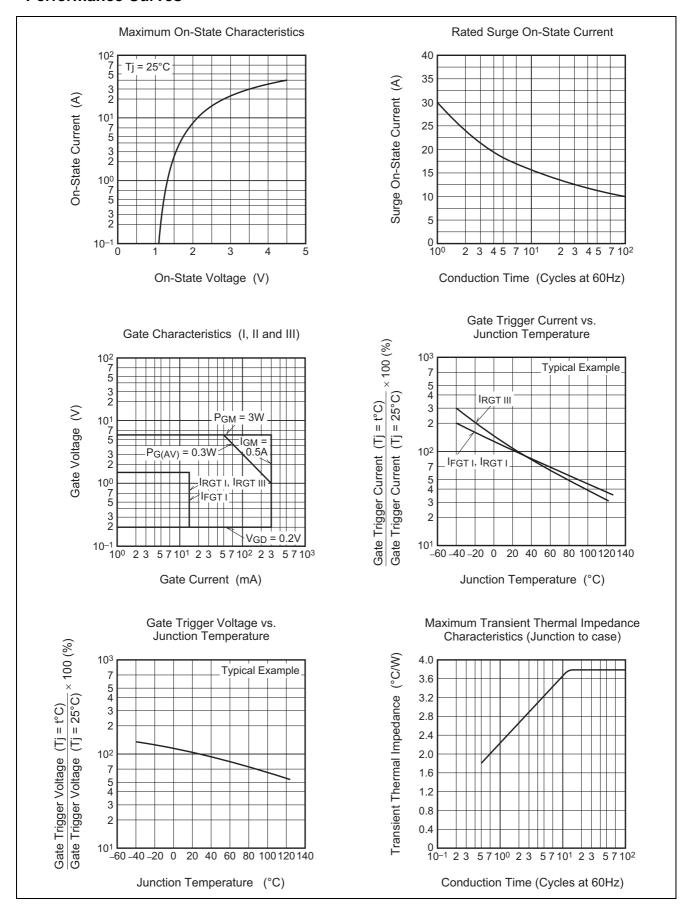
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	_	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V_{TM}	_	_	1.7	V	Tc = 25°C, I _{TM} = 4.5 A, Instantaneous measurement
Gate trigger voltage ^{Note2}	I	V _{FGT I}	_	_	1.5	V	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	$V_{RGT\mathrm{I}}$		_	1.5	V	$R_G = 330 \Omega$
	III	V _{RGT III}		_	1.5	V	
Gate trigger current Note2	I	I _{FGT I}		_	15	mA	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	I _{RGT I}		_	15	mA	$R_G = 330 \Omega$
	III	I _{RGT III}	_	_	15	mA	
Gate non-trigger voltage		V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th(j-c)}	_	_	3.8	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-state commutating voltage Note4		(dv/dt)c	5	_	_	V/μs	Tj = 125°C

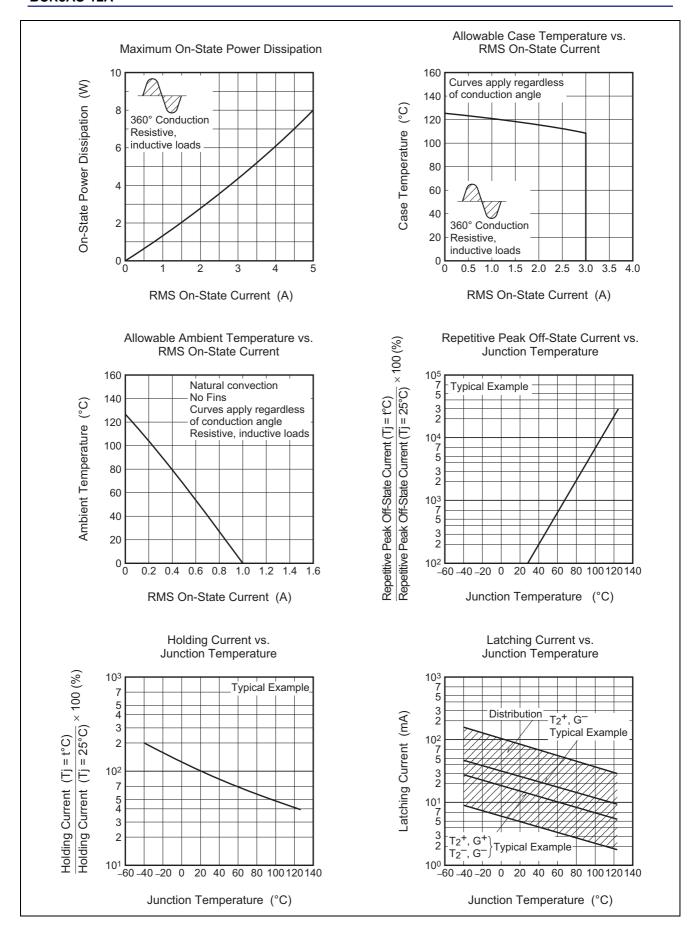
Notes: 2. Measurement using the gate trigger characteristics measureme circuit.

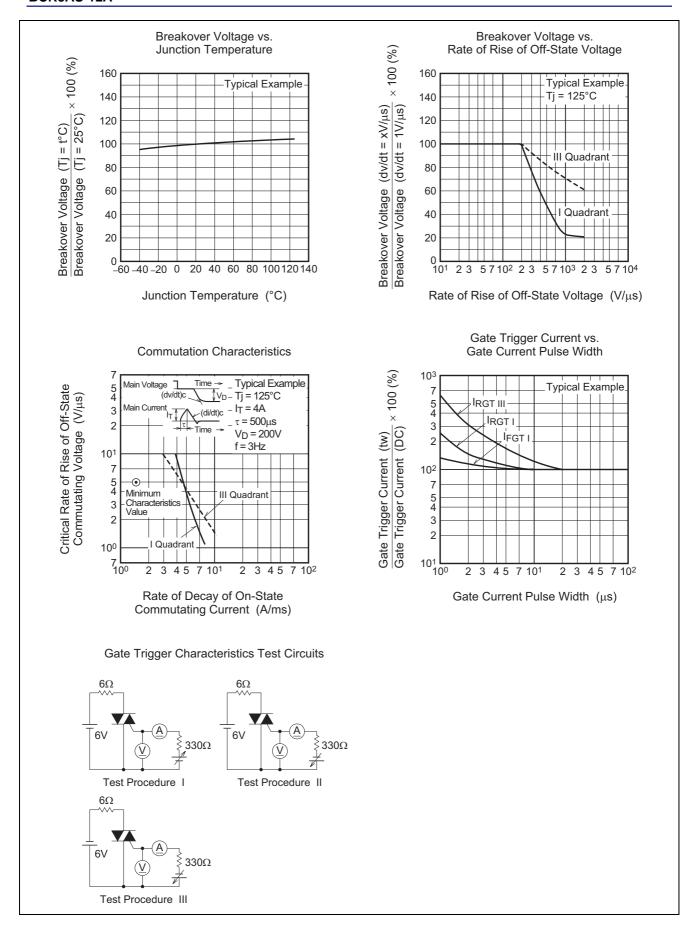
- 3. Case temperature is measured on the T_2 tab.
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage → Time
2. Rate of decay of on-state commutating current (di/dt)c = -1.5 A/ms 3. Reals off state voltage.	Main Current (di/dt)c Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage Time

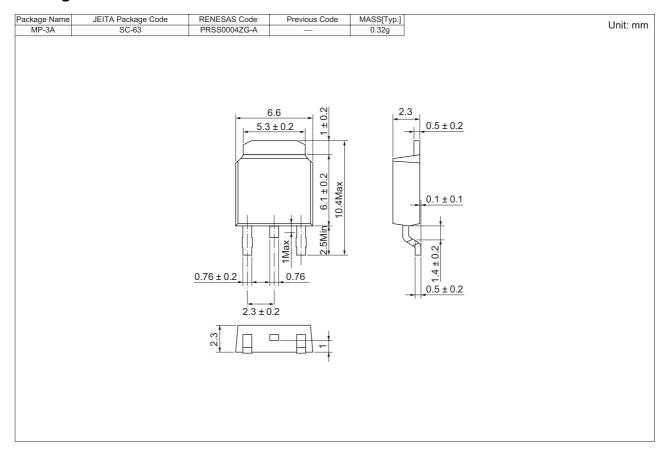
Performance Curves







Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	3000	Type name – T +Direction (1 or 2) +3	BCR3AS-12A-T13
Surface-mounted type	Plastic Magazine (Tube)	75	Type name	BCR3AS-12A

Note: Please confirm the specification about the shipping in detail.

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April 1st, 2010 Renesas Electronics Corporation

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