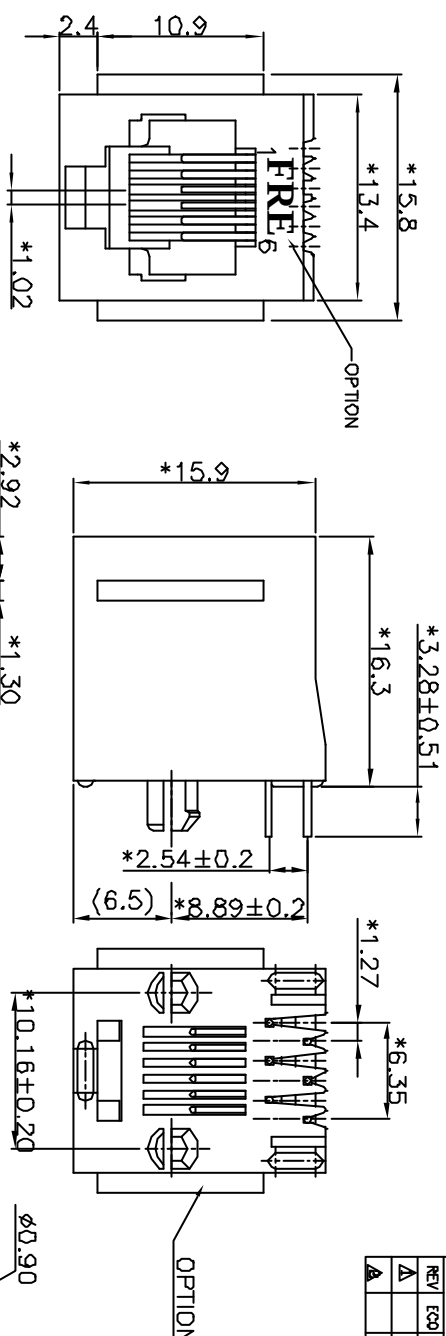


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD
A		ADD PART NUMBER	MM/11/06	
A		ADD PART NUMBER	MM/11/06	



- NOTES:**
- ELECTRICAL:**
1. VOLTAGE RATING: 125 VAC RMS MIN.
 2. CURRENT RATING: 1.5 AMP MIN.
 3. CONTACT RESISTANCE: 30 MILLIOHMS MAX.
 4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
 5. DIELECTRIC STRENGTH: 1000 VAC RMS 60HZ, 1MIN.
- MECHANICAL:**
1. HOUSING MATERIAL: PBT+30%G.F UL94V-0.
 2. CONTACT MATERIAL: PHOSPHOR BRONZE $\phi 0.46\text{mm}$.
 3. PLATING: GOLD PLATING OVER NICKEL.
 4. OPERATING LIFE: 750 CYCLES MIN.
 5. PCB RETENTION PRE-SOLDER: 1 LB MIN.
 6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

- ENVIRONMENTAL:**
1. STORAGE: -40°C TO $+85^{\circ}\text{C}$.
 2. OPERATION: -40°C TO $+85^{\circ}\text{C}$.
 3. WAVE SOLDERING TEMPERATURE: $230\sim 250^{\circ}\text{C}$ (3~5 SECONDS)
- MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.
- CUL FILE NO. E163191

LEAD_FREE P/N: E526X-XX0XXX-L

NO. OF CONTACT

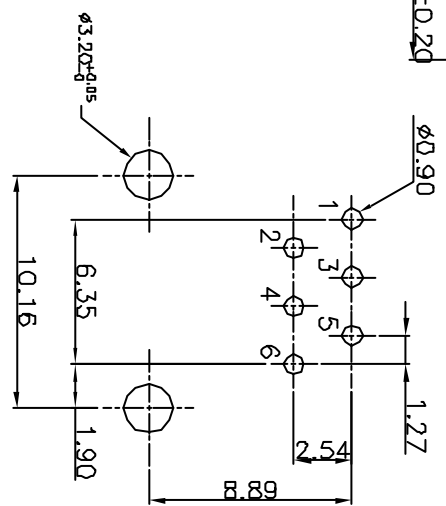
2-2C 4-4C

6-6C

00-W/O "FRE" LOGO

01-W/ "FRE" LOGO

- Gold Plating:
- 1-3u" 2-6u"
- 3-15u" 4-30u"
- 5-50u" 6-Pd 20u"
- 03-W/ PANEL STOP PBT BK
- 04-W/O PANEL STOP PBT BK
- 63-W/ PANEL STOP PBT WT
- 64-W/O PANEL STOP PBT WT
- 46-W/ PANEL STOP PBT GY



PC Board Layout

Component Side Shown

Dimension Tolerance: ± 0.05

DETACHED LISTS			
UNITS	DRTO	DRFT	DATE 4/02/06
TOLERANCES EXCEPT AS NOTED	Q=0	DATE	
MM	APPL. CIRCUIT	DATE 11/10/05	
± 0.10, 0.15			
± 0.00 ± 0.075			
ANGLES ±			
THIRD ANGLE PROJECTION	FINISH	QTY :	
	SCALE : 3:1		

TITLE		FULL RISE ELECTRONIC CO., LTD	
180° TOP ENTRY PCB JACK 6P		DRAWING NO. GE5230T1-LC	
/PART NO. SEE NOTE		SIZE REV	
DO NOT SCALE DRAWING		A3 2	