

| | SPECI | FICATIONS | |
|--|---|------------------|---|
| CUSTOMER | | | |
| SAMPLE CODE (Ver.) | | | |
| MASS PRODUCTION CODE (Ver.) DRAWING NO. (Ver.) | | PC1602LRS | -FWA-B-Q (Rev.0) |
| | | PC-95003 | |
| | Custom | er Approved | |
| | | | |
| | | D | ate: |
| Approved | QC | D Confirmed | ate: Designer |
| Approved | QC | | |
| Approval For Specifications Only. | | Confirmed | |
| Approval For Specifications Only. * This specification is subject to c. | hange without no | Confirmed | Designer AAXIZAhran |
| Approval For Specifications Only. | hange without no | Confirmed | Designer AAXIZAhran |
| Approval For Specifications Only. * This specification is subject to c Please contact Powertip or it's r Approval For Specifications and S | hange without no representative bef Sample. | Confirmed | Designer MANSAMA act based on this specification. |
| Approval For Specifications Only. * This specification is subject to c Please contact Powertip or it's r Approval For Specifications and S | hange without no representative bef Sample. | Confirmed | Designer MANSAMA act based on this specification. |



RECORDS OF REVISION

| Rev. | Description | Note | Page |
|------|---|--|--|
| 0 | PC1602LRS-FWA-B-Qis the ROHS compliant part number based on Powertip's standard PC1602LRS-FWA-B | | |
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| | | PC1602LRS-FWA-B-Qis the ROHS compliant part number | PC1602LRS-FWA-B-Qis the ROHS compliant part number |

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Note : For detailed information please refer to IC data sheet : <u>ST7066U,KS0065B</u>



1. SPECIFICATIONS

1.1 Features

| Item | Standard Value |
|-------------------|--|
| Display Type | 16*2 Characters |
| LCD Type | STN Gray Positive Transflective Normal Temp. |
| Driver Condition | LCD Module: 1/16 Duty, 1/5 Bias |
| Viewing Direction | 6 O'clock |
| Backlight | YG LED B/L |
| Weight | 36 g |
| Interface | _ |
| | THIS PRODUCT CONFORMS THE ROHS OF PTC |
| ROHS | Detail information please refer web side : |
| | http://www.powertip.com.tw/news/LatestNews.asp |

1.2 Mechanical Specifications

| Item | Standard Value | Unit |
|-------------------|----------------------------------|------|
| Outline Dimension | 84.0(L) * 44.0(w) * 13.7(H)(Max) | mm |
| Viewing Area | 61.0(L) * 15.8(w) | mm |
| Active Area | 56.21(L) * 11.5(w) | mm |
| Dot Size | 0.56(L) * 0.66(w) | mm |
| Dot Pitch | 0.60 (L) * 0.70(w) | mm |

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

| Item | Symbol | Condition | Min. | Max. | Unit | | |
|---------------------------|------------------|--------------|----------|----------------------|------|--|--|
| Power Supply Voltage | V_{DD} | _ | -0.3 | 7.0 | V | | |
| LCD Driver Supply Voltage | V _{LCD} | _ | VDD-10.0 | V _{DD} +0.3 | V | | |
| Input Voltage | $V_{\rm IN}$ | — | -0.3 | V _{DD} +0.3 | V | | |
| Operating Temperature | T _{OP} | Excluded B/L | 0 | 50 | °C | | |
| Storage Temperature | T _{ST} | Excluded B/L | -20 | 70 | °C | | |
| Storage Humidity | H _D | Ta<40 °C | - | 90 | %RH | | |



1.4 DC Electrical Characteristics

| $V_{DD} = 5.0 V \pm 0.5 V$, $V_{SS} = 0V$, $Ta = 25^{\circ}C$ | | | | | | |
|---|-----------------|------------------|---------|------|------|------|
| Item | Symbol | Condition | Min. | Тур. | Max. | Unit |
| Logic Supply Voltage | V _{DD} | _ | 4.5 | 5.0 | 5.5 | V |
| "H" Input Voltage | V _{IH} | _ | 0.7 Vdd | - | Vdd | V |
| "L" Input Voltage | V _{IL} | _ | -0.3 | - | 0.6 | V |
| "H" Output Voltage | V _{OH} | IOH=-0.1mA | 3.9 | - | Vdd | V |
| "L" Output Voltage | V _{OL} | IOL=0.1mA | - | - | 0.4 | V |
| Supply Current | I _{DD} | $V_{DD} = 5.0 V$ | - | 1.5 | 3.0 | mA |
| | | 0°C | - | - | - | |
| LCM Driver Voltage | V _{OP} | 25°C*1 | 4.3 | 4.5 | 4.7 | V |
| | | 50°C | - | - | - | |

Note: *1. THE V_{OP} TEST POINT IS V_{DD} - V_O .

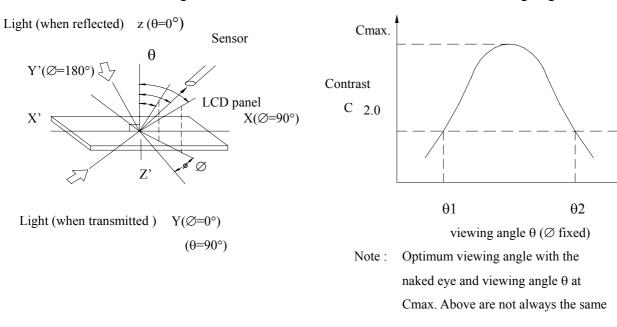
1.5 Optical Characteristics

| | | LCD I allel : | | | VLCD T. | $2 \mathbf{v} \cdot \mathbf{la} 25 \mathbf{C}$ |
|---------------------|--------|---|------|--------|---------|---|
| Item | Symbol | Conditions | Min. | Тур. | Max. | Reference |
| View Angle | θ | $C \ge 2.0, \emptyset = 0^{\circ}$ | 40° | - | - | Notes 1 & 2 |
| Contrast Ratio | С | $\theta = 5^{\circ}, \emptyset = 0^{\circ}$ | 5 | 7 | - | Note 3 |
| Response Time(rise) | tr | $\theta = 5^{\circ}, \emptyset = 0^{\circ}$ | - | 150 ms | - | Note 4 |
| Response Time(fall) | tf | $\theta = 5^{\circ}, \emptyset = 0^{\circ}$ | - | 300 ms | - | Note 4 |

LCD Panel : 1/16 Duty , 1/4 Bias , $V_{LCD} = 4.2$ V , $Ta = 25^{\circ}C$



Note 1: Definition of angles θ and \emptyset

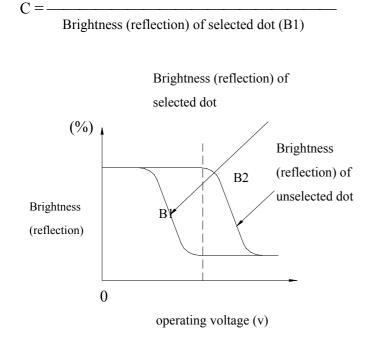


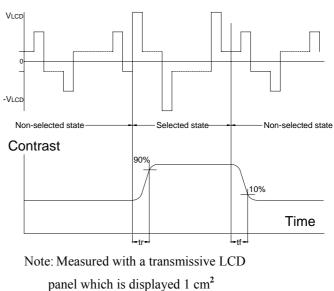
Note 3: Definition of contrast C

Brightness (reflection) of unselected dot (B2)

Note 4: Definition of response time

Note 2: Definition of viewing angles $\theta 1$ and $\theta 2$





 V_{LCD} : Operating voltage f_{FRM} : Frame frequency t_r : Response time (rise) t_f : Response time (fall)



1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

| Item | Symbol | Conditions | Min. | Max. | Unit |
|-------------------|--------|------------|------|------|------|
| Forward Current | IF | Ta =25°C | - | 300 | mA |
| Reverse Voltage | VR | Ta =25℃ | - | 8 | V |
| Power Dissipation | РО | Ta =25℃ | - | 1.38 | W |

Electrical / Optical Characteristics

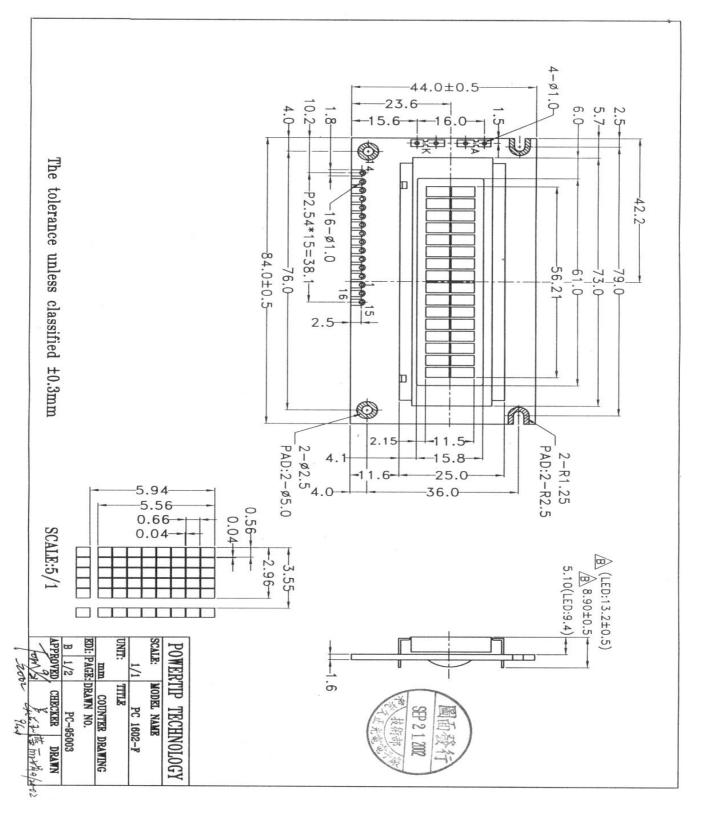
Ta =25°C

| | | | | | 1a – | 250 |
|-------------------------------------|--------------|------------|------|------|------|-------------------|
| Item | Symbol | Conditions | Min. | Тур. | Max. | Unit |
| Forward Voltage | VF | IF= 120 mA | - | 4.2 | 4.6 | V |
| Reverse Current | IR | VR= 8 V | - | - | 0.2 | mA |
| Wavelength | λp | IF= 120 mA | 571 | - | 576 | nm |
| Luminous Intensity (without LCD) | IV | IF=120 mA | 160 | 210 | 250 | cd/m ² |
| Color | Yellow-green | | | | | |

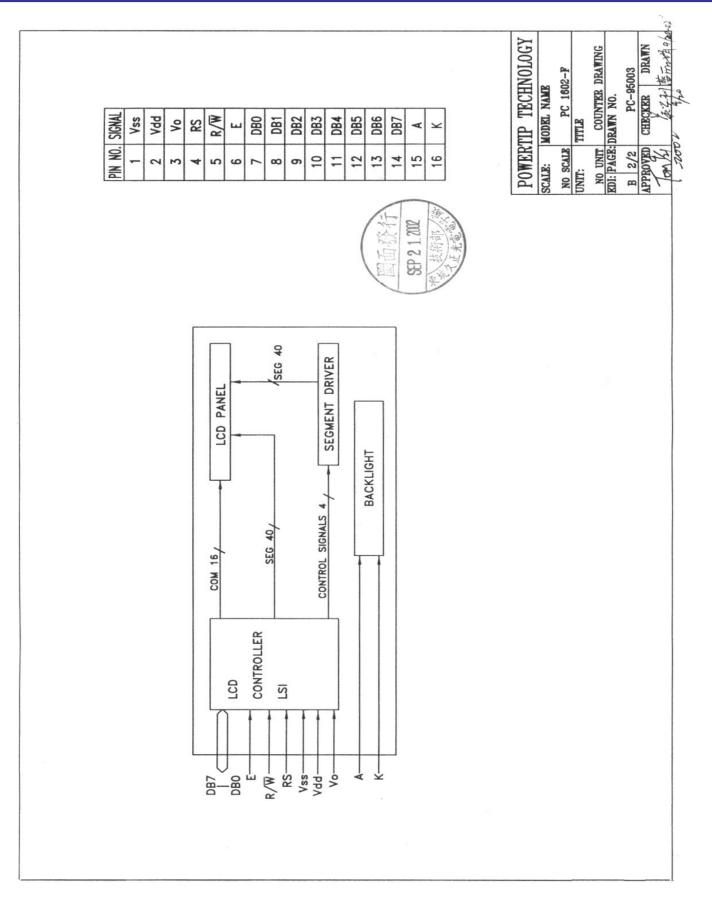


2. MODULE STRUCTURE

2.1 Counter Drawing





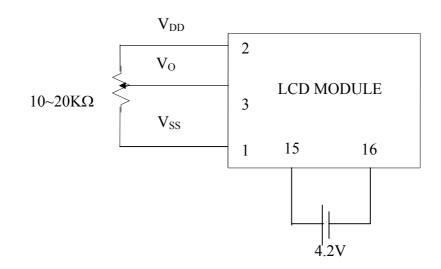


POWERTIP

2.2 Interface Pin Description

| Pin No. | Symbol | Signal Description | | |
|---------|----------------|---|--|--|
| 1 | V_{SS} | Power Supply (Vss=0) | | |
| 2 | V_{DD} | Power Supply (V _{DD} >V _{SS}) | | |
| 3 | Vo | Operating voltage for LCD | | |
| | | Register Selection input | | |
| 4 | RS | High = Data register | | |
| 4 | KS | Low = Instruction register (for write) | | |
| | | Busy flag address counter (for read) | | |
| | | Read/Write signal input is used to select the read/write | | |
| 5 | R/W | mode | | |
| | | High = Read mode, Low = Write mode | | |
| 6 | Е | Start enable signal to read or write the data | | |
| | | Four low order bi-directional three-state data bus lines. Use | | |
| 7~10 | $DB0 \sim DB3$ | for data transfer between the MPU and the LCD module. | | |
| | | These four are not used during 4-bit operation. | | |
| | | Four high order bi-directional three-state data bus lines. | | |
| | | Used for data transfer between the MPU and the LCD | | |
| 11~14 | DB4~DB7 | module. | | |
| | | DB7 can be used as a busy flag. | | |
| 15 | А | Power supply for LED B/L (+) | | |
| 16 | К | Power supply for LED B/L (-) | | |

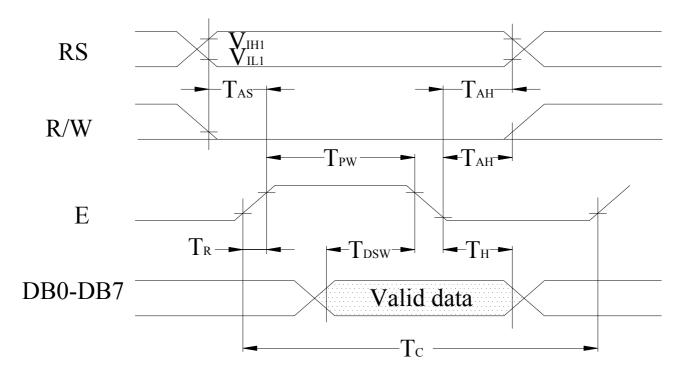
Contrast Adjust



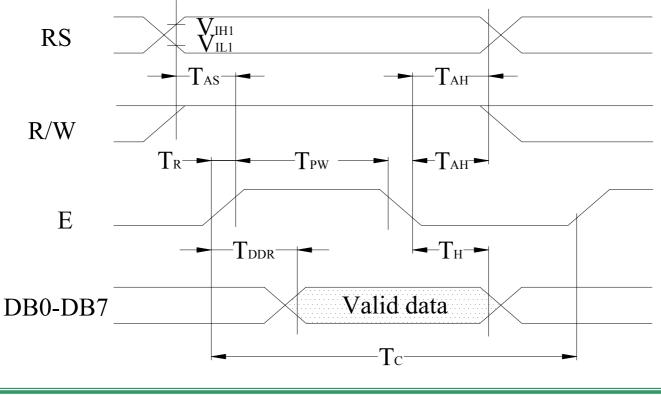


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



•Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

$$(Vcc = +5V, Ta = 25^{\circ}C)$$

| Symbol | Characteristics | Test Condition | Min. | Тур. | Max. | Unit |
|------------------|-------------------------|-----------------|------|------|------|------|
| T _C | Enable Cycle Time | Pin E | 1200 | - | - | ns |
| T_{PW} | Enable Pulse Width | Pin E | 140 | - | - | ns |
| T_R, T_F | Enable Rise / Fall Time | Pin E | - | - | 25 | ns |
| T _{AS} | Address Setup Time | Pins: RS , RW,E | 0 | - | - | ns |
| $T_{\rm AH}$ | Address Hold Time | Pins :RS,RW,E | 10 | - | - | ns |
| T _{DSW} | Data Setup Time | Pins:DB0~DB7 | 40 | - | _ | ns |
| $T_{\rm H}$ | Data Hold Time | Pins:DB0~DB7 | 10 | - | - | ns |

• Read Mode (Reading data from ST7066U to MPU)

| _ | | | | (| $V_{cc} = +5V$ | ,Ta=25°C) |
|------------------|-------------------------|-----------------|------|------|----------------|-----------|
| Symbol | Characteristics | Test Condition | Min. | Тур. | Max. | Unit |
| T _C | Enable Cycle Time | Pin E | 1200 | - | - | ns |
| T_{PW} | Enable Pulse Width | Pin E | 140 | - | - | ns |
| T_R, T_F | Enable Rise / Fall Time | Pin E | - | - | 25 | ns |
| T _{AS} | Address Setup Time | Pins: RS , RW,E | 0 | - | - | ns |
| $T_{\rm AH}$ | Address Hold Time | Pins :RS,RW,E | 10 | - | - | ns |
| T _{DDR} | Data Setup Time | Pins:DB0~DB7 | _ | - | 100 | ns |
| T _H | Data Hold Time | Pins:DB0~DB7 | 10 | - | - | ns |

Downloaded from Arrow.com.



2.4 Display Command

| | | | | | Instru | ction | Code | ļ | | | | Description |
|-------------------------------|----|-----|---------|---------|---------|---------|---------|---------|---------|---------|---|------------------|
| Instructions | RS | R/W | DB 7 | DB 6 | DB 5 | DB 4 | DB 3 | DB 2 | DB 1 | DB 0 | Description | Time (270KHz) |
| Clear Display | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | Write "20H" to DDRAM. and set DDRAM address to "00H" from AC. | 1.52ms |
| Return Home | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | × | Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed. | 1.52ms |
| Entry Mode Set | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | I/D | S | Sets cursor move direction and specifies display shift. These operations are performed during data write and read. | 37us |
| Display ON/OFF | 0 | 0 | 0 | 0 | 0 | 0 | 1 | D | С | В | D=1 : entire display on C=1 : cursor on B=1 : cursor position on | 37µs |
| Cursor or Display Shift | 0 | 0 | 0 | 0 | 0 | 1 | S/C | R/L | × | × | Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data. | 37µs |
| Function Set | 0 | 0 | 0 | 0 | 1 | DL | N | F | × | × | DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8 | 37µs |
| Set CGRAM Address | 0 | 0 | 0 | 1 | AC 5 | AC 4 | AC 3 | AC 2 | AC 1 | AC 0 | Set CGRAM address in address counter. | 37µs |
| Set DDRAM Address | 0 | 0 | 1 | AC 6 | AC 5 | AC 4 | AC 3 | AC 2 | AC 1 | AC 0 | Set DDRAM address in address counter. | 37µs |



| Read Busy Flag and Address | 0 | 1 | BF | AC 6 | AC 5 | AC 4 | AC 3 | AC 2 | AC 1 | AC 0 | Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read. | 0µs |
|----------------------------------|---|---|----|---------|---------|---------|---------|---------|---------|---------|---|------|
| Write Data to RAM | 1 | 0 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Write data into internal RAM (DDRAM/CGRAM). | 37µs |
| Read Data from RAM | 1 | 1 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Read data from internal RAM (DDRAM/CGRAM). | 37µs |

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Refer to Instruction Table for the list of each instruction execution time .

POWERTIP

2.5 Character Pattern

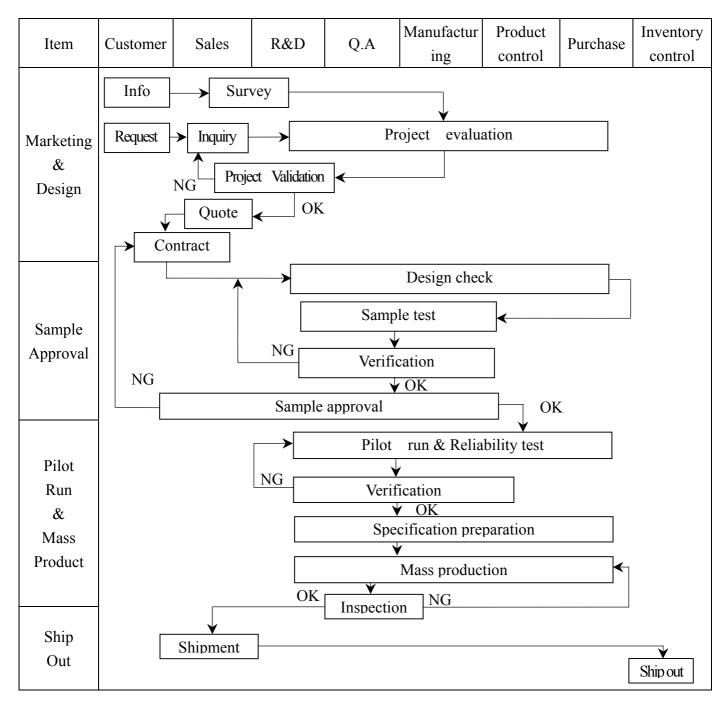
CHARACTER PATTERN(SO/HO/EA,WA)

| Lower 4 Bits | 0000 | 0001 | 0010 | 0011 | 0100 | 0101 | 0110 | 0111 | 1000 | 1001 | 1010 | 1011 | 1100 | 1101 | 1110 | 111 |
|-----------------|------------------|------|-------|-----------|------------|------|------------|--------------|------|------|-------------|----------|------------|-------------------|--------------|-----------|
| xxxx0000 | CG RAM (1) | | | \square | 30 | | ••• | | | | | | -37 | ₩. | <u>:</u> ::: | <u> </u> |
| xxxx0001 | (2) | | : | 1 | | | -331 | -::: | | | | 7 | | £; | | <u> </u> |
| xxxx0010 | (3) | | :: | 22 | | R | Ŀp | ŀ | | | 1 | | • • • | .:-: ¹ | jiii: | 0 |
| xxxx0011 | (4) | | # | | <u> </u> | | : <u>.</u> | : <u>.</u> . | | | 1 | r) | Ţ | 1 | | ::-: |
| xxxx0100 | (5) | | : | 4 | D | T | | ÷. | | | · | | ŀ | | ļl | 57 |
| xxxx0101 | (6) | | | 5 | | | | I] | | | :: | 7 | <u>;</u>] | | :::: | Ċ., |
| xxxx0110 | (7) | | 8. | 6 | [' | U | ÷ | е., i | | 1 | | <u>_</u> | | | ρ | 2 |
| xxxx0111 | (8) | | | 7 | G | | - | 11 | | | | - | 32 | | 9 | 31 |
| xxxx1000 | (1) | | < | 8 | | 2 | ŀ'n | :»:: | | | d. | 0 | : | U. | .,F" | 3.2 |
| xxxx1001 | (2) | |) | 9 | Ι | ب | i | • | | | - | ·']' | J | 11. | : | ۱ <u></u> |
| xxxx1010 | (3) | | :-[-: | :: | | 2 | .1 | | | | | | 1 | L | j | ÷ |
| xxx1011 | (4) | |] | 3 | k: | Ľ | k | 4 | | | : | ÿ | ! | | | ;;;;; |
| xxxx1100 | (5) | | := | | I | ÷ | 1 | I | | | -> | | | <u> </u> | \$ | P |
| xxxx1101 | (6) | | | | 6 | | r-i | 3 | | - | | | ••••• | | ÷ | <u>-</u> |
| xxxx1110 | (7) | | | 2 | ŀ··l | ···· | F~1 | | | | | 12 | | ••• | Ë, | |
| xxxx1111 | (8) | | | · ; ; ; | | | <u> </u> | | | | | ·! | | | | |

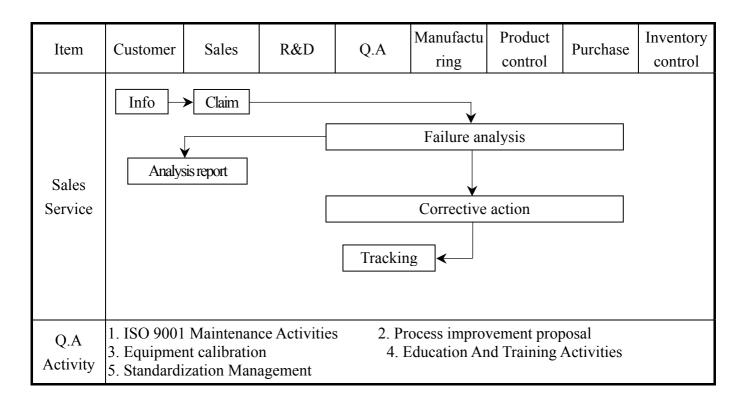


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





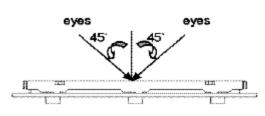


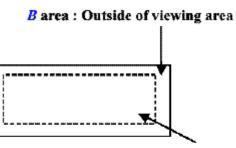


3.2 Inspection Specification

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 .
- OUT Going Defect Level : Sampling .
- ◆Manner of appearance test :
 - (1). The test be under $40W \times 2$ fluorescent light ' and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (3). Definition of area . (Fig. 2)





A area : viewing area

Specification:

| NO | Item | Criterion | level | | | | | |
|----|--|--|-------|--|--|--|--|--|
| | | 1.1 The part number is inconsistent with work order of Production. | Major | | | | | |
| 01 | Product condition | 1.2 Mixed production types. | | | | | | |
| | | 1.3 Assembled in inverse direction. | Major | | | | | |
| 02 | Quantity | 2.1 The quantity is inconsistent with work order of production. | Major | | | | | |
| 03 | Outline dimension | 3.1 Product dimension and structure must conform to Structure diagram. | Major | | | | | |
| | | 4.1 Missing line character > dot and icon. | Major | | | | | |
| | | 4.2 No function or no display. | Major | | | | | |
| 04 | Electrical Testing | 4.3 Output data is error. | Major | | | | | |
| | | 4.4 LCD viewing angle defect. | Major | | | | | |
| | | 4.5 Current consumption exceeds product specifications. | Major | | | | | |
| 05 | Black or white dot scratch contamination Round type | 5.1 Round type: 5.1.1 display only : White and black spots on display ≤ 0.25mm, no more than Four white or black spots present. Densely spaced : NO more than two spots or lines within 2mm | Minor | | | | | |
| | | 3mm | | | | | | |



| ecification : | | | | | | | |
|---|--|--|--|---|--|---|--|
| Item | Criterion | | | | | | |
| Black or white dot \cdot scratch \cdot contamination Round type x ψ ψ ψ ψ ψ ψ ψ ψ ψ ψ | Din 0.1 0.2 | $\frac{\Phi \leq 0.10 \text{mm}}{\Phi \leq 0.10 \text{mm}}$ $\frac{10 \text{mm} < \Phi \leq 0.20}{20 \text{mm} < \Phi \leq 0.25}$ $\frac{10 \text{mm}}{\text{Total}}$ $\frac{10 \text{mm}}{\text{Total}}$ $\frac{10 \text{mm}}{\text{mm}} = \frac{10 \text{mm}}{100 \text{mm}}$ $\frac{10 \text{mm}}{1000 \text{mm}} = 1000000000000000000000000000000000000$ | mm mm)).05mm).075mm | Accept no dense 3 2 4 Accept A area Accept no dens 4 | Minor | | |
| | | w>0.075m | m | As ro | und type | | |
| Polarizer Bubble | $ \begin{array}{c} \Phi \leq \\ 0.20 \text{mm} < \\ 0.50 \text{mm} < \\ \Phi > \\ \hline \text{Total} \end{array} $ | $\leq 0.20 \text{mm}$ $< \Phi \leq 0.50 \text{mm}$ $< \Phi \leq 1.00 \text{mm}$ $> 1.00 \text{mm}$ $= 1.00 \text{mm}$ | | area | Q'ty) B area Don't count Don't count Don't count Don't count Don't count | Minor | |
| The crack of glass | • Glass Crack: 7.1 Crack on the circuit of electrode terminal : \overrightarrow{x} \overrightarrow{x} \overrightarrow{x} \overrightarrow{Y} \overrightarrow{Z} Front $X \leq 1/5$ a $Y \leq 1/2$ D $Z \leq t$ Back | | | | | | |
| | Item Black or white dot \cdot scratch \cdot contamination Round type \downarrow_{x} \downarrow_{y} =(x+y)/2 \downarrow_{u} Polarizer Bubble The crack of | ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-or Dimension 0.1 0.2 $\Psi = (x+y)/2$ $5.1.3$ Line ty Dimensi Length $$ L ≤ 3.0 mm L ≤ 2.5 mm $$ $\Psi = (x+y)/2$ $5.1.3$ Line ty Dimensi Length $$ $\Psi = (x+y)/2$ $\Phi \geq 0.20$ mm $$ $\Psi = (x+y)/2$ $\Psi = 0.20$ mm $$ $\Psi = (x+y)/2$ $\Psi = 0.20$ mm $$ $\Psi = (x+y)/2$ $\Psi = 0.20$ mm $$ $\Psi = (x+y)/2$ <t< td=""><td>ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display :\bigstarDimension (diameter $\Phi \le 0.10 mm$ 0.10mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ Total$\Phi = (x+y)/2$5.1.3 Line type: Dimension (diameter : Φ) Length width $L \le 3.0mm$ 0.03mm $< \Phi \le 0.075m$$\clubsuit$$1.4 \le 3.0mm$ L $\le 2.5mm$ 0.05mm $< \Phi \le 0.075m$Polarizer BubbleDimension (diameter : Φ) $\Phi \le 0.20mm$ 0.20mm $< \Phi \le 0.50mm$ 0.50mm $< \Phi \le 1.00mm$ $\Phi > 1.00mm$ Total quantityThe crack of glassGlass Crack: 7.1 Crack on the circuit or χ</td><td>ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display :\square contamination Round type\square mension (diameter : \square) \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square $\square$$\square$ contamination Round type\square mension (diameter : \square) \square \square \square \square \square \square \square \square \square \square $\square$$\square$ \square \square $\square$$\square$ \square \square \square $\square$$\square$ \square $\square$$\square$ \square \square $\square$$\square$ \square $\square$$\square$ \square \square $\square$$\square$ \square<!--</td--><td>ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display :</td><td>Item Criterion Black or white dot \cdot scratch \cdot contamination Round type 5.1.2 Nom-display : $\hline Dimension (diameter : \Phi) & Acceptance(Q'ty) \\ \hline \Phi \leq 0.10mm & Accept no dense \\ \hline 0.10mm < \Phi \leq 0.20mm & 3 \\ \hline 0.20mm < \Phi \leq 0.25mm & 2 \\ \hline Total & 4 \\ \hline \hline \hline \end{bmatrix}$ $\Phi = (x+y)/2$ 5.1.3 Line type: Dimension (diameter : Φ) & Acceptance (Q'ty) Length width A area B area $& w \leq 0.03mm & Accept no dense Don't count \\ L \leq 3.0mm & 0.03mm < \Phi \leq 0.05mm & 4 & Don't count \\ L \leq 3.0mm & 0.03mm < \Phi \leq 0.075mm & 4 & Don't count \\ L \leq 2.5mm & 0.05mm & \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.50mm & 3 & Don't count \\ 0.50mm < \Phi \leq 0.50mm & 3 & Don't count \\ 0.50mm < \Phi \leq 1.00mm & 2 & Don't count \\ \hline Don't count \\ Total quantity & 4 & Don't count \\ \hline \end{bmatrix}$ The crack of glass Crack on the circuit of electrode terminal : $V = V = V = V = V = V = V = V = V = V$</td></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></br></td></t<> | ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display : \bigstar Dimension (diameter $\Phi \le 0.10 mm$ 0.10mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ 0.20mm $< \Phi \le 0.20$ Total $\Phi = (x+y)/2$ 5.1.3 Line type: Dimension (diameter : Φ) Length width $L \le 3.0mm$ 0.03mm $< \Phi \le 0.075m$ \clubsuit $1.4 \le 3.0mm$ L $\le 2.5mm$ 0.05mm $< \Phi \le 0.075m$ Polarizer BubbleDimension (diameter : Φ) $\Phi \le 0.20mm$ 0.20mm $< \Phi \le 0.50mm$ 0.50mm $< \Phi \le 1.00mm$ $\Phi > 1.00mm$ Total quantityThe crack of glassGlass Crack: 7.1 Crack on the circuit or χ | ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display : \square contamination Round type \square mension (diameter : \square) \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square \square contamination Round type \square mension (diameter : \square) \square | ItemCriterionBlack or white dot \cdot scratch \cdot contamination Round type5.1.2 Nom-display : | Item Criterion Black or white dot \cdot scratch \cdot contamination Round type 5.1.2 Nom-display : $ \hline Dimension (diameter : \Phi) & Acceptance(Q'ty) \\ \hline \Phi \leq 0.10mm & Accept no dense \\ \hline 0.10mm < \Phi \leq 0.20mm & 3 \\ \hline 0.20mm < \Phi \leq 0.25mm & 2 \\ \hline Total & 4 \\ \hline \hline \hline \end{bmatrix}$ $\Phi = (x+y)/2$ 5.1.3 Line type: Dimension (diameter : Φ) & Acceptance (Q'ty) Length width A area B area $ & w \leq 0.03mm & Accept no dense Don't count \\ L \leq 3.0mm & 0.03mm < \Phi \leq 0.05mm & 4 & Don't count \\ L \leq 3.0mm & 0.03mm < \Phi \leq 0.075mm & 4 & Don't count \\ L \leq 2.5mm & 0.05mm & \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.075mm & As round type \\ \hline \end{bmatrix}$ Polarizer Dimension (diameter : Φ) $Accept no dense Don't count \\ 0.20mm < \Phi \leq 0.50mm & 3 & Don't count \\ 0.50mm < \Phi \leq 0.50mm & 3 & Don't count \\ 0.50mm < \Phi \leq 1.00mm & 2 & Don't count \\ \hline Don't count \\ Total quantity & 4 & Don't count \\ \hline \end{bmatrix}$ The crack of glass Crack on the circuit of electrode terminal : $V = V = V = V = V = V = V = V = V = V $ | |



| | pecification : | | | | | |
|----|---------------------------|--|--------------|---|--------------|-------|
| NO | Item | Criterion | | | | Level |
| | | Glass 7.2 Gene 7.2.1 | | and corner edge: | Z | |
| | The crack of glass | | X | Y | Z | Minor |
| | X: The length of Crack | | Neglect | Out A area | Neglect | |
| | Y: The width of crack | 7.2,2 | | The second se | | |
| 07 | Z: The thickness of crack | | · | x | | |
| | D: terminal length | | X Neglect | Y Out A area | Z Neglect | |
| | T: The thickness of glass | | Regieer | OutMalea | Regieer | |
| | A : The length of glass | 7.3 Glass 1 | remain: | | | |
| | | | | | Y 1/3 d | Minor |



◆Specification :

| | ecification : | | | | · · · · · | | | |
|----|---|---|--|---|-----------|--|--|--|
| NO | Item | Criterion | | | Level | | | |
| 07 | The crack of glass X: The length of Crack Y: The width of crack Z: The thickness of crack D: terminal length T: The thickness of glass A : The length of glass | 7.4 Corner crass x SP x $\leq 1/5a$ $\leq 1/5a$ | Ack and medial crack: | x SP G] Z $\leq 1/2t$ $1/2t < Z \leq 2t$ | Minor | | | |
| | | 8.1 Backlight c | an't work normally. | <u> </u> | Major | | | |
| | Backlight | 8.2 Backlight d | loesn't light or color is wrong. | | Major | | | |
| 08 | elements | 8.3 Illumination source flickers when lit. | | | | | | |
| | | 9.1 pin type mu | ust match type in specification she | et | Major | | | |
| | | 9.2 No short circuits in components on PCB or FPC | | | | | | |
| 09 | General appearance | 9.3Product packaging must the same as specified on packaging specification sheet. | | | | | | |
| | | 9.4 The folding and peeled off in polarizer are not acceptable | | | | | | |
| | | | or FPC between B/L assembled d PC) is ≤ 1.5 mm | istance | Major | | | |



4. RELIABILITY TEST 4.1 Reliability Test Condition

| 4.1 | Reliability Test Collution | | | | | | | |
|-----|-------------------------------|--|--------------------------------|--|--|--|--|--|
| NO. | TEST ITEM | TEST CONDITION | | | | | | |
| 1 | High Temperature Storage Test | Keep in 70 $\pm 2^{\circ}$ C 96 hrs | | | | | | |
| | | Surrounding temperature, then storag | e at normal condition 4hrs | | | | | |
| 2 | Low Temperature Storage Test | Keep in -20 $\pm 2^{\circ}$ C 96 hrs | | | | | | |
| | | Surrounding temperature, then storag | | | | | | |
| | | Keep in $+60^{\circ}$ C/90%RH duration for 9 | 96 hrs | | | | | |
| 3 | High Humidity Storage | Surrounding temperature, then storage at normal condition 4hrs (Excluding the polarizer)Or Keep in $+40^{\circ}$ C/90%RH duration for 96 hrs | | | | | | |
| | | Surrounding temperature, then storag | e at normal condition 4hrs | | | | | |
| | | 1. Sine wave $10 \sim 55$ HZ frequency | (1 min) | | | | | |
| 4 | Vibration Test | 2. The amplitude of vibration :1.5 m | | | | | | |
| | | 3. Each direction (XYZ) duration fo | | | | | | |
| | | Air Discharge: | Contact Discharge: | | | | | |
| | | Apply 6 KV with 5 times | Apply 250V with 5 times | | | | | |
| | | Discharge foreach polarity +/- | discharge foreach polarity +/- | | | | | |
| | | 1. Temperature ambinace: $15^{\circ}C \sim 35$ | °C | | | | | |
| | | 2. Humidity relative: $30\% \sim 60\%$ | | | | | | |
| 5 | ESD Test | 3. Energy Storage Capacitance(Cs+Cd):150pF±10% | | | | | | |
| | | 4. Discharge Resistance(Rd):330 $\Omega \pm 10\%$ | | | | | | |
| | | 5. Discharge, mode of operation: | | | | | | |
| | | Single Discharge (time between successive discharges at least 1 s) | | | | | | |
| | | (Tolerance If the output voltage indication: $\pm 5\%$) | | | | | | |
| | | $-20^{\circ}C \rightarrow 25^{\circ}C \rightarrow 70^{\circ}C$ | $C \rightarrow 25^{\circ}C$ | | | | | |
| | Town on town Couling Toot | (30mins) (5mins) (3 | Omins) (5mins) | | | | | |
| 6 | Temperature Cycling Test | (30mins) (5mins) (3 10 Cyc | le | | | | | |
| | | Surrounding temperature, then storage at normal condition 4hrs | | | | | | |
| | | 1. Sine wave $10 \sim 55$ HZ frequency (| | | | | | |
| 7 | Vibration Test (Packaged) | 2. The amplitude of vibration :1.5 m | · · · · · | | | | | |
| | | I he amplitude of vibration :1.5 mm Each direction (XYZ) duration for 2 Hrs | | | | | | |
| | | · · · · · | | | | | | |
| | | | Drop Height (cm) | | | | | |
| | | 0~45.4 | 122 | | | | | |
| | | 45.4 ~ 90.8 | 76 | | | | | |
| 8 | Drop Test (Packaged) | 90.8 ~ 454 | 61 | | | | | |
| | | Over 454 | 46 | | | | | |
| | | | /1 edges /6 sides etch 1times | | | | | |

POWERTIP

5. PRECAUTION RELATING PRODUCT HANDLING 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.