Revision. 3

Transistors with Built-in Resistor

DRC2523Y0L

## **Panasonic**

## DRC2523Y0L

### Silicon NPN epitaxial planar type

For digital circuits
Complementary to DRA2523Y

#### ■ Features

- Low collector-emitter saturation voltage Vce(sat)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

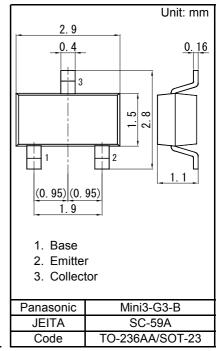
■ Marking Symbol: TH

#### ■ Packaging

Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	500	mA
Total power dissipation	PT	200	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C



Internal Connection							
$R_1$ $C$							
$R_2$ $\bigcirc$ $\bigcirc$ $\bigcirc$ $\bigcirc$ $\bigcirc$ $\bigcirc$ $\bigcirc$ $\bigcirc$							
Resistance	R1	2.2	kΩ				
value	R2	10	kΩ				

### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Symbol	Conditions		l yp	Max	Unit
VCBO	IC = 10 μA, IE = 0				V
VCEO	IC = 2 mA, IB = 0	50			V
ICBO	VCB = 50 V, IE = 0			1	μA
ICEO	VCE = 50 V, IB = 0			1	μA
IEBO	VEB = 6 V, IC = 0			1	mA
hFE	VCE = 10 V, IC = 100 mA	60			-
'CE(sat)	IC = 100 mA, IB = 5 mA			0.25	V
Vi(on)	VCE = 0.2 V, IC = 50 mA	1.9			V
Vi(off)	VCE = 5 V, IC = 100 μA			0.4	V
R1		-30%	2.2	+30%	kΩ
R1/R2		0.17	0.22	0.27	-
/(	VCEO ICBO ICEO IEBO hFE CE(sat) Vi(on) Vi(off) R1	VCEO IC = 2 mA, IB = 0 ICBO VCB = 50 V, IE = 0 ICEO VCE = 50 V, IB = 0 IEBO VEB = 6 V, IC = 0 hFE VCE = 10 V, IC = 100 mA CE(sat) IC = 100 mA, IB = 5 mA Vi(on) VCE = 0.2 V, IC = 50 mA Vi(off) VCE = 5 V, IC = 100 μA R1	VCEO IC = 2 mA, IB = 0 ICBO VCB = 50 V, IE = 0 ICEO VCE = 50 V, IB = 0 IEBO VEB = 6 V, IC = 0 hFE VCE = 10 V, IC = 100 mA CE(sat) IC = 100 mA, IB = 5 mA Vi(on) VCE = 0.2 V, IC = 50 mA Vi(off) VCE = 5 V, IC = 100 μA R1 -30%	VCBO IC = 10 μA, IE = 0  VCEO IC = 2 mA, IB = 0  ICBO VCB = 50 V, IE = 0  ICEO VCE = 50 V, IB = 0  IEBO VEB = 6 V, IC = 0  hFE VCE = 10 V, IC = 100 mA  CE(sat) IC = 100 mA, IB = 5 mA  Vi(on) VCE = 0.2 V, IC = 50 mA  Ni(off) VCE = 5 V, IC = 100 μA  R1  -30% 2.2	VCBO IC = 10 μA, IE = 0  VCEO IC = 2 mA, IB = 0  ICBO VCB = 50 V, IE = 0  ICEO VCE = 50 V, IB = 0  IEBO VEB = 6 V, IC = 0  hFE VCE = 10 V, IC = 100 mA  CE(sat) IC = 100 mA, IB = 5 mA  Vi(off) VCE = 5 V, IC = 100 μA  R1  50  50  1  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  1  50  1  1  50  1  1  50  1  1  50  1  50  1  1  50  1  50  1  50  1  50  1  50  1  50  1  50  1  50  50

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

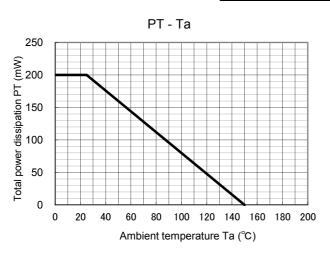
Established: 2010-02-04 Revised: 2014-03-25

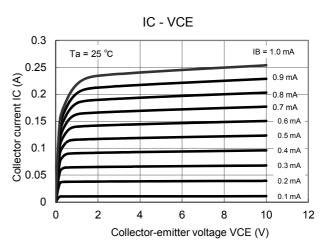
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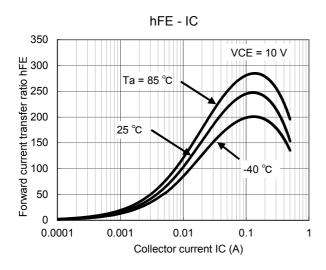
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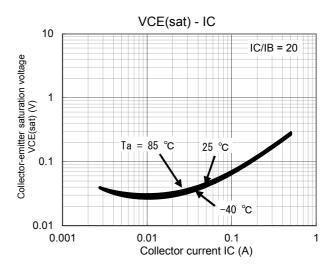
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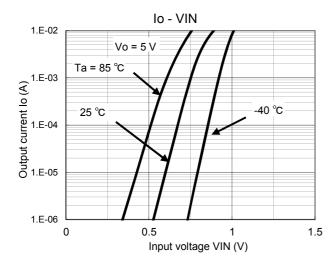
## Technical Data (reference)

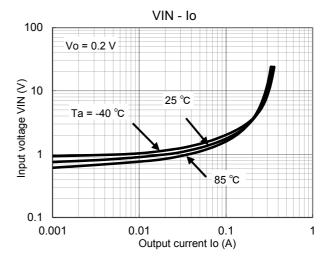












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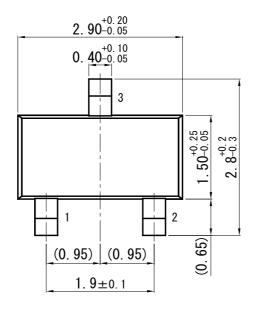
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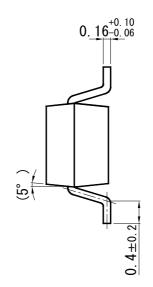
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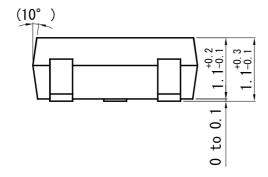
## Mini3-G3-B

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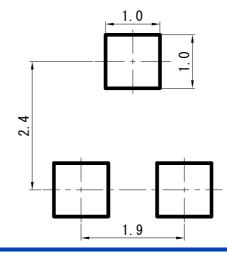
Unit: mm







### ■ Land Pattern (Reference) (Unit: mm)



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Established: 2010-02-04 Revised: 2014-03-25

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