



ON Semiconductor®

## FCD900N60Z

### N-Channel SuperFET® II MOSFET

600 V, 4.5 A, 900 mΩ

#### Features

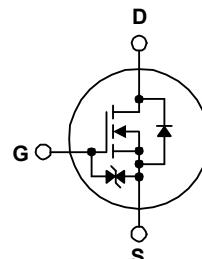
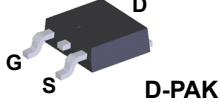
- 650 V @  $T_J = 150^\circ\text{C}$
- Typ.  $R_{DS(on)} = 820 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 13 \text{ nC}$ )
- Low Effective Output Capacitance (Typ.  $C_{oss(\text{eff.})} = 49 \text{ pF}$ )
- 100% Avalanche Tested
- ESD Improved Capacity
- RoHS Compliant

#### Applications

- LCD / LED / PDP TV and Monitor Lighting
- Solar Inverter
- Charger

#### Description

SuperFET® II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.



**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter		FCD900N60Z	Unit
$V_{DSS}$	Drain to Source Voltage		600	V
$V_{GSS}$	Gate to Source Voltage	- DC	$\pm 20$	V
		- AC ( $f > 1\text{Hz}$ )	$\pm 30$	V
$I_D$	Drain Current	- Continuous ( $T_C = 25^\circ\text{C}$ )	4.5	A
		- Continuous ( $T_C = 100^\circ\text{C}$ )	3.5	
$I_{DM}$	Drain Current	- Pulsed	(Note 1)	13.5
$E_{AS}$	Single Pulsed Avalanche Energy		(Note 2)	47.5
$I_{AR}$	Avalanche Current		(Note 1)	1
$E_{AR}$	Repetitive Avalanche Energy		(Note 1)	0.52
$dv/dt$	MOSFET $dv/dt$		100	V/ns
	Peak Diode Recovery $dv/dt$		(Note 3)	
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	52	W
		- Derate Above $25^\circ\text{C}$	0.42	
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

#### Thermal Characteristics

Symbol	Parameter	FCD900N60Z	Unit
$R_{0JC}$	Thermal Resistance, Junction to Case, Max.	2.4	$^\circ\text{C/W}$
$R_{0JA}$	Thermal Resistance, Junction to Ambient, Max.	100	$^\circ\text{C/W}$

## Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FCD900N60Z	FCD900N60Z	DPAK	Tape and Reel	330 mm	16 mm	2500 units

**Electrical Characteristics**  $T_C = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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### Off Characteristics

$\text{BV}_{\text{DSS}}$	Drain to Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^\circ\text{C}$	600	-	-	V
		$V_{\text{GS}} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 150^\circ\text{C}$	650	-	-	
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 10 \text{ mA}$ , Referenced to $25^\circ\text{C}$	-	0.67	-	$\text{V}^\circ\text{C}$
$\text{BV}_{\text{DS}}$	Drain to Source Avalanche Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 4.5 \text{ A}$	-	700	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 480 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	-	-	5	$\mu\text{A}$
		$V_{\text{DS}} = 480 \text{ V}, T_C = 125^\circ\text{C}$	-	-	20	
$I_{\text{GSSF}}$	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	-	-	10	uA
$I_{\text{GSSR}}$	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = -20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	-	-	-10	uA

### On Characteristics

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_D = 250 \mu\text{A}$	2.5	-	3.5	V
$R_{\text{DS(on)}}$	Static Drain to Source On Resistance	$V_{\text{GS}} = 10 \text{ V}, I_D = 2.3 \text{ A}$	-	0.82	0.90	$\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 20 \text{ V}, I_D = 2.3 \text{ A}$	-	4.6	-	S

### Dynamic Characteristics

$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	-	543	720	pF
$C_{\text{oss}}$	Output Capacitance		-	400	530	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	20	30	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}} = 380 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	-	11	-	pF
$C_{\text{oss(eff.)}}$	Effective Output Capacitance	$V_{\text{DS}} = 0 \text{ V to } 480 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	-	49	-	pF
$Q_{\text{g(tot)}}$	Total Gate Charge at 10V	$V_{\text{DS}} = 380 \text{ V}, I_D = 2.3 \text{ A}, V_{\text{GS}} = 10 \text{ V}$	-	13	17	nC
$Q_{\text{gs}}$	Gate to Source Gate Charge		(Note 4)	2.3	-	nC
$Q_{\text{gd}}$	Gate to Drain "Miller" Charge			4.8	-	nC
ESR	Equivalent Series Resistance	$f = 1 \text{ MHz}$	-	2.4	-	$\Omega$

### Switching Characteristics

$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = 380 \text{ V}, I_D = 2.3 \text{ A}, V_{\text{GS}} = 10 \text{ V}, R_G = 4.7 \Omega$	-	10.9	32	ns
$t_r$	Turn-On Rise Time		-	5.3	21	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		-	33.6	77	ns
$t_f$	Turn-Off Fall Time		(Note 4)	11.9	34	ns

### Drain-Source Diode Characteristics

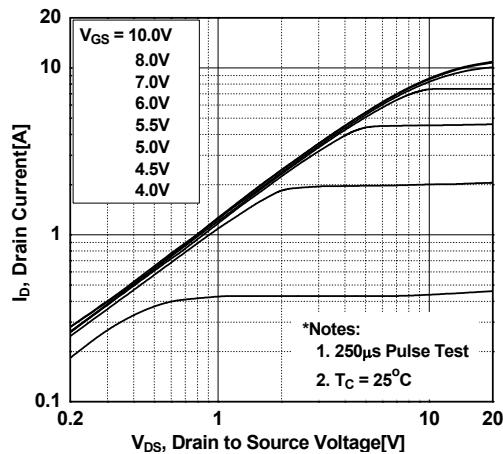
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	4.5	A	
$I_{\text{SM}}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	13.5	A	
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage	$V_{\text{GS}} = 0 \text{ V}, I_{\text{SD}} = 2.3 \text{ A}$	-	-	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0 \text{ V}, I_{\text{SD}} = 2.3 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	156	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		-	1.3	-	$\mu\text{C}$

#### Notes:

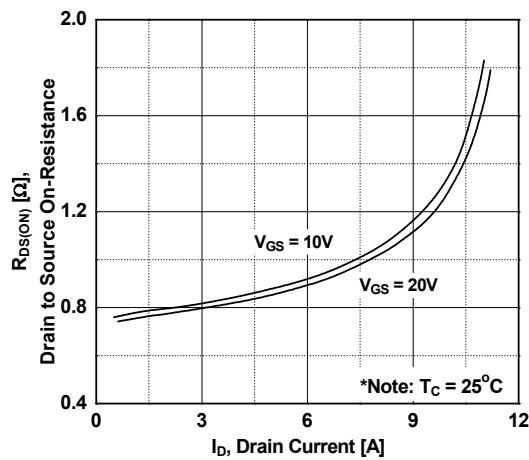
1. Repetitive rating: pulse-width limited by maximum junction temperature.
2.  $I_{\text{AS}} = 1 \text{ A}, V_{\text{DD}} = 50 \text{ V}, R_G = 25 \Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{\text{SD}} \leq 2.3 \text{ A}, di/dt \leq 200 \text{ A}/\mu\text{s}, V_{\text{DD}} \leq BV_{\text{DSS}}$ , starting  $T_J = 25^\circ\text{C}$ .
4. Essentially independent of operating temperature typical characteristics.

## Typical Performance Characteristics

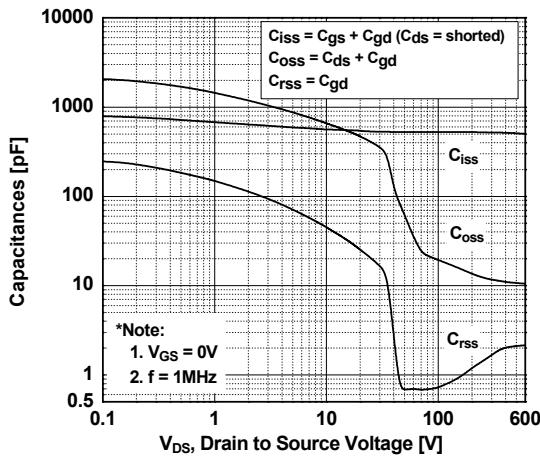
**Figure 1. On-Region Characteristics**



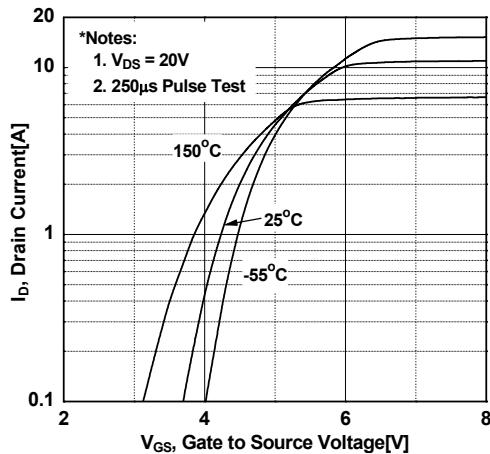
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



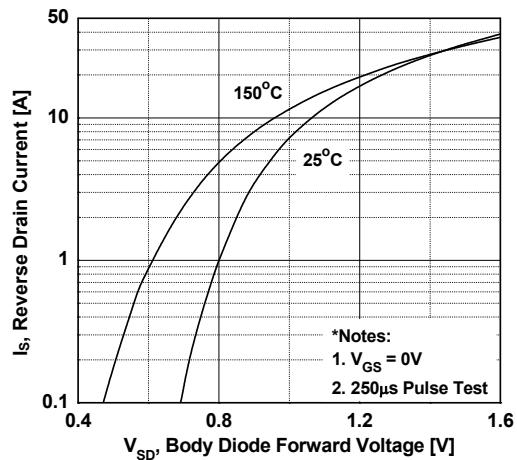
**Figure 5. Capacitance Characteristics**



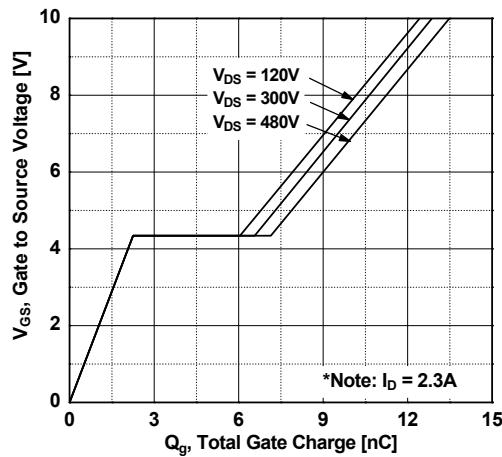
**Figure 2. Transfer Characteristics**



**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**

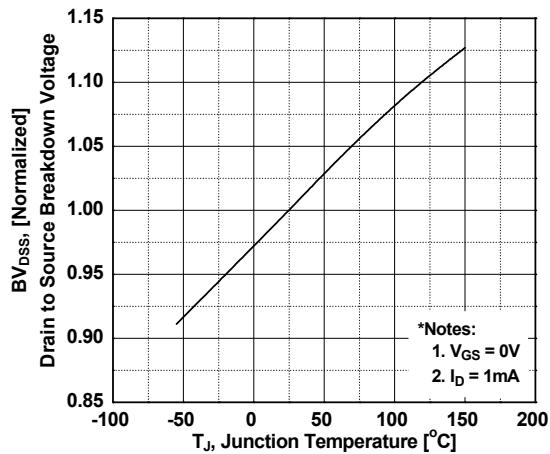


**Figure 6. Gate Charge Characteristics**

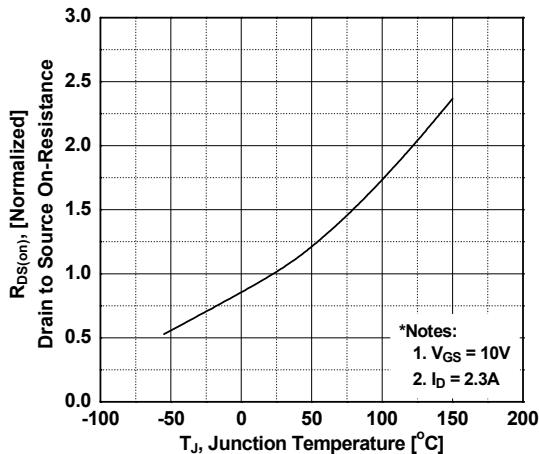


## Typical Performance Characteristics (Continued)

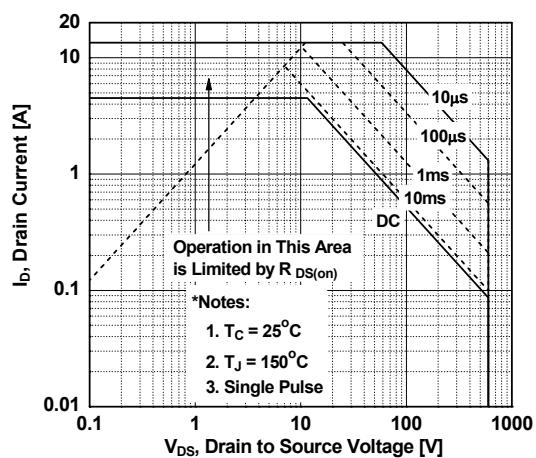
**Figure 7. Breakdown Voltage Variation vs. Temperature**



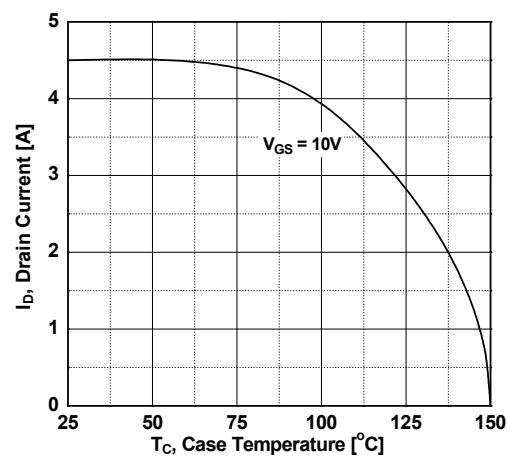
**Figure 8. On-Resistance Variation vs. Temperature**



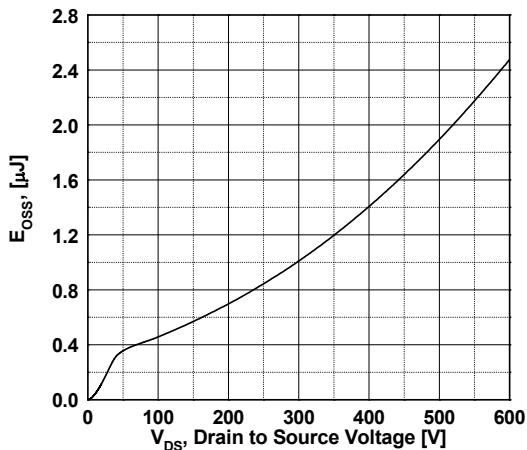
**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs. Case Temperature**

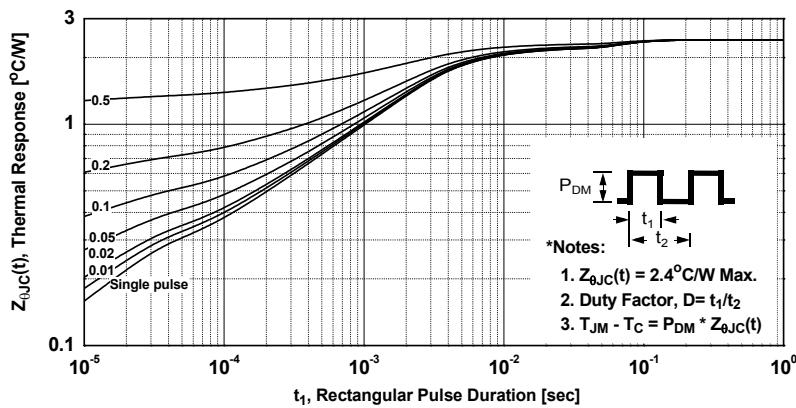


**Figure 11. Eoss vs. Drain to Source Voltage**



## Typical Performance Characteristics (Continued)

Figure 12. Transient Thermal Response Curve



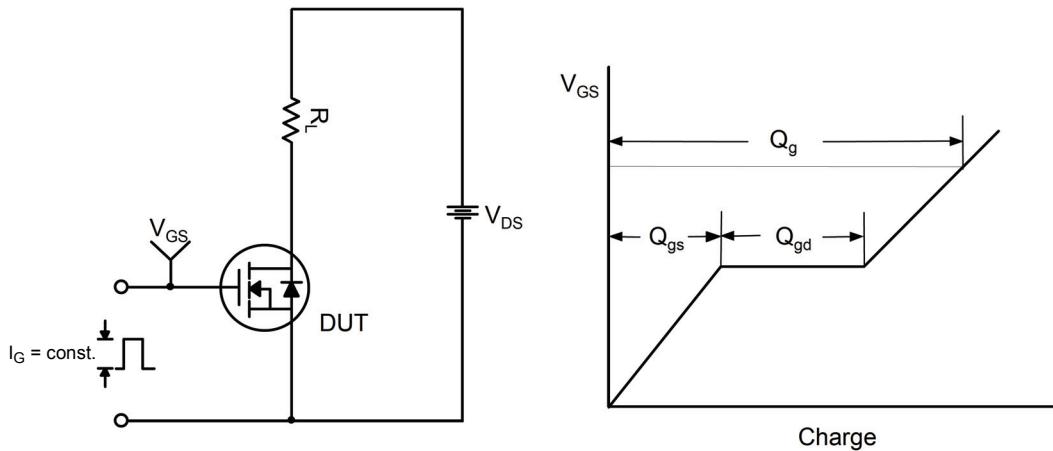


Figure 13. Gate Charge Test Circuit & Waveform

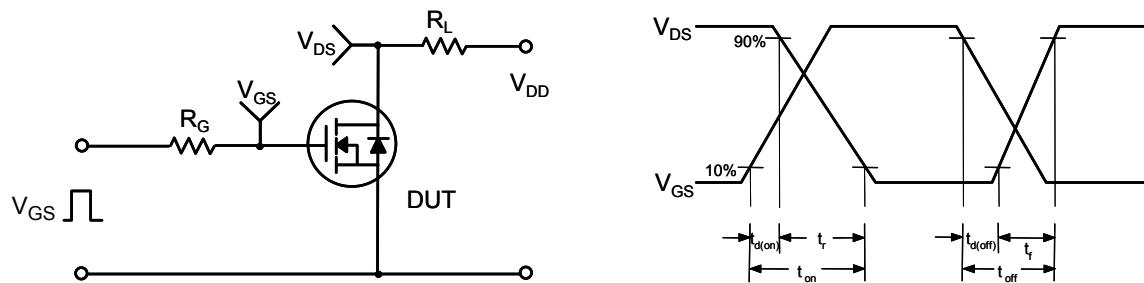


Figure 14. Resistive Switching Test Circuit & Waveforms

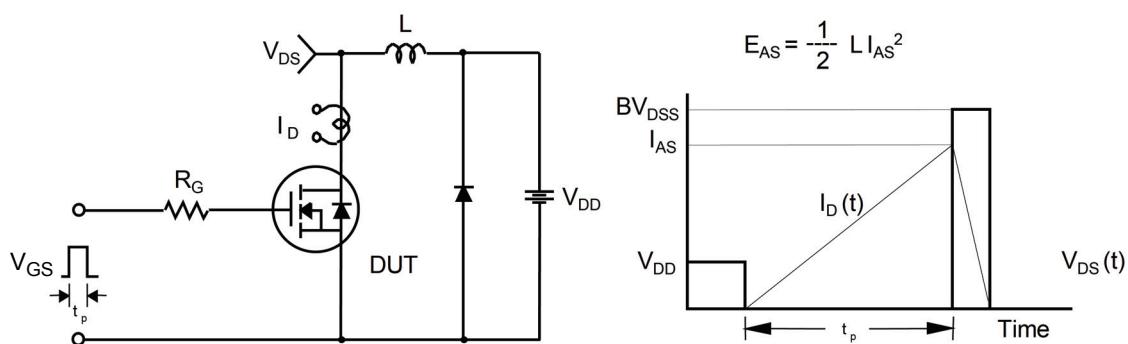


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

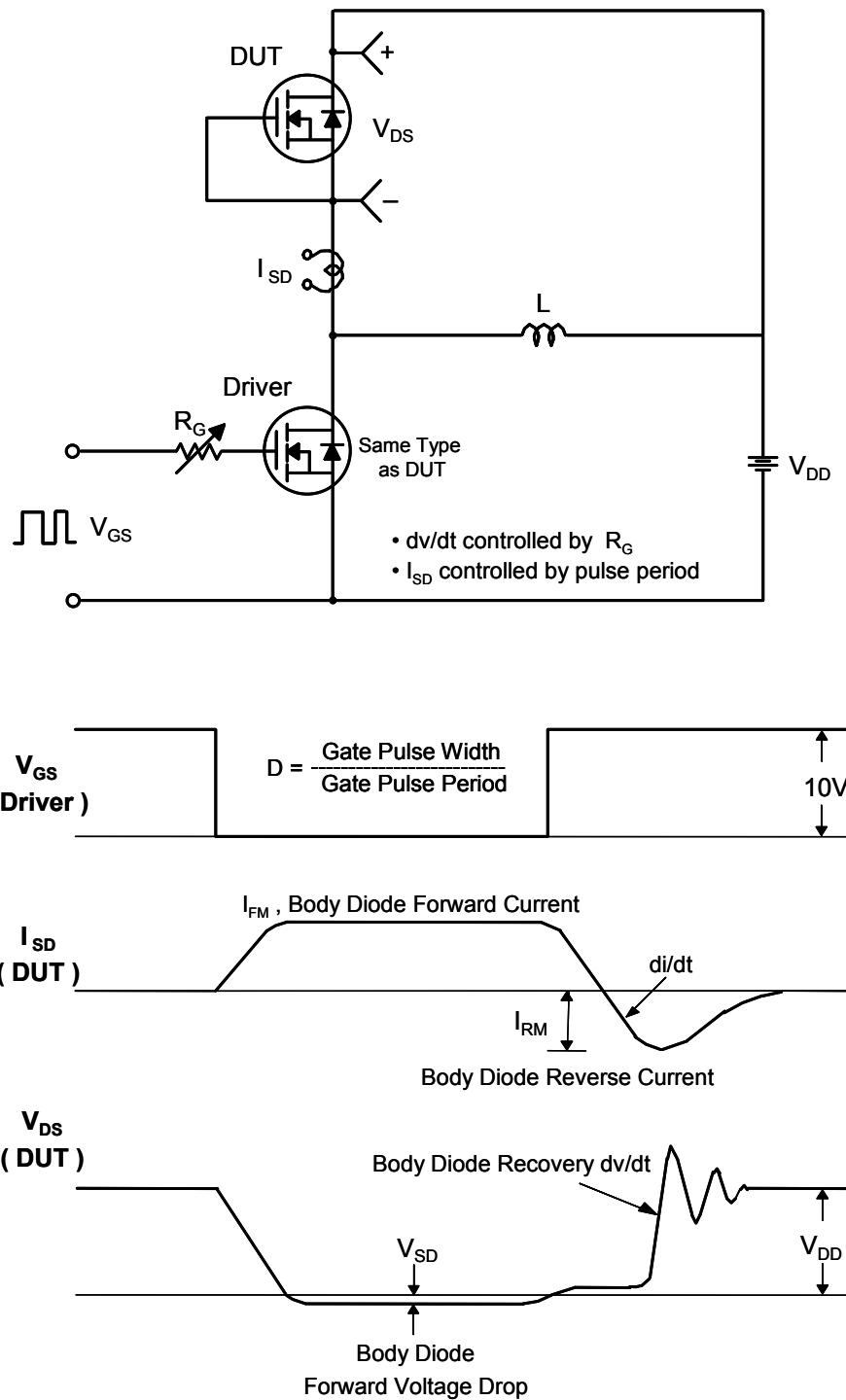
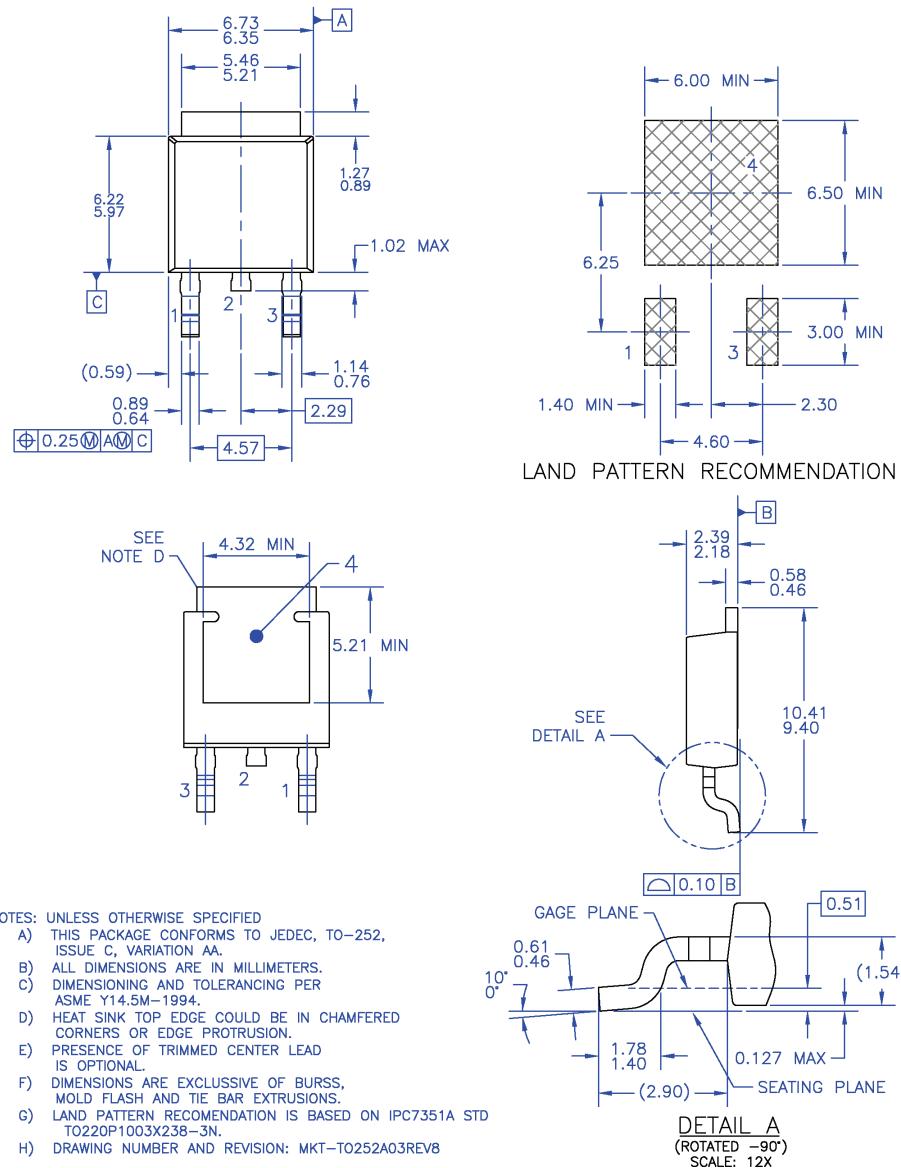


Figure 16. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms

## Mechanical Dimensions



**Figure 17. TO252 (D-PAK), Molded, 3-Lead, Option AA&AB**

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