

May 2007

# DM74AS257, DM74AS258 3-STATE Quad 1 of 2 Line Data Selector/Multiplexers

#### **Features**

- Switching specifications at 50pF
- Switching specifications guaranteed over full temperature and V<sub>CC</sub> range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky, low power Schottky, and advanced low power Schottky TTL counterpart
- Improved AC performance over Schottky, low power Schottky, and advanced low power Schottky counterparts
- 3-STATE buffer-type output drive bus lines directly
- Expand any data input point
- Multiplex dual data buses
- General four functions of two variables (one variable is common)
- Source programmable counters

#### **General Description**

These data selectors/multiplexers contain inverters and drivers to supply full on-chip data selection to the four 3-STATE outputs that can interface directly with data lines of bus-organized systems. A 4-bit word selected from one of two sources is routed to the four outputs. The DM74AS257 presents true data whereas the DM74AS258 presents inverted data to minimize propagation delay time.

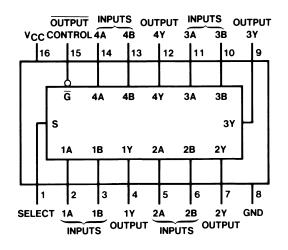
This 3-STATE output feature means that n-bit (paralleled) data selectors with up to 300 sources can be implemented for data buses. It also permits the use of standard TTL registers for data retention throughout the system.

# **Ordering Information**

Order Number	Package Number	Package Description
DM74AS257M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
DM74AS257N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
DM74AS258M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering number.

# **Connection Diagram**



#### **Function Table**

	Inputs	Out	out Y		
OUTPUT Control	Select	Α	В	AS257	AS258
Н	Х	Х	Х	Z	Z
L	L	L	Х	L	Н
L	L	Н	Х	Н	L
L	Н	Х	L	L	Н
L	Н	Х	Н	Н	L

H = HIGH Level

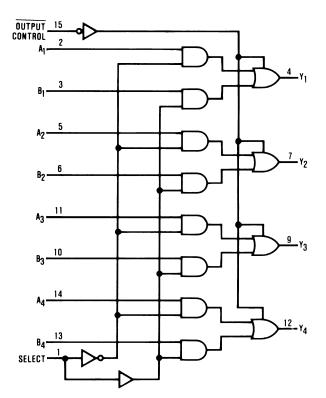
L = LOW Level

X = Don't Care

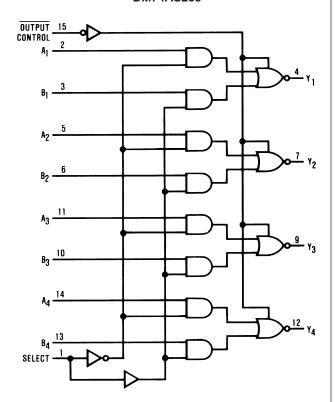
Z = High Impedance (OFF)

# **Logic Diagrams**

#### DM74AS257



#### DM74AS258



# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	7V
VI	Input Voltage	7V
	Voltage Applied to Disabled Output	5.5V
T <sub>A</sub>	Operating Free Air Temperature Range 0°C to	
T <sub>STG</sub>	Storage Temperature Range	–65°C to +150°C
$\theta_{JA}$	Typical Thermal Resistance, N Package	75.0°C/W

### **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Nom.	Max.	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-15	mA
I <sub>OL</sub>	LOW Level Output Current			48	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

#### **Electrical Characteristics**

Over recommended operating free air temperature range. All typical values are measured at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Symbol	Parar	neter	Conditio	ns	Min.	Тур.	Max.	Units
V <sub>IK</sub>	Input Clamp Vol	tage	$V_{CC} = 4.5V, I_{I} = -18 \text{ m/s}$	\			-1.2	V
V <sub>OH</sub>	HIGH Level Out	put Voltage	$V_{CC} = 4.5V$ , $I_{OH} = Max$		2.4	3.2		V
			$I_{OH} = -2 \text{ mA}, V_{CC} = 4.5$	V to 5.5V	V <sub>CC</sub> – 2			
V <sub>OL</sub>	LOW Level Outp	out Voltage	$V_{CC} = 4.5V$ , $I_{OL} = Max$			0.35	0.5	V
I <sub>I</sub>	Input Current @	Max. Input	$V_{CC} = 5.5V, V_{IH} = 7V$	A, B, $\overline{G}$			0.1	mA
	Voltage			Select			0.2	
I <sub>IH</sub>	HIGH Level Inpu	ut Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$	A, B, $\overline{G}$			20	μA
				Select			40	1
I <sub>IL</sub>	I <sub>IL</sub> LOW Level Input Current		CC ** , IL	Select			-1	mA
				All Others			-0.5	
I <sub>O</sub> <sup>(1)</sup>	Output Drive Current		$V_{CC} = 5.5V, V_{O} = 2.25V$		-30		-112	mA
I <sub>OZH</sub>	Off-State Output Current, HIGH Level Voltage Applied		$V_{CC} = 5.5V, V_{O} = 2.7V$				-50	μA
I <sub>OZL</sub>	Off-State Output LOW Level Volta	,	$V_{CC} = 5.5V, V_{O} = 0.4V$				-50	μA
I <sub>CCH</sub>	Supply Current	DM74AS257	$V_{CC} = 5.5V,$	Outputs HIGH		12.9	19.7	mA
		DM74AS258	Outputs Open			8.8	13.5	
I <sub>CCL</sub>	Supply Current	DM74AS257	Outputs LOW	Outputs LOW		19	30.6	mA
		DM74AS258				15.8	24.6	1
I <sub>CCZ</sub>	Supply Current	Current DM74AS257 Out	Outputs		19.7	31.9	mA	
		DM74AS258	Disabled	Disabled		15.5	25.2	1

#### Note:

1. The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I<sub>OS</sub>.

# **DM74AS257 Switching Characteristics**

Over recommended operating free air temperature range.

Symbol	Parameter	Conditions	From	То	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output	$V_{CC} = 4.5V \text{ to } 5.5V,$ $C_L = 50 \text{ pF},$	Data	Any Y	1	5.5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output	$R_L = 500\Omega$			1	6	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		Select	Any Y	2	11	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output				2	10	ns
t <sub>PZH</sub>	Output Enable Time to HIGH Level		OUTPUT Control	Any Y	2	7.5	ns
t <sub>PZL</sub>	Output Enable Time to LOW Level				2	9.5	ns
t <sub>PHZ</sub>	Output Disable Time from HIGH Level		OUTPUT Control	Any Y	1.5	6.5	ns
t <sub>PLZ</sub>	Output Disable Time from LOW Level				2	7	ns

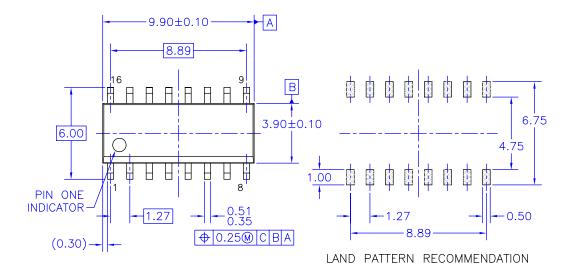
# **DM74AS258 Switching Characteristics**

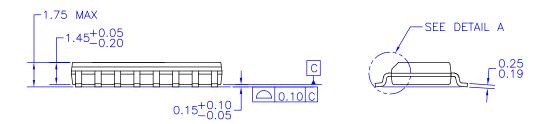
Over recommended operating free air temperature range.

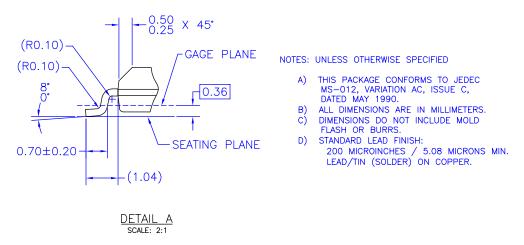
Symbol	Parameter	Conditions	From	То	Min.	Max.	Units
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output	$V_{CC} = 4.5V \text{ to } 5.5V,$ $C_L = 50pF,$	Data	Any Y	1	5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output	$R_L = 500\Omega$			1	4	ns
t <sub>PLH</sub>	Propagation Delay Time, LOW-to-HIGH Level Output		Select	Any Y	2	9.5	ns
t <sub>PHL</sub>	Propagation Delay Time, HIGH-to-LOW Level Output				2	10	ns
t <sub>PZH</sub>	Output Enable Time to HIGH Level		OUTPUT Control	Any Y	2	8	ns
t <sub>PZL</sub>	Output Enable Time to LOW Level				2	10	ns
t <sub>PHZ</sub>	Output Disable Time, from HIGH Level		OUTPUT Control	Any Y	1.5	6	ns
t <sub>PLZ</sub>	Output Disable Time from LOW Level				2	6.5	ns

# **Physical Dimensions**

Dimensions are in millimeters unless otherwise noted.







M16AREVK

Figure 1. 16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Package Number M16A

# Physical Dimensions (Continued)

Dimensions are in inches (millimeters) unless otherwise noted.

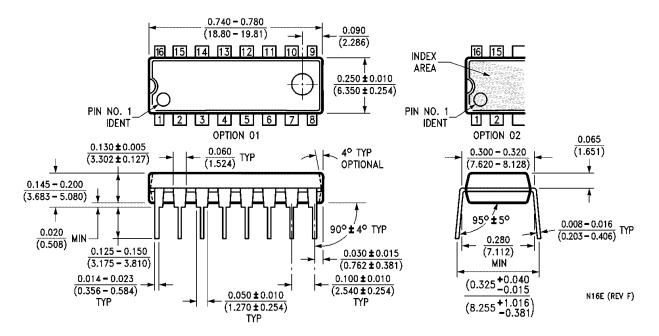


Figure 2. 16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide Package Number N16E





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