# **BU406, BU407**

# **NPN Power Transistors**

These devices are high voltage, high speed transistors for horizontal deflection output stages of TV's and CRT's.

#### **Features**

- High Voltage
- Fast Switching Speed
- Low Saturation Voltage
- These Devices are Pb-Free and are RoHS Compliant\*

#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector–Emitter Voltage	BU406 BU407	V <sub>CEO</sub>	200 150	Vdc
Collector-Emitter Voltage	BU406 BU407	V <sub>CEV</sub>	400 330	Vdc
Collector-Base Voltage	BU406 BU407	V <sub>CBO</sub>	400 330	Vdc
Emitter-Base Voltage		V <sub>EBO</sub>	6	Vdc
Collector Current – Continuous – Peak Repetitiv	e	I <sub>C</sub>	7 10	Adc
Collector Current - Peak (10 ms)		I <sub>CM</sub>	15	Adc
Base Current		I <sub>B</sub>	4	Adc
Total Device Dissipation @ T <sub>C</sub> = 25 Derate above 25°C	°C	P <sub>D</sub>	60 0.48	W W/°C
Operating and Storage Junction Temperature Storage		T <sub>J</sub> , T <sub>stg</sub>	-65 to 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.08	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	70	°C/W
Maximum Lead Temperature for Soldering Purposes1/8" from Case for 5 Seconds	TL	260	°C

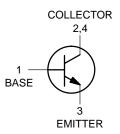


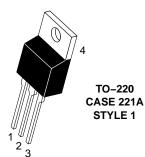
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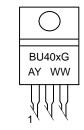
# NPN SILICON POWER TRANSISTORS 7 AMPERES – 60 WATTS 150 AND 200 VOLTS

#### **SCHEMATIC**





#### MARKING DIAGRAM



BU40x = Specific Device Code

x = 6 or 7

A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
BU406G	TO-220AB (Pb-Free)	50 Units / Rail
BU407G	TO-220AB (Pb-Free)	50 Units / Rail

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## BU406, BU407

### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector–Emitter Sustaining Voltage (Note 1) (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	BU406 BU407	V <sub>CEO(sus)</sub>	200 150	_ _	_ _	Vdc
Collector Cutoff Current		I <sub>CES</sub>	- - -	- - -	5 0.1 1	mAdc
Emitter Cutoff Current (V <sub>EB</sub> = 6 Vdc, I <sub>C</sub> = 0)	BU406, BU407	I <sub>EBO</sub>	-	_	1	mAdc
ON CHARACTERISTICS (Note 1)						
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 5 Adc, I <sub>B</sub> = 0.5 Adc)		$V_{CE(sat)}$	_	_	1	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 5 Adc, I <sub>B</sub> = 0.5 Adc)		V <sub>BE(sat)</sub>	-	_	1.2	Vdc
Forward Diode Voltage (I <sub>EC</sub> = 5 Adc) "D" only		V <sub>EC</sub>	-	_	2	Volts
DYNAMIC CHARACTERISTICS						
Current–Gain – Bandwidth Product (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 20 MHz)		f <sub>T</sub>	10	-	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1 MHz)		C <sub>ob</sub>	_	80	_	pF
SWITCHING CHARACTERISTICS	<u>.</u>		-		•	•
Inductive Load Crossover Time $(V_{CC} = 40 \text{ Vdc}, I_C = 5 \text{ Adc}, I_{B1} = I_{B2} = 0.5 \text{ Adc}, L_{CC} = 0.5 \text{ Adc}$	_ = 150 μH)	t <sub>c</sub>	_	_	0.75	μs

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

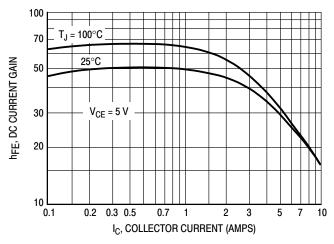


Figure 1. DC Current Gain

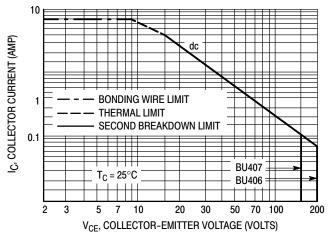


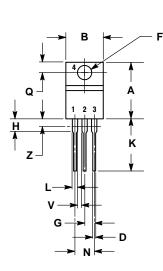
Figure 2. Maximum Rated Forward Bias Safe Operating Area

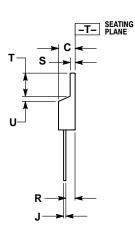
<sup>1.</sup> Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 1%.

#### BU406, BU407

#### PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH** 





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 1:

BASE PIN 1.

- COLLECTOR
- **EMITTER** 3
- COLLECTOR

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