

Product Overview

AP0100CS: Image Signal Processor, 1 MP

For complete documentation, see the data sheet.

AP0100CS is a dedicated image co-processor that enables flexible camera platforms using high performance ON Semiconductor megapixel high dynamic range (HDR) sensors. The two-chip solution of sensor and co-processor allows for multiple camera price and performance points with re-use of circuit board design, fast time to market and design flexibility. Sensor performance is enhanced by the reduction in heat resulting from a separate co-processor chip while at the same time enabling high performance features. The AP0100CS supports 185-degree fisheye lens distortion correction, perspective correction and multiple view options such as split side view, triptych and trailer hitch.

Features

- High Dynamic Range

Applications

- Security

Part Electrical Specifications

Product	Pricing (\$/Unit)	Compliance	Status	Frame Rate (fps)	Video	Output Format	Compatible Sensors	Package Type
AP0100CS2L00SUGA 0-DR		Pb-free	Active	45	1.2 MP/45 fps 720p/60 fps	NTSC/PAL	AR0130	VFBGA-100
		Halide free				YUV	AR0140	
							MT9M034	
AP0100CS2L00SUGA 0-DR1		Pb-free	Active	45	720p/60 fps 1.2 MP/45 fps	NTSC/PAL	MT9M034	VFBGA-100
		Halide free				YUV	AR0130	
							AR0140	

For more information please contact your local sales support at www.onsemi.com.

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