

BLS2933-100

Microwave power LDMOS transistor Rev. 2 — 1 September 2015



Product profile 1.

1.1 General description

100 W LDMOS power transistor (at a supply voltage of 32 V) for S-band radar applications in the 2.9 GHz to 3.3 GHz frequency range.

Table 1: **Typical performance**

Mode of operation	f (GHz)	V _{DS} (V)	P _L (W)	G _p (dB)	η₀ (%)	l _{Dq} (mA)	
class AB	2.9 to 3.3	32	100	8	40	20	

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Easy power control
- Integrated ESD protection
- Excellent ruggedness
- Excellent thermal stability
- Designed for broadband operation (2.9 GHz to 3.3 GHz)
- Internally matched for ease of use

1.3 Applications

S-band radar applications

2. Pinning information

Pin	Description	Simplified outline	Symbol
1	drain		
2	gate		1 لـــــا
3	source		
			3 sym112

3. Ordering information

Table 3. Ordering information				
Type number Package				
	Name	Description	Version	
BLS2933-100	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A	

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-	15	V
I _D	drain current		-	12	А
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
Z _{th(j-h)}	transient thermal impedance from junction to heatsink	T _h = 25 °C; t _p = 200 μs; δ = 12 %	0.4	K/W

6. Characteristics

Table 6. $T_j = 25 \ ^{\circ}C$	Characteristics unless otherwise specified.					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V_{GS} = 0 V; I _D = 2.1 mA	65	-	-	V
$V_{\text{GS(th)}}$	gate-source threshold voltage	V _{DS} = 10 V; I _D = 180 mA	2.5	3.1	3.5	V
V_{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _{DS} = 900 mA	-	3.3	4.5	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	2	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 9 V;$ $V_{DS} = 10 V$	27	30	-	А
I _{GSS}	gate leakage current	V_{GS} = 15 V; V_{DS} = 0 V	-	-	200	nA
g fs	forward transconductance	V_{DS} = 10 V; I _D = 10 A	-	9.0	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 6 V;$ $I_D = 6 A$	-	0.09	-	Ω
C _{rs}	feedback capacitance	V _{GS} = 0 V; V _{DS} = 28 V; f = 1 MHz	-	2.5	-	pF

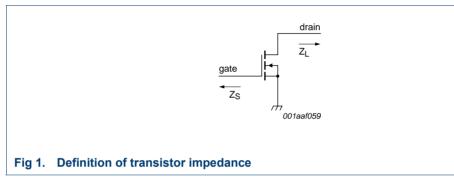
7. Application information

Table 7. Application information

RF performance in common source class-AB circuit; $T_h = 25$ °C; $t_p = 200 \ \mu$ s; $\delta = 12 \%$; $Z_{th(mb-h)} = 0.15 \ \text{K/W}$; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
f _{oper}	operating frequency		2.9	-	3.3	GHz
V _{CC}	supply voltage		-	-	32	V
t _p	pulse duration		-	200	-	μs
δ	duty cycle		-	12	-	%
PL	output power		100	-	-	W
$P_{L(1dB)}$	output power at 1 dB gain compression		-	120	-	W
G _p	power gain		6	8	-	dB
η_D	drain efficiency		33	40	-	%
P _{droop(pulse)}	pulse droop power		-	0.1	0.5	dB
t _r	rise time		-	20	50	ns
t _f	fall time		-	6	50	ns
VSWR load	load voltage standing wave ratio		10 : 1	-	-	
IRL	input return loss		-	-10	-	dB

Table 8.	Typical impedance		
f		Z _S	ZL
GHz		Ω	Ω
2.9		3.3 – j5.6	3.5 – j3.3
3.0		3.7 – j5.3	3.1 – j3.6
3.1		5.9 – j5.8	3.3 – j3.3
3.2		6.8 - j3.4	3.2 – j3.5
3.3		6.6 – j2.7	3.1 – j3.6



7.1 Ruggedness in class-AB operation

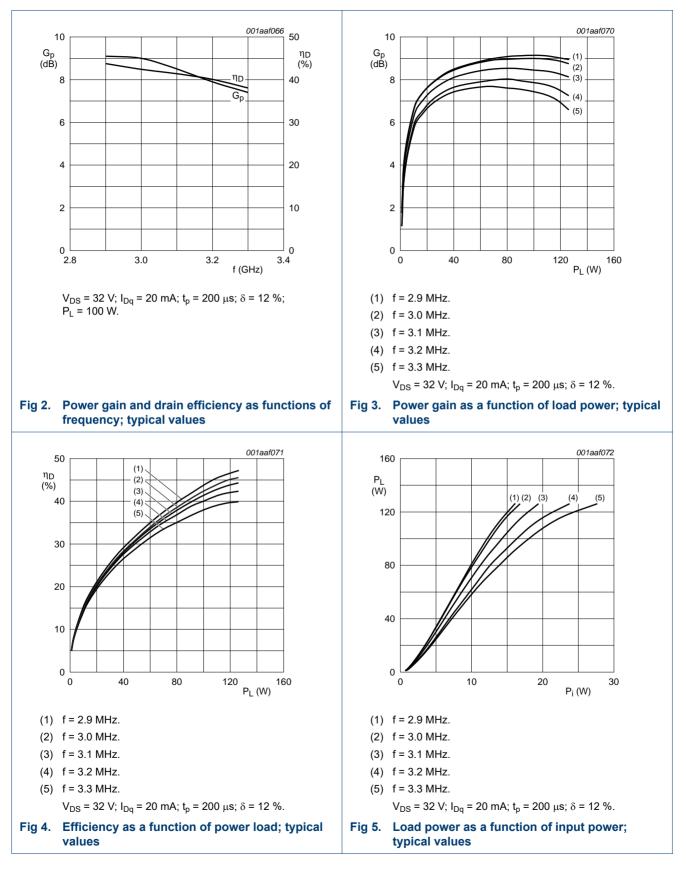
The BLS2933-100 is capable of withstanding a load mismatch corresponding to VSWR > 10 : 1 through all phases under the following conditions: V_{DS} = 32 V; I_{Dq} = 20 mA; P_L = 100 W pulsed, t_p = 200 µs; δ = 12 %.

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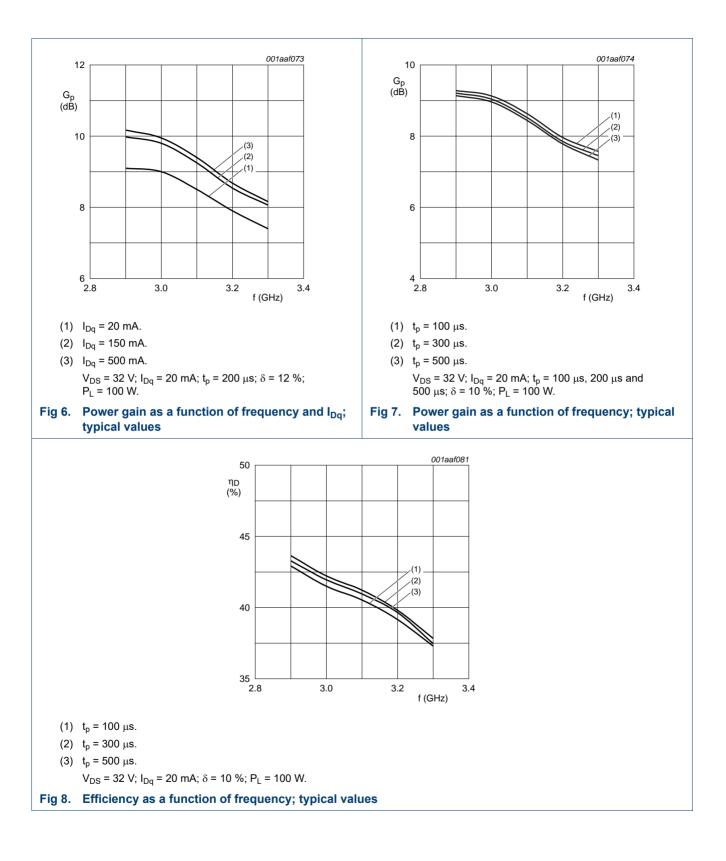
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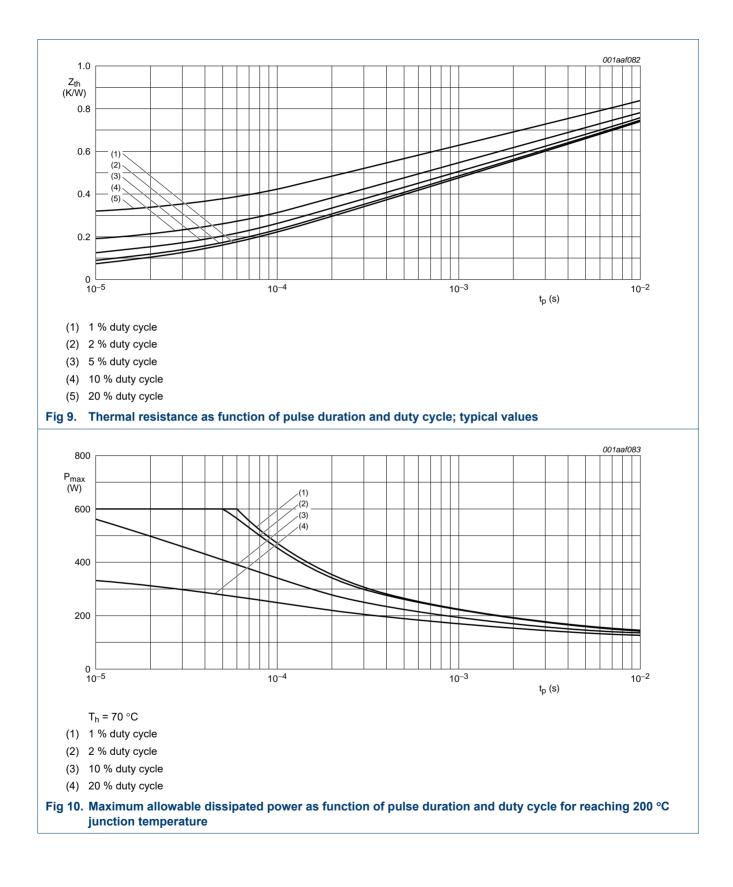
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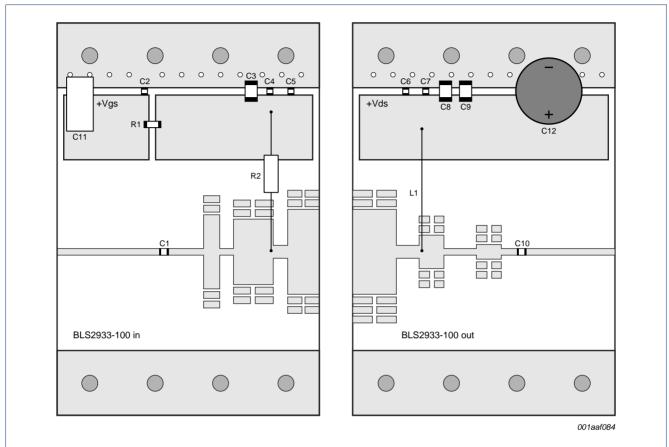
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8. Test information



The components are situated on one side of the copper-clad Duroid 6006 Printed-Circuit Board (PCB). Both the input and output PCB are 40 mm \times 60 mm with ϵ_r = 6.15 and thickness 0.64 mm.

See Table 9 for list of components

Fig 11. Component layout for test circuit

Table 9. List of components (see Figure 11)

Component	Description	Value	Dimensions	Catalogue number
C1, C2, C4, C5, C6, C7, C10	multilayer ceramic chip capacitor [1]	22 pF		
C3, C8, C9	multilayer ceramic chip capacitor [2]	470 pF		
C11	tantalum capacitor	4.7 μF; 50 V		Kemet T491D475K050AS
C12	electrolytic capacitor	220 μF; 63 V		
R1	resistor	560 Ω	SMD 0805	
R2	metafilm resistor	49.9 Ω; 0.6 W		
L1	copper wire 1 mm diameter		length of loop = 20 mm; height of loop = 10 mm	
N1	N-connector male			Suhner 13N-50-057/1
N2	N-connector female			Suhner 23N-50-057/1

[1] American Technical Ceramics type 100A or capacitor of same quality.

[2] American Technical Ceramics type 100B or capacitor of same quality.

BLS2933-100#2

9. Package outline

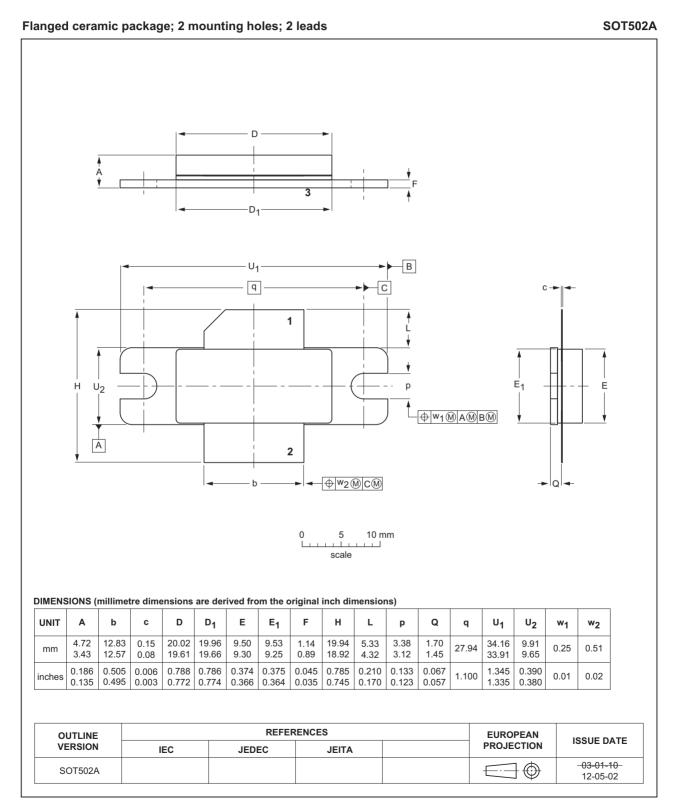


Fig 12. Package outline SOT502A

BLS2933-100#2 Product data sheet

10. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BLS2933-100#2	20150901	Product data sheet	-	BLS2933-100_1	
Modifications:	• The format of of Ampleon.	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.			
	 Legal texts have been adapted to the new company name where appropriate. 			appropriate.	
BLS2933-100_1	20060801	Product data sheet	-	-	

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11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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BLS2933-100#2

13. Contents

1	Product profile 1
1.1	General description 1
1.2	Features
1.3	Applications 1
2	Pinning information 2
3	Ordering information 2
4	Limiting values 2
5	Thermal characteristics 2
6	Characteristics 3
7	Application information
7.1	Ruggedness in class-AB operation 4
8	Test information 8
9	Package outline 9
10	Revision history 10
11	Legal information 11
11.1	Data sheet status
11.2	Definitions 11
11.3	Disclaimers
11.4	Trademarks 12
12	Contact information 12
13	Contents

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