

MU100HxxxAQ_CLKS Series

General - Outdoor

DWG NO. : MSSD-5817 A0

03		 Features · Input voltage: 90-305VAC Built-in active PFC function: 0.99 Typ. Low THD: 10% Typ. High efficiency: 91% Typ. IP67 design for indoor or outdoor installations High surge immunity Support Time-shared dimming function Compliance to worldwide safety regulations for lighting Suitable for dry/damp locations 																			
03			C	C 0	E348796	(M.		\bigcirc	\vee		1										
	5 045	053	070	085	105	120	140	175	210	245	280	300	315	350	420						
_KS)																					
Typ.) _{Note.1} 899		89%	89%	88%	88%	88%	88%	88%	87%	87%	87%	86%	86%	86%	86%						
												88%									
pte.2	90 ~ 305Vac, OR 127~ 430Vdc (Derating may be need under low inputs, Refer to 'Derating Curve') 100Vac-277Vac																				
.2																					
Hz)	47~63																				
	0.99 (Typ.) with 80%~100% load,at 120Vac																				
	0.96 (Typ.) with 80%~100% load,at 230Vac																				
	>0.9 with 80%~100% load,at 277Vac																				
	<15% (typical), at 100~277Vac input, with 80%~100% load conditions																				
	<10% (typical), at 220Vac/50Hz input, with 80%~100% load conditions																				
	1.2A at 100VAC input, 0.6A at 230VAC																				
	A at 230Vac input 25°C Cold Start (time wide=500uS, measured at 50% Ipeak,Not applicable for the inrush current to Noise Filter for less than 0.2m 1mA at 277Vac/60Hz																				
ax.)															1						
		2294~188							23~46		17~34		15~30.5		12~23						
350		530	700	850	1050	1200	1400	1750	2100	2450	2800	3000	3150	3500	4200						
100	0 100	100	100	100	100	100	100	96	96	96	96	100	96	96	96						
nt(Typ.)	≤30%((PK-AV) /AV) with LED default mode and full load)																				
	±5%																				
	±1%																				
	±3%																				
9	·																				
	<1.2s, at 120Vac; <0.6s, at 277Vac																				
373	373 289 230 186 143 124 103 92 72 60 51 44 43 40 35 30 Protection type: Voltage limiting.output will not exceed the upper limit voltage , recovers automatically after fault condition is removed.																				
	Protection type: Constant current limiting.																				
			F	rotection	type: Hic	cup mod	e. recover	s automa	tically aft	er short is	s remove	d.									
	When the Tc of PSU rise to 90~110°C, Decreases output current, returning to normal after over temperature is removed.																				
	-40~+70°C(Refer to 'Derating Curve')																				
							90 °C	max													
							10~10	0%RH													
nidity						-4	0~+80°C ,	5-100%l	RH												
						(0.03%/°C	(0~50℃)												
	10-500Hz,5G 12min/cycle, period for 72min each along X, Y, Z axes																				
	UL87	50, UL1012	2, UL1310) Class 2	,CSA-C22	2.2 No. 10	7.1, CSA	C22.2	NO. 223-	M91 Cla	ss 2,EN	61347-1	EN613	47-2-13							
					I/P-O/F	2:3.75KVa	ic I/P-FG	6:1.875K\	O/P-FC	G:1.5KV											
•				I/F	P-O/P, I/P	P-FG, O/F	-FG:100N	1 Ohms/5	00Vdc/25	°C/70%F	۲H										
l l				EN5	5015/FC	C Part 15	, EN610	00-3-2 C	lass C, E	EN61000)-3-3										
ľ				EN61000	-4-2,3,4,5	,6,8,11	Surge L,	N-FG 6k	V, L-N	4KV),	EN6154	17									
	300,000 Hours, measured at full load, 25°C ambient temperature																				
				- ,						, -											
									,												
		eady-state temperature in 25°C			U// U// ENE ENE EN61000 300 50,	I/P-O/F I/P-O/P, I/F EN55015/FC EN61000-4-2,3,4,5 300,000 Hou 50,000 Hour	I/P-O/P:3.75KVa I/P-O/P, I/P-FG, O/P EN55015/FCC Part 15 EN61000-4-2,3,4,5,6,8,11 300,000 Hours, measu 50,000 Hours at Tc 74 187 x 6	I/P-O/P:3.75KVac I/P-FG I/P-O/P, I/P-FG, O/P-FG:100M EN55015/FCC Part 15, EN610 EN61000-4-2,3,4,5,6,8,11 S00,000 Hours, measured at full 50,000 Hours at Tc 75°C (Refer 187 x 67.5 x 40 (0.8	I/P-O/P:3.75KVac I/P-FG:1.875KV I/P-O/P, I/P-FG, O/P-FG:100M Ohms/5 I/P-O/P, I/P-FG, O/P-FG:100M Ohms/5 EN55015/FCC Part 15, EN61000-3-2 C EN55015/FCC Part 15, EN61000-3-2 C EN61000-4-2,3,4,5,6,8,11 (Surge L,N-FG 6K 300,000 Hours, measured at full load,25 °C 50,000 Hours at Tc 75 °C (Refer to "Life Ti 187 x 67.5 x 40 (mm) (Lx 0.85kg	I/P-O/P:3.75KVac I/P-FG:1.875KV O/P-FG I/P-O/P, I/P-FG, O/P-FG:100M Ohms/500Vdc/25 EN55015/FCC Part 15, EN61000-3-2 Class C, E EN61000-4-2,3,4,5,6,8,11 Surge L,N-FG 6KV, L-N 300,000 Hours,measured at full load,25°C ambient 50,000 Hours at Tc 75°C (Refer to"Life Time VS. T 187 x 67.5 x 40 (mm) (LxWxH) 0.85kg	I/P-O/P:3.75KVac I/P-FG:1.875KV O/P-FG:1.5KV I/P-O/P, I/P-FG, O/P-FG:100M Ohms/500Vdc/25°C/70%F EN55015/FCC Part 15, EN61000-3-2 Class C, EN61000 EN55015/FCC Part 15, EN61000-3-2 Class C, EN61000-4-2,3,4,5,6,8,11 Surge L,N-FG 6KV, L-N 4KV), 300,000 Hours, measured at full load,25°C ambient tempera 50,000 Hours at Tc 75°C (Refer to*Life Time VS. Tcase (Re 187 x 67.5 x 40 (mm) (LxWxH) 0.85kg	I/P-O/P:3.75KVac I/P-FG:1.875KV O/P-FG:1.5KV I/P-O/P, I/P-FG, O/P-FG:100M Ohms/500Vdc/25°C/70%RH EN55015/FCC Part 15, EN61000-3-2 Class C, EN61000-3-3 EN61000-4-2,3,4,5,6,8,11 Surge L,N-FG 6KV, L-N 4KV), EN6154 300,000 Hours,measured at full load,25°C ambient temperature 50,000 Hours at Tc 75°C (Refer to*Life Time VS. Tcase (Ref.)*) 187 x 67.5 x 40 (mm) (LxWxH) 0.85kg	I/P-O/P, I/P-FG, O/P-FG:100M Ohms/500Vdc/25°C/70%RH EN55015/FCC Part 15, EN61000-3-2 Class C, EN61000-3-3 EN61000-4-2,3,4,5,6,8,11 (Surge L,N-FG 6KV, L-N 4KV), EN61547 300,000 Hours, measured at full load,25°C ambient temperature 50,000 Hours at Tc 75°C (Refer to"Life Time VS. Tcase (Ref.)") 187 x 67.5 x 40 (mm) (LxWxH)								

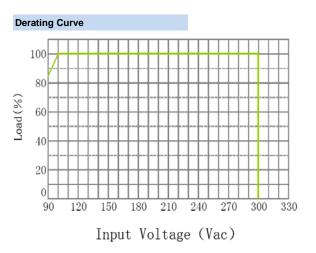
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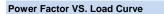
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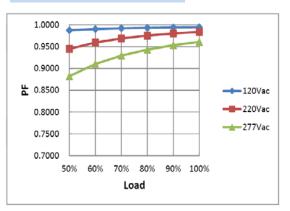


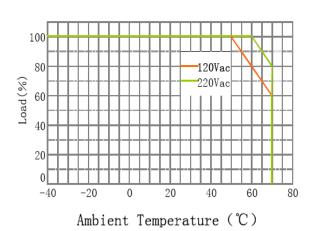
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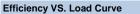
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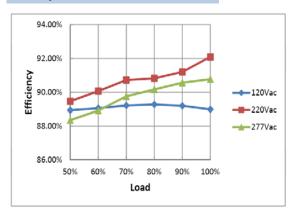


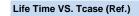


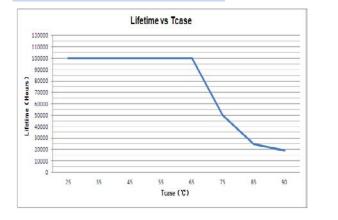




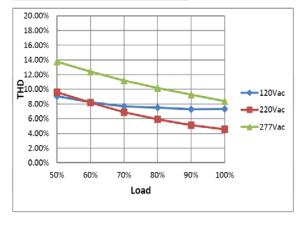












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Instruction





The programmable driver can be programmed by using special PC software and the programmer module.

2.Dimming Interface Description

Pin description				CLKS DIMMING PROGRAMMING INTERFACE
Pin	Name	Value	Description	Vaux 12V / YE(黄色)
1	Vaux 12V	10.8V-13.2V	Passive dimmers power supply	1
2	Dim+/Program	0-10V	Dimming/Programming input	Dim+ Program ^{/ PU(} 紫色)
3	Dim-	0V	DC Ground	2
-			•	Dim- / GR(灰色)

3.Dimming Software Function Instruction

Adjustable Output Current(AOC)



Users can set the rated current between 10%*Max Current and 100%*Max Current

PWM

Input a PWM signal from the 2nd pin(Dim+/Program) of the dimming interface to change the output current. User can set "Positive Logic" or " Negative Logic" of the PWM signal. PWM duty circle: 1%~99%(it has both positive and negative logics), frequency: 500Hz~5kHz, 3V~10V is

Adjustable Startup Time(AST)



Set driver's "Start Fade up Time". It means how much time the driver costs to achieve the "Module Current " that the user set. The valid value is 0s, 1s, 2s, 5s, 10s, 20s, 40s.

Set driver's "Fade up Time". This function is available in the Smart Midnight ClockDIM and Fixed ClockDIM mode; It means how much time the driver costs to achieve another dimming level from previous dimming level. The valid value is 0s, 1s, 2s, 5s, 10s, 20s, 40s.

Dimming Interface Selection(DIS) O 1-10V PWM O Smart Midnight ClockDIM Fixed ClockDIM No Dimming PWM Logic(PWML) Positive Or Negative Logic + Output current percentage 100

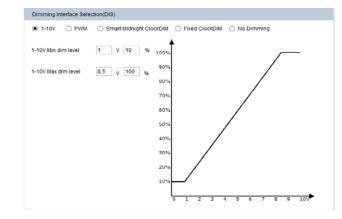
Fade Time(FT) Fadeup Time



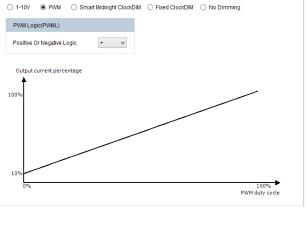
Fade Time(FT)

■ 1-10V

Allow users to set the max and min output current and corresponding output voltage to clarify the 1-10V dimming curve. Input a 0~10V signal from 2nd pin of the dimming interface. Default: input \leq 1V, output current 10%; input \geq



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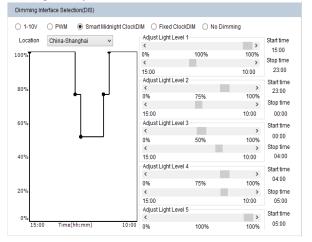


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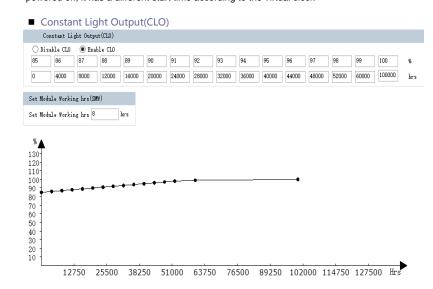
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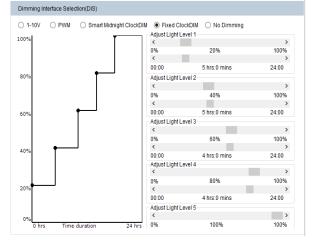
Integrated Dynadimmer



Integrated Dynadimmer allows dimming to predefined light levels based on the nightly operating time. With flexibility in setting time and light levels, the user can configure the driver for specific locations and application needs. Using Integrated Dynadimmer, it is possible to set up to 5 dim levels and time intervals. The driver does not have a real time clock. Instead it runs a virtual clock, determined by the length of nightly operating hours. After 3 ON-OFF cycles, the driver will calculate the virtual clock time. A valid ON-time is defined as a period during which the driver operates continuously for ≥ 4 hours to \leq 24 hours. For example, if the requirement in summer is: 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75% (other time 100% or Off). The driver should be powered on for 7h, so it can calculate the virtual clock time as 22:00. Then we can set the dimming plan: 22:00~23:00: 100%, 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75%. From summer to winter, the valid ON-time changes day by day. The driver should be powered on for 17h in winter, and it also can calculate the virtual clock time as 17:00. Then the dimming plan is 17:00~23:00: 100%, 23:00-00:00: 75%, 00:00-04:00: 50%, 04:00-05:00: 75%, 05:00~10:00: 100%. From the above, if we set the dimming plan as shown in the picture, after repeating the driver ON-time for 3 consecutive days, the dimming plan takes effect from the 4th day onwards. Each day the driver powered on, it has a different start time according to the virtual clock



Integrated Dynadimmer Time Based



Allow users to separate 24hrs into 5 sections and corresponding output current.

No Dimming

Dimming Interface Selection(DIS)

○ 1-10V ○ PWM ○ Smart Midnight ClockDIM ○ Fixed ClockDIM ● No Dimming

The driver will be in constant output mode.

Set MODULE Working hrs(SMW)

Set Module Working hrs(SMW)

Set Module Working hrs 10 hrs

User can check how much time the driver works through this function.

Traditional light sources suffer from depreciation in light output over time. This applies to LED light sources as well. The CLO feature enables LED solutions to deliver constant lumen output through the life of the light engine. Based on the type of LEDs used, heat sinking and driver current, it is possible to estimate the depreciation of light output for specific LEDs and this information can be entered into the driver. The driver counts the number of light source working hours and will increase output current based on this input to enable CLO.

When the CLO feature is enabled, the driver nominal output current will be defined by the CLO percentage as shown by the equation below: Driver target nominal output current = CLO percentage * AOC. For example, in the CLO profile shown in Figure, between 52,000-60,000 working hours, the CLO percentage is set at 98%. Assuming the nominal AOC is set to 500mA, the driver output current with CLO enabled will be 0.98 x 500 = 600 mA.

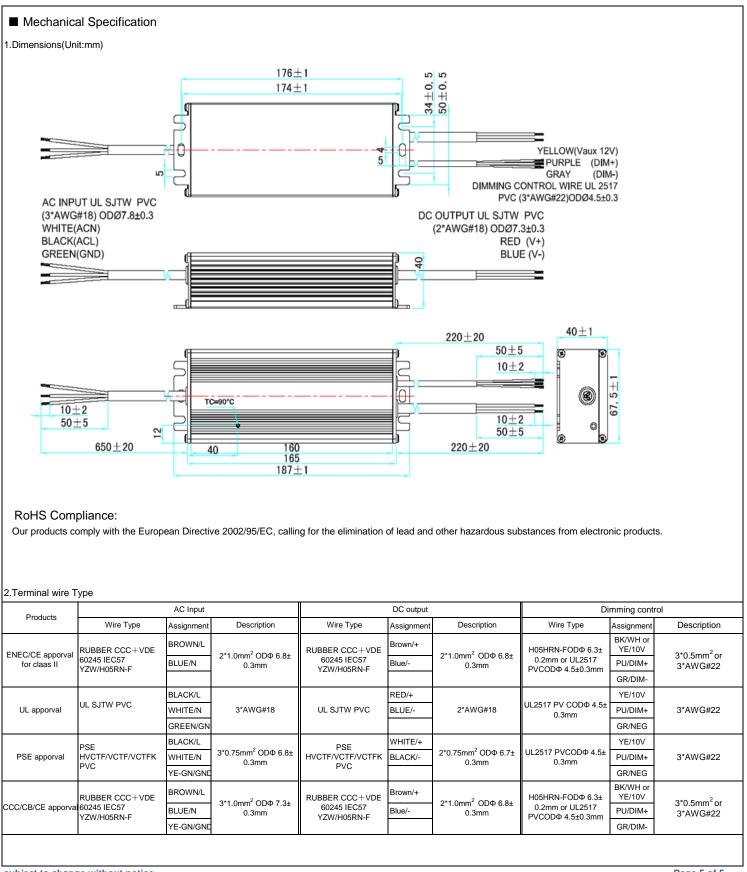
The CLO percentage can be set to a value between 85%-100%, in increments of 1%. The LED module working house can be set at an unable between (0)

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moving in better ways

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