

# Reverse Right Angle Mini DIMM, 200 / 244 Ckt 0.60mm pitch SMT

#### 1.0 SCOPE

This Product Specification covers the performance requirements of the 0.60 mm centerline edge card socket for board to board interconnect of 1.00 mm thick memory modules.

### 2.0 PRODUCT DESCRIPTION

## 2.1 PRODUCT NAME AND SERIES NUMBER (S)

<u>Series Number</u> <u>Product Descriptions</u>

78001 200/244 Ckt Reverse Right Angle Mini DIMM

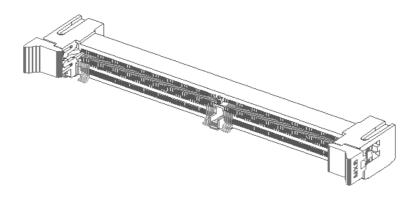
# 2.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKINGS

See the appropriate Sales Drawings for information on dimensions, materials, plating and markings, recommended module outlines and footprint Specifications.

### 2.3 SAFETY AGENCY APPROVALS

UL File : E29179

CSA File : 1699020 (LR 19980)



| REVISION:        | ECR/ECN         | NINFORMATION:            | TITLE: Reverse Ri                           | ght Angle Mini DIMM, | SMT | SHEET No.  |
|------------------|-----------------|--------------------------|---|----------------------|-----|------------|
| В                | EC No:<br>DATE: | S2007-1019<br>2007/05/17 | 200/2                                       | <b>1</b> of <b>6</b> |     |            |
| DOCUMENT NUMBER: |                 |                          | CREATED / REVISED BY: CHECKED BY: APPRO     |                      |     | OVED BY:   |
| PS-78001-001     |                 |                          | CM TEO 2007/05/17 YT YANG 2007/05/25 SH LEN |                      |     | 2007/05/25 |



### 3.0 APPLICABLE DOCUMENTS AND SPECIFICATIONS

The following documents are part of this specification between the requirements of this specified herewith. In the event of conflict between the requirements of this specification and the product drawings, the product drawings shall take precedence. In the event of conflict between the requirements of this specification and reference documents, this specification shall take precedence.

## 4.0 RATINGS

### 4.1 VOLTAGE

30 VRMS at 60 Hz

### **4.2 CURRENT**

1.0 Amps at 30°C Temperature Rise

## **4.3 TEMPERATURE**

Operating Temperature: -55°C to +85°C

#### 5.0 PERFORMANCE

### **5.1 ELECTRICAL REQUIREMENTS**

| ITEM | DESCRIPTION                           | TEST CONDITION   | REQUIREMENT  |
|------|---------------------------------------|--|--|
| 1    | Contact<br>Resistance<br>(Low Level)  | Mate connectors: apply a maximum voltage of 20 mV and a current of 100 mA. EIA-364-23  | $50~\text{m}\Omega$ maximum at initial $10~\text{m}\Omega$ maximum change from initial |
| 2    | Temperature Rise at rated current     | Temperature of mater connector at rated current for 96 hours (6 consecutive ckts link in series)                               | 1.0 Amps per contact at a maximum of 30°C temperature rise                             |
| 3    | Insulation<br>Resistance              | Unmate & unmount connectors: apply a voltage of 500 VDC between adjacent terminals and between terminals to ground. EIA-364-21 | 1000 Mega Ω minimum  |
| 4    | Dielectric<br>Withstanding<br>Voltage | Apply 500 VAC for 1 minute between adjacent terminals of an unmated connector. EIA-364-20                                      | No breakdown or flashover  |

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| В            | DATE:    | 2007/05/17            | 200/2                                     | <b>2</b> of <b>6</b> |       |            |
| D            | EC No:   | S2007-1019            | Connector<br>200/244Ckt, 0.60mm Pitch     |                      |       | 2 (6       |
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# **5.2 MECHANICAL REQUIREMENTS**

| ITEM | DESCRIPTION                               | TEST CONDITION  | REQUIREMENT   |
|------|---|---|---|
| 5    | Vibration                                 | Amplitude: 1.52mm peak to peak Sweep: 10-55-10 Hz in one min. Duration: 2 hrs each on XYZ axis Module weight: 15g for 244Ckt and 13g for 200Ckt. EIA 364-28                                     | No change in LLCR greater than $10m\Omega$ from initial. Discontinuity : No greater than 1.0 micro sec. |
| 6    | Shock<br>(Mechanical)                     | Mate connectors and shock at 30 g's with half -sine waveform for 11 milliseconds, 3 shocks in each perpendicular axis (18 shocks total). Module weighted: 15g for 244Ckt and 13g for 200Ckt.    | No change in LLCR greater than $10m\Omega$ from initial. Discontinuity: No greater than 1.0 micro sec.  |
| 7    | Durability                                | Mate and unmated connectors up to 25 cycles at a maximum rate of 10 cycles per minute. Reseating to be done for 3 cycles.   | No change in LLCR greater than 10 m $\Omega$ from initial.  |
| 8    | Module Insertion<br>Force<br>(w/ Latches) | Insert a 1.00 mm thick Module(0.10x0.10 mm chamfer) at a rate of $25 \pm 6$ mm ( $1 \pm \frac{1}{4}$ inch) per minute. See Sales drawing for PCB/ Module details.                               | Total insertion force not to exceed: 195.0 N (43.73lbs) for 200 ckt 238.0 N (53.37lbs) for 244 ckt      |
| 9    | Module Rip Out<br>Force                   | Apply a pulling force on module card at a rate of $25 \pm 6$ mm/min. $(1 \pm \frac{1}{4}$ inch) with recommended test module as per sales drawing, inserted into connector with latches closed. | 35.0 N (7.85lbs) min. retention force of the module in connector with no damage                         |
| 10   | Latch Actuation<br>Force                  | Apply an actuation force on each latch at a rate of 25 ± 6 mm/ min (1 ± ¼ inch) with recommended test module as per sales drawing inserted into connector.                                      | The force to fully actuate the latch open shall be 35 N (7.85 lbs) max. per latch.                      |
| 11   | Latch Overstress<br>Force                 | Apply an actuation force on each latch at a rate of $25 \pm 6$ mm / min $(1 \pm \frac{1}{4}$ inch) in the fully open position.  | 35 N (7.85 lbs) min force with no damage.   |
| 12   | Terminal<br>Retention Force               | Axial pullout force on the terminal in the housing at a rate of $25 \pm 6$ mm (1 $\pm \frac{1}{4}$ inch) per minute.  | 3 N minimum   |

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| PS-78001-001                   |                      |                   | CM TEO 2007/05/17 YT YANG 2007/05/25 SH LENI 200 |                      |      | 2007/05/25 |  |  |  |
|                                |                      |                   |  |                      |      |            |  |  |  |



# **5.3 ENVIRONMENTAL REQUIREMENTS**

| ITEM | DESCRIPTION                             | TEST CONDITION  | REQUIREMENT   |
|------|---|---|---|
| 13   | Shock<br>(Thermal)                      | Mate connectors; expose to 5 cycles of:  Temperature °C Duration (Minutes)  -55 +0/-3 30  +25 +10/-5 5 MAXIMUM  +85 +3/-0 30  +25 +10/-5 5 MAXIMUM  EIA-364-32-Test condition I       | No change in LLCR greater than 10 m $\Omega$ from initial.    |
| 14   | Thermal Aging                           | Mate connectors and expose to 500 hours at 105 ± 2°C. Per EIA-364-17 Preconditioning to be done at 105°C for 72 hrs   | No change in LLCR greater than 10 m $\Omega$ from initial.    |
| 15   | Cyclic<br>Temperature &<br>Humidity     | Mate connectors and expose for 10 days at 25°C to 65°C at 90-98% RH. Per EIA-364-31, Method III.  | No change in LLCR greater than 10 m $\Omega$ from initial.    |
| 16   | Solderability                           | Solder time: 5±0.5 seconds. Solder temperature: 260±5°C Subject to steam aging for 8 hours ± 5 mins.  | Solder coverage:<br>95% minimum                               |
| 17   | Porosity                                | Nitric Acid Test, 10 contacts per contact type selected at random. Per EIA 364-53   | Maximum number of pores : 30uin-1 pore per 10 contacts        |
| 18   | Solvent<br>Resistance                   | 42 parts DI water by volume, 1 part of propylene glycol monomethyl ether (Glycolether PM, 1 methoxy-2-propanol). 1 part by volume of monoethanolamine.  MIL – STD – 202F Method 215J. | No Damage or discoloration of connector materials or marking. |
| 19   | Resistance to<br>Soldering Heat<br>Test | Unmated, exposed to reflow profile as defined in Section 8.1.   | No Damage or blistering.                                      |

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| DOCUMENT NUMBER:                   |       |            | CREATED / REVISED BY:                             | CHECKED BY:          | CHECKED BY: APPROVED BY |            |  |
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# **6.0 TEST SEQUENCE**

| Test Description                              | Test Group         |             |                  |        |   |   |   |   |   |     |    |    |
|---|--------------------|-------------|------------------|--------|---|---|---|---|---|-----|----|----|
| Sequence                                      | 1                  | 2           | 3                | 4      | 5 | 6 | 7 | 8 | 9 | 10  | 11 | 12 |
| Contact Resistance                            | 1 9<br>3<br>5<br>7 | 1<br>3<br>5 | 1<br>4<br>6<br>8 |        |   |   |   |   |   |     |    |    |
| Temperature Rise                              |                    |             |                  |        |   |   |   |   |   |     | 1  |    |
| Insulation Resistance                         |                    |             |                  | 1<br>5 |   |   |   |   |   |     |    |    |
| Dielectric Withstand Voltage                  |                    |             |                  | 2      |   |   |   |   |   |     |    |    |
| Vibration                                     | 6                  |             |                  |        |   |   |   |   |   |     |    |    |
| Mechanical Shock                              | 8                  |             |                  |        |   |   |   |   |   |     |    |    |
| Durability (5X) pre-cond.                     | 2                  |             |                  |        |   |   |   |   |   |     |    |    |
| Durability (25X)                              |                    |             | 2                |        |   |   |   |   |   |     |    |    |
| Module Insertion Force                        |                    |             |                  |        | 1 |   |   |   |   |     |    |    |
| Module Ripout Force                           |                    |             |                  |        | 2 |   |   |   |   |     |    |    |
| Latch Actuation Force                         |                    |             |                  |        |   |   |   |   |   |     |    | 1  |
| Latch Overstress Force                        |                    |             |                  |        |   |   |   |   |   |     |    | 2  |
| Thermal Shock                                 |                    |             | 3                | 3      |   |   |   |   |   |     |    |    |
| Thermal Aging 105°C – 500hrs                  |                    | 2           |                  |        |   |   |   |   |   |     |    |    |
| Cyclic Temp & Humidity                        |                    |             | 5                | 4      |   |   |   |   |   |     |    |    |
| Plating thickness                             |                    |             |                  |        |   | 1 |   |   |   |     |    |    |
| Solderability                                 |                    |             |                  |        |   |   |   | 1 |   |     |    |    |
| Porosity                                      |                    |             |                  |        |   |   |   |   | 1 |     |    |    |
| Solvent Resistance                            |                    |             |                  |        |   |   | 1 |   |   |     |    |    |
| Terminal Retention Force                      |                    |             |                  |        |   |   |   |   |   | 1 3 |    |    |
| Resistance to Soldering Heat                  |                    |             |                  |        |   |   |   |   |   | 2   |    |    |
| Temp life (pre-conditioning)<br>105°C – 72hrs | 4                  |             |                  |        |   |   |   |   |   |     |    |    |
| Reseating (3X)                                |                    | 4           | 7                |        |   |   |   |   |   |     |    |    |
| Sample Size per Test Group                    | 5                  | 5           | 5                | 5      | 5 | 5 | 5 | 5 | 5 | 5   | 5  | 5  |

# 7.0 PACKAGING

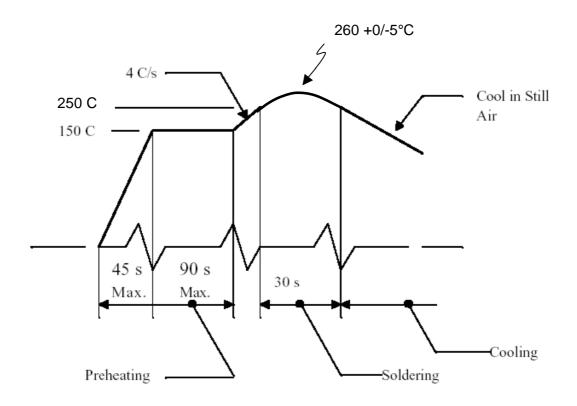
Parts shall be packed in trays and protected against damage during handling, transportation and storage.

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| B EC No: S2007-1019            | 200/2   | Connector<br>44Ckt, 0.60mm Pitch |  | <b>5</b> of <b>6</b> |
| DATE: 2007/05/17               | 200/2   | 3 01 0                           |  |                      |
| DOCUMENT NUMBER:               | CREATED / REVISED BY: CHECKED BY: APPROVED      |                                  |  | OVED BY:             |
| PS-78001-001                   | CM TEO 2007/05/17 YT YANG 2007/05/25 SH LENI 20 |                                  |  | 2007/05/25           |



# **8.0 OTHER INFORMATIONS**

8.1 Reflow Profile.



## Notes:

- 1. Reflow solder Preheat at 3°C/s to 150°C.
- 2. Reflow at 250°C for 30s per figure.
- 3. Peak to be at 260 + 0/-5°C.
- 4. Component must withstand (2) reflow solder cycles with a cool down between.

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