

Pin #	Name VeeT	Function Transmitter Ground	Plug Sequence (from SFP)	Notes Note 6				Inductrial SED Circuit 1	Vision Sabana	
2 3 4	TD- VeeT TD+	Inv. Transmit Data. In Transmitter Ground Transmit Data. In	1 3	Note 9 Note 6 Note 9				Industrial SFP Circuit N	Viring Scheme	
5	VeeT VccT	Transmitter Ground Transmitter Power	1 2	Note 6 3.3 ± 5%, Note 8						
7 8	Ground Ground	Ground Ground								
10	Ground VccR VeeR	Ground Receiver Power Receiver Ground	2	3.3 * 5%, Note 8 Note 6					Manada at the	1 00 29 2 00 28
12	RD+ VeeR	Received Data Out Receiver Ground	3	Note 7 Note 6			<u> </u>		Mounted on the bottom of the Flex	2 00 29 3 00 28 4 00 27
14	RD- VeeR	Inv. Received Data Out Receiver Ground	3 1	Note 7 Note 6			Mounted the top	d on o of the Flex	1 1 30 29	3 000 227 5 000 227 7 000 224 9 000 222 9 000 222 10 000 221 11 000 18
16 17	VeeR VeeR	Receiver Ground Receiver Ground	1	Note 6 Note 6		] \		20	2 3 00 28 4 00 27	
18 19	LOS Rate Select	Loss of Signal Full ar reduced receiver bandwidth	3	Note 5 Note 4, Low or Open - Reduced Bandwidth, High - Full	ull Bandwidth				5 88 26	9 00 22 10 00 21
20	Ground Ground	Ground					Connection inside SFP Cage 4 Pinout defined by SFP-MSA 5	0017 0016 Flex Assembly	6 00 23 7 00 24 8 00 23 9 00 22 10 00 21 11 00 20 12 00 19 18	
22 23	Ground	Ground Ground				. / '	6		9 00 22	
24 25	VeeC MOD-DEF0	Clock Ground Module Definition 0 (Ground)	3	Note 6 Note 3, grounded in module			8	0 0 13 0 0 12	10 00 21 11 00 20 12 00 19	
26 27	MOD-DEF1 MOD-DEF2	Module Definition 1 (Clock) Module Definition 2 (Data)	3	Note 3, 2 wire serial ID interface Note 3, 2 wire serial ID interface						
28 29	TX Disable TX Fault	Transmitter Disable Transmitter Fault Indication	3	Note 2, module disables on high or open Note 1		] )			14 00 17 15 00 16	Customer PCB
30	VeeT	Transmitter Ground	1	.1		]	$\sim$			
							4	🛛 = solder pad (rej	presents the	
							1	connector circuit within the connec	location	
										12
										$\mathbf{h}$
										ý
							$\bigvee$			•
									Coor	nector wiring on Customore PCB
						SFP Pinc	but			nector wiring on Customers PCB
Plug Sequenc		quence during hat plugging of SFP Madule.				1 (key) VeeT	VeeT 20		1 (key) Vee	eT (ground) VeeT (ground) 30
Plug Sequenc 1) TX Fault Ic Pull up vo	an open collector/drai tage between 2.0V and	n output, which should be pulled up with a VccT, R+0.3V. When high, output indicates	4.7K - 10K W resistor on the host t a laser fault of some kind. Low indi	boord. Cades normal operation.		1 (key)     VeeT       2     TXFault       3     TX Disable	VeeT         20           TD-         19           TD+         18		1 (key) Vee 2 TD 3 Ve	eT (ground) VeeT (ground) 30 D- TXFault 29 seT (ground) TX Disable 28
Plug Sequence 1) TX Fault In Pull up vo In the low	an open collector/drai tage between 2.0V and state, the output will be	n output, which should be pulled up with a VccT, R+0.3V, When high, output indicates e pulled to < 0.8V.	a laser fault of some kind. Low ind	dcates normal operation.		1 (key) VeeT 2 TXFault	VeeT         20           TD-         19           TD+         18           VeeT         17		1 (key) Vee 2 TC 3 Ve 4 TC	-         VeeT (ground)         30           -         TXFault         29           0-=         TXTsult         29           est (ground)         TX Disable         28           0+         MOD-DEF(2)         27
Plug Sequence 1) TX Fault le Pull up vo In the low 2) TX disable Its states	an open collector/drai itage between 2.0V and state, the output will be is an input that is use are:	n output, which should be pulled up with a VccT, R+0.3V. When high, output indicates e pulled to < 0.8V. d ta shut down the transmitter optical outp	a laser fault of some kind. Low ind	dcates normal operation.		1 (key)         VeeT           2         TXFault           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15		1 (key) Vee 2 TC 3 Vee 4 TC 5 Ve 6 Vc	eT (ground)         VeeT (ground)         30           D-         TXFoul1         29           seT (ground)         TX Disable         28           D-         MOD-DEF(2)         27           seT (ground)         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25
Plug Sequence 1) TX Fault is Pull up vo in the low 2) TX disable its states Low (0 - 1 (>0.8, < 2	an open collector/drai lage between 2.0V and state, the output will be is an input that is use are: 	n output, which should be pulled up with a VccT, RA3V, When high, output indicates a pulled to < 0.8V, id to shut down the transmitter optical outp ther on	a laser fault of some kind. Low ind	dcates normal operation.		1 (key)         VeeT           2         TXFault           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)	VeeT         20           TD-         19           TD-         18           VeeT         17           VccT         16           VccR         15           VeeR         12		1 (key)         Vec           2         TC           3         Ve           4         TC           5         Ve           6         Vc           7         Gr	eT (ground) VeeT (ground) 30 3- TXFouli 29 peeT (ground) TX.Disable 28 0- MOD-DEF(2) 27 peeT (ground) MOD-DEF(1) 26
Plug Sequence 1) TX Fault le Pull up vo In the low 2) TX disable Its states Low (0 - 1	an open collector/drai tage between 2.0V and state, the output will be is an input that is use are: .8Vk Transmil DV): Undefined 3.465Vi Transmit	n output, which should be pulled up with a VccT, R+0.3V, When high, output indicates e pulled to < 0.8V. Id to shut down the transmitter optical outp tter on	a laser fault of some kind. Low ind	dcates normal operation.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 likey)         Vec           2         TC           3         Ve           4         TC           5         Vec           6         Vcc           7         Gr           8         Gr           9         Gr	eT (ground) VeeT (ground) 30 D- TXFault 29 peeT (ground) TX Disable 28 MOD-DEF(2) 27 peeT (ground) MOD-DEF(2) 27 peeT (ground) MOD-DEF(1) 26 ccT MOD-DEF(1) 26 aund VeeC (ground) 22 round Ground 23 ound Ground 232
<ol> <li>Plug Sequend</li> <li>TX Fault le Pull up vo In the law</li> <li>TX disable Its states Low (0 - 1) (&gt;0.8, &lt; 2 High (2.0 - Open:</li> <li>Mod-Def 0</li> </ol>	an open collector/drak tage between 2.0V and state. The output will be is an input that is use are: 3.465VI: Transmit Transmit Transmit. 1.2. These are the mode	n output, which should be pulled up with a VCCT. Ro3V, when high, output indicates a pulled to - 03V, d to shul down the transmitter optical outp ter on d er Deabled titler Disabled d actimition pins. They should be pulled up	a laser fault of some kind. Low ind aut, It is pulled up within the module p with a 4.7% - 10% W resistor on th	dcales normal operation. with a 4.7 - 10 K W resistor.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Rate Select           8         LOS	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13		1 (key)         Vec           2         TC           3         Ve           4         TC           5         Ve           6         Vc           7         Gr           8         Gr           9         Gr           10         Vc	eT (ground)         YeeT (ground)         30            TXFault         29           ref (ground)         TX Disable         28           0-         MOD-DEF(2)         27           xeT (ground)         MOD-DEF(1)         26           ccT         MOD-DEF(1)         26           csd         MOD-DEF(1)         26           cund         Ground         24           ound         Ground         22           cR         Ground         22           ref         Ground         22           ref         Ground         22           ref         Ground         22           ref         Ground         23
<ol> <li>Plug Sequence</li> <li>TX Fault le Pull up vo in the low</li> <li>TX disable Its states Low (0 - 1 (&gt;0.8, &lt; 2 High (2.0 - Open:</li> <li>Mod-Def 0 The pull-operation</li> </ol>	an open collector/drai lage between 2.0V and state, the autput will b is an input that is use are: 18Vb: Transmit 32.65Vb: Transmit Transmit 12. These are the modu o voltage shall be Vcc	n output, which should be pulled up with a VCCT. RoO3V, when high, output indicates a pulled to - 03V, d to shull down the transmitter optical outp ther on d er Disobled itter Utsabled d eachinion pins. They should be pulled up T or VCCR (see Section IV for further detail	a loser fault of some kind. Low ind within the module $\mu$ within the module $\mu$ within a $4.7 K$ - 10K W resistor on thes).	dcales normal operation. with a 4.7 - 10 K W resistor.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TC           3         Ve           4         TC           5         Ve           6         Vc           7         Gr           9         Gr           10         Vc           11         Vet	eT (ground)         VeeT (ground)         30           -         TXFault         29           pef (ground)         TX Disable         28           Da         MOD-DEF(2)         27           pet (ground)         MOD-DEF(2)         27           out         MOD-DEF(2)         26           cound         Ground         25           ound         Ground         23           ound         Ground         23           cound         Ground         23           cound         Ground         23           ound         Ground         21           eff (ground)         Rate Select         19           Da         Rate Select         19
<ol> <li>TX Foult le Pullu py voi in the low</li> <li>TX foult le Pullu py voi in the low</li> <li>TX disable its states Low (0 - 1 (-0.8, &lt; 2 High (2.0 - Open:</li> <li>Mod-Def 0 Mod-Def 1 Mod-Def 1</li> </ol>	an open collector/drai lage between 2.0V and state. the output will be is an input that is use orce orce orce orce orce orce orce orc	n output, which should be pulled up with a VCCT. Ro37, When high, output indicates a pulled to - 0.87. d to shut down the transmitter optical outp ther and ar Disabled itter Disabled itter disabled it or VCCR (see Section V for further detail due to indicate that the module is present up are earlied inforce for serial D.	a loser fault of some kind. Low ind within the module $\mu$ within the module $\mu$ within a $4.7 K$ - 10K W resistor on thes).	dcales normal operation. with a 4.7 - 10 K W resistor.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
<ol> <li>TX Fault le Pull up vo In the low</li> <li>TX disable Its states Low (0 - 1 (-0.8, &lt; 2 High (2.0 - Open:</li> <li>Mod-Def 0 The pull-L Mod-Def 1 Mod-Def 2</li> </ol>	an open collector/drail tage between 2.0V and state. the output will bl is an input that is use are an input that is use are 3.465Vk. Transmit 3.465Vk. Transmit 12. These are the modu violage shall be Vec' is grounded by the mo is the clack line of two is the data line of two	n output, which should be pulled up with a VCCT. R-03V, When high, output indicates a pulled to - 0.8V. d to shut down the transmitter optical outp ther on a d booband ther Disabled due to indicate the pulled up T or VCCR (see Section IV for further detal due to indicate than the module is present over senial interface for serial D	a laser fault of some kind. Low ind but, If is pulled up within the madule p with a 4.7K - 10K W resistor on It liak	scales normal operation. with a 4.7 - 10 K W resistor. he host board.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           >-         TXFGul1         29           pet (ground)         TX Disable         28           >-         MOD-DEF(2)         27           pet (ground)         MOD-DEF(1)         26           c:r         MOD-DEF(1)         26           c:ound         Konud         23           ound         Ground         23           cund         Ground         23           cund         Ground         21           eft (ground)         Ground         22           ound         Ground         23           ound         Ground         21           eft (ground)         Ground         22           vand         Ground         21           eft (ground)         Ground         20           -         Rate Select         19           eft (ground)         LOS         18
Plug Sequence 11 TX Foult is Pull up vc in the low 2) TX disable lits states Low (0 - 1 (>0.8, < 2 High 12.0 - Open: 3) Mod-Def 0 Mod-Def 1 Mod-Def 2 1) Mod-Def 2 1) This is an If implement	an open collector/drail tage between 20% and stelle. The output will be is an input that is use are: 20% Transmit 20% Tran	n output, which should be pulled up with a VCCT. R-03V, When high, output indicates pulled to - 0.8V. d to shuld down the transmitter optical output ter on d beabled ther Disobled d addinition pins. They should be pulled up T or VCCR (see Section IV for further detal dute to indicate than the module is present over serial interface for serial ID where serial interface for serial ID control the receiver bandwidth for compatite pressing with resident.	a laser fault of some kind. Low ind nut, it is pulled up within the module p with a 4.7% - 10% W resistor on th lay,	scales normal operation. with a 4.7 - 10 K W resistor. he host board.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
Plug Sequence 1) TX Fault le Putt up vc in the low 2) TX disable Law t0a- tils states Law t0a- tils states Law t0a- Open: 3) Mod-Def 0 Mod-Def 1 Mod-Def 2 4) The juli- til implement Low t0a- til implement Low t0a- Low t0a- til implement Low t0a- til	an open collector/drail tage between 20% and stelle. The output will be sen input that is uses are: 20% Transell 20% Undefined by Undefined by Undefined is the clock time of two is the clock time of two is the clock time of two optional input used to 20% Undefined	n output, which should be pulled up with a VCCT. R-03V, When high, output indicates e pulled to < 0.8V. If to shull down the transmitter optical output ter on a difference of the standard of the standard wither Disabled difference of the standard be pulled up to vCcR (see Section IV for further detail due to indicate that the sould be pulled up to we serial interface for serial D wire serial interface for serial D control the receiver bandwidth for compatible ternally pulled down with > 30k W resistor.	a laser fault of some kind. Low ind nut, it is pulled up within the module p with a 4.7% - 10% W resistor on th lay,	scales normal operation. with a 4.7 - 10 K W resistor. he host board.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
Plug Sequence 1) TX Foull In Pull up vc In the low 2) TX disable Low (0 – 1 (>0.8, < 2 Hgh (2.0 – 0 Open: 3) Mod-Def 0 The pull-vc Mod-Def 1 Hod-Def 2 4) The is an If implement Low (0 – 1 (>0.8, < 2	an open collector/drait tage between 2.0% and state. The output with be is an input that is use of the output that is use of the output that is use with the output that with the output with the output with the output is the clack the of huc is the other of huc is the other other other other other is the other other other other other is the othe	n output, which should be pulled up with a VCCT. Ro32, when high, output indicates a pulled to - 030, d to shull down the transmitter optical output for observed to the should be pulled up for ViccR issee Section IV for further detail due to indicate that the module is present in service service information be write service for service D write service inforce for service D sondwidth terminity pulled down with > 30k W resistor, sondwidth	a laser fault of some kind. Low ind nut, it is pulled up within the module p with a 4.7% - 10% W resistor on th lay,	scales normal operation. with a 4.7 - 10 K W resistor. he host board.		1 (key)         VeeT           2         TXFoult           3         TX Disable           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(1)           7         Role Select           8         LOS           9         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
<ul> <li>Plug Sequence</li> <li>TX Fault In</li> <li>Puti up vc</li> <li>In the low</li> <li>Its states</li> <li>Low (0 - 1</li> <li>(+0.8, &lt; 2</li> <li>High 120 - Open:</li> <li>Mod-Def 0</li> <li>The pull-tc</li> <li>Mod-Def 1</li> <li>Mod-Def 24</li> <li>High 120 - Open:</li> <li>States</li> <li>Low (0 - 1</li> <li>Cove, &lt; 4</li> <li>High 120 - Open:</li> <li>St Los (Loss / A</li> <li>High 120 - Open:</li> </ul>	an open collector/drait tage between 20% and site. The august will be a on input that is use are are are are are are are are are ar	n output, which should be pulled up with a VCCT. Ro32V, when high, output indicates pulled to - 0.82V. d to shull down the transmitter optical output er Deabled wither Disabled d a definition pins. They should be pulled up T or VCCR (see Section IV for further defail due to indicate that the module is present vier senial interface for senial D wire eared interface for senial D control ther receiver bandwidth for compatible train Bandwidth Bandwidth baltect/archain output, which should be pulled the received policid power is below the van	a loser fault of some kind. Low ind ad, II is pulled up within the module p with a 4.7K - 10K W resistor on it te), Silly with multiple data rates (most lik The input states are:	scales normal operation. with a 4.7 - 10 K W resistor. he hast board. Keiy Fibre Channel 1x and 2x Rates). Pull up voltage between 2.0V and VccT, R+0.31V.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
Plug Sequenci 1 TX Floatil II TX Floatil II Pull up vc In the law 21 TX discubil IIs states Law (0 - 1 (0.08, < 2 Hgh (2.0 Open: 33 Mod-Def 0 Mod-Def 1 Mod-Def 2 43 The pull-t Mod-Def 2 40 The law Mod-Def 2 40 The law Mod-Def 2 50 LoS (Loss When high Low Indice	an open collector/draitage between 20% and tage between 20% and site. The output will be is an input that is use are site. The output will be the output site of the source of the output site of the drait line of two optional input used to the drait line of two optional input sets of the drait line of two optional input sets of the output indicates it the output indicates it	n output, which should be pulled up with a VCCT. R-03V, When high, output indicates pulled to < 0.8V. d to shut down the transmitter optical output for one of the should be pulled up for Deabled if the Disabled d de definition pins. They should be pulled up T or VCCR (see Section V for further detail duale to indicate that the module is present one sensiti interface for serial D control the receiver bandwidth for compatite formally pulled down with > 30k W resistor. Sandwidth callector/archin output, which should be pulled up the received policy lower is behaviour he won the low store, the output with be pulled to the low store, the output with be pulled to the low store, the output with be pulled to the low store, the output with be pulled to	a loser fault of some kind. Low ind ad, II is pulled up within the module p with a 4.7K - 10K W resistor on it te), Silly with multiple data rates (most lik The input states are:	scales normal operation. with a 4.7 - 10 K W resistor. he hast board. Keiy Fibre Channel 1x and 2x Rates). Pull up voltage between 2.0V and VccT, R+0.31V.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
<ul> <li>Plug Sequence</li> <li>1) TX Foult Ir</li> <li>Pollug Sequence</li> <li>In TX Foult Ir</li> <li>Pollug Sequence</li> <li>In the low</li> <li>In the low</li></ul>	an open collector/drai tage between 20% and state. The autyout will be is an input that is use ore 20% and 20% and 20% and 20% 20% and 20% and 20% and 20% and 20% and 20% and 20% and 20% and 20% and 20%	n output, which should be pulled up with a VCCT, RAG3V, When high, output indicates pulled to < 0.8V. d to shut down the transmitter optical output indicates the should be pulled up ther on d beated ther Disabled ther Disabled d to Adacte that the module is present of a vacR (see Section V for further detail due to Indicate that the module is present on where serial interface for serial D control the receiver bandwidth for compatible terraily pulled down with > 30k V resistor, bandwidth collector/drain output, which should be pulled up the received optical point is build be pulled to the received optical point is build be pulled to the law store, the output will be pulled to terraily connected within the SFP module.	a loser fault of some kind. Low ind but, If is pulled up within the module p with a 4.7K - 10K W resistor on it list. Nithy with multiple data roles (most lik The input states are: at up with a 4.7K - 10K W resistor. at-case receiver sensitivity (as defir < 0.0K.	scales normal operation. with a 4.7 - 10 K W resistor, he hast board. Kely Fbre Channel fx and 2x Rates). Pull up voltage between 2.04 and VccT, R+0.34, med by the standard in use).		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11		1 (key)         Vec           2         TE           3         Ve           4         TE           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD	eT (ground)         VeeT (ground)         30           D-         TXFault         29           ground1         TX Disable         28           D-         MOD-DEF(2)         27           D-         MOD-DEF(2)         27           card         MOD-DEF(1)         26           ccT         MOD-DEF(0)         25           ound         Ground         22           ound         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         23           ref (ground1)         Ground         210           ref (ground1)         LOS         18      ref (groun
Plug Sequence           1) TX Foult is           Pulug Sequence           Pulug Network           2) TX deable           III is states           Low (0 - 1           Hgh (20 - 0           Open:           3) Mod-Def 0           Mod-Def 0           Hispital           Hoot-Def 2           4) The son if inpleme           III inpleme           When high (20 - 0)           Open:           5) LOS (Loss when high (20 - 0)           When high (20 - 0)           Open:           5) LOS (Loss the mode (20 - 1)           The Act (20 - 1)           The Act (20 - 1)           The Act (20 - 1)	an open collector/drai tage between 20% and state. The august will be sean input that is use are search and and and and the august and and the august and the august and solver. Transme 12. These are the mode to voltage shall be VCC' is grounded by the nei she clack the nopul will be in a she clack the nopul will be in AV: Reduced the number of two polload input used to the august and and the august and and the august and and a she clack the nopul will be in a she and and and the august and and a she clack the nopul will be in the august indicates it the august indicates it th	n output, which should be pulled up with a VCCT, RAG3V, When high, output indicates pulled to < 0.8V. d to shut down the transmitter optical output indicates the should be pulled up ther on d beated ther Disabled ther Disabled d to Adacte that the module is present of a vacR (see Section V for further detail due to Indicate that the module is present on where serial interface for serial D control the receiver bandwidth for compatible terraily pulled down with > 30k V resistor, bandwidth collector/drain output, which should be pulled up the received optical point is build be pulled to the received optical point is build be pulled to the law store, the output will be pulled to terraily connected within the SFP module.	a loser fault of some kind. Low ind but, If is pulled up within the module p with a 4.7K - 10K W resistor on it list. With multiple data rates (most lik The input states are: at-case receiver sensitivity (as define < 0.8K. W differential lines which should be hoat board. The valiage swing an it	scales normal operation. with a 4.7 - 10 K W resistor, he host board. Keiy Fibre Channel 1x and 2x Rates), Pull up voltage between 2.0V and VccT, R+0.3V, hed by the standard in use). e terminated with 100 W (differential) of the user SERDES.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11		1 (key)         Vec           2         TC           3         Vec           4         TC           5         Vec           6         Vcc           7         Gr           9         Gr           10         Vcc           12         RO           13         Vec           14         RO           15         Vec	eT (ground)         VeeT (ground)         30           -         TXFault         29           set (ground)         TX Disable         28           yeat (ground)         TX Disable         28           yeat (ground)         MOD-DEF(2)         27           set (ground)         MOD-DEF(0)         25           cond         Ground         23           ound         Ground         23           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         20           -         Rate Select         19           peff (ground)         L05         18           y-         VeeR (ground)         16           y-         VeeR (ground)         16
<ol> <li>TX Foult Ir Puly Sequence</li> <li>TX Foult Ir Pull up vc In the low</li> <li>TX deable Its states</li> <li>TX deable Nod-Deit</li> <li>The pull-t</li> <li>The pull-t</li> <li>The low Indeable Its states</li> <li>The low Indeable Its states</li> <li>St LOS Closes When high Low Indice</li> <li>VecR and Not Acces</li> <li>VecR and Not Acces</li> </ol>	an open collector/drait tage between 2.0% and state. The output will be is an input that is use and the output will be added to the output will be added to the output added to the output will be added to the output is the clock the of the is output indicates it es on the differential upping is done indek the vector the the receiver	n output, which should be pulled up with a VCCT. Ro32, when high, output indicates a pulled to - 030, d to shull down the transmitter optical output for one of the state of the state of the d d the shull down the transmitter optical output for ViccR (see Section IV for further detail due to indicate that the module is present or series interface for series (D write series) interface for series (D write series) interface for series (D write series) interface for series (D output) by the state of the series of the series interface for series (D output) by the series of the series of the series interface for series (D output) by the series of the series of series with the should be pulled to the received optical power is below the wor the low state, the output with the pulled to ternally connected within the SFP module. Thereview on output, They or e AC coupled noo is module and is Thus not required on the differential 1055 - 1000 and single ended b.	a loser foult of some kind. Low ind aut, II is pulled up within the module p with a 4.7K - 10K W resistor on it is), with multiple data rates (most list may with multiple data rates (most list is), with a 4.7K - 10K W resistor, and is), with a 4.7K - 10K W resistor, and is	scales nomal operation. with a 47 - 10 K W resistor, he hast board. Kelly FBre Channel fx and 2x Rates). Pull up voltage between 2,0V and VccT, R+0,3V, med by the standard in use). In ternhated with 100 W (differential) of the user SERDES, hese lines		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL TOLERANCES (UNLESS_SPECIFIED)	1 (key)         Vec           2         TC           3         Vec           4         TC           5         Vec           6         Vc           7         Gn           9         Gr           10         Vc           12         RD           13         Ve           14         RD           15         Ve           MONSON STYLE         MMONLY	ET (ground) VeeT (ground) 30 D- TXFault 29 Set (ground) TX Disable 28 MOD-DEF(2) 27 aet (ground) MOD-DEF(2) 25 aund VeeC (ground) 22 cound Ground 23 ound Ground 23 ound Ground 21 eft (ground) Ground 21 eft (ground) Ground 21 perk (ground) LOS 18 Perk (ground) LOS 18 SCALE DESIGN UNITS METRIC © THRD METRIC PROJECT
Plug Sequence           1) TX Foult is           Pull up vc           In the low           2) TX deable           In the low           20 TX deable           In the low           20 TX deable           10 A           21 TX deable           11 TX feature           22 TX deable           23 Mod-Def 0           Mod-Def 1           Mod-Def 2           11 Mod Def 1           Mod-Def 2           11 Mod Def 2           20 The low indice           20 Qen:           33 Mod-Def 2           11 Mod Def 2           20 The low indice           20 Qen:           32 NG8 (close           When they be bel           30 VccR and Maximum a           When they be bel           30 VccR and Maximum a	an open collector/drait tage between 2.0% and state. The output will be a on input that is use the state of the output will be a state. The output will be a state of the output will be a state of the output will be a state of the output will be a state	n output, which should be pulled up with a VCCT. Ro32, when high, output indicates pulled to -0.82, d to shull down the transmitter optical output error backed if an output of the should be pulled up of the con- dite of a d de definition pins. They should be pulled up of c VCCR (see Section IV for further defail due to indicate that the module is present where sensit interface for senial D output the defailed down with > 30k W resister, Sandwidth Bandwidth Bandwidth Bandwidth Bandwidth with the pulled to pulled up therait public down with > 30k W resister, Sandwidth Bandwidth Bandwidth Bandwidth be pulled to terraity pulled output, which should be pulled the rate received public, which should be pulled to terraity connected within the SPF module. In dimension output, They are AC coupled 100 terraity connected within the SPF module. In dimensional to the son of required on the differentiates - 1000 are signing endow. A constant was an of required on the son differentiates. How you can be pulled to the up atoms with power supplies they are and in dimension output, up and the pulled to the dimension output, up and the pulled to the solution with DC resistance of tess time of transmitter power supplies. They are act to make the solution output, up and the pulled to the module couple of the two the solution with DC resistance of tess time of the solution with DC resistance of tess time them prevent and up and the pulled to the module couple. They are act the solution with DC resistance of tess time them prevents and the two the power pulles the two the prevent of the two the power pulles. They are act the solution and the solution the power pulles the two	a loser fault of some kind. Low ind aut, II is pulled up within the module p with a 4.7K - 10K W resistor on it tel, SIII with multiple data rates (most lik The input states are: at up with a 4.7K - 10K W resistor. Interview of the states o	scales nomal operation. with a 47 - 10 K W resistor. he hast board. Kely Fibre Channel fx and 2x Ratea). Pull up voltage between 2,0V and VccT, R+0,3V, hed by the standard in use). the standard in use in the standard in the standard in use in the standard in the standard in the standard in use in the standard in th	upply voltoge.	1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL TOLERANCES (UNLESS SPECIFIED)	1 fikeyi         Vec           2         TC           3         Vec           4         TC           5         Va           6         Vc           7         Gr           9         Gr           10         Vc           11         Vec           12         RDN           13         Vec           14         RDN           15         Vec           MMONLY         AAN BY	ET (ground) VeeT (ground) 30 D- TXFault 29 peeT (ground) TX Disable 28 MOD-DEF(2) 27 peeT (ground) MOD-DEF(2) 27 peeT (ground) MOD-DEF(1) 26 ccT MOD-DEF(1) 26 cound Ground 22 cmd Ground 22 cmd Ground 21 ground Ground 21 ground Ground 21 ground Ground 21 ground Ground 21 ground Ground 21 ground Select 19 peeR (ground) Ground 21 peeR (ground) Ground 21 ground 10 ground 10 Ground 21 ground 10 Ground 21 ground 10 Ground 21 Ground 21 ground 10 Ground 21 ground 10 Ground 21 Ground 21 Groun
Plug Sequent           Plug Sequent           PAIL (pv)	an open collector/drait tage between 20% and state. The adjust will be a on input that is use are an adjust of the state are are an adjust of the state are are an adjust of the 3.455V: Tronentin Tronent Tronent Tronent Tronent are an adjust of the between adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of the adjust of th	n output, which should be pulled up with a VCCT. Ro32V, when high, output indicates pulled to - 0.8V. d to shull down the transmitter optical output error backed if an output of the should be pulled up for Deabled if a deathillion pins. They should be pulled up f or ViccR (see Section IV for further deal due to indicate that the module is present vie senial interface for serial D wire earent interface for serial D control ther receiver bandwidth for compatibe wire and interface for serial D control the receiver bandwidth for compatibe and the tradition of the series of the series before the series of the series of the series before the series of the series of the series the nervel of optication output, which should be pulled to the series of the output will be pulled to the soften, the output will be pulled to the soften, the output will be pulled to the soften the SFP module. The constant of the series of the series of the and the SFP module. The and transmitter power supplies the series of the outputs. They are AC coupled 300 to a transmitter power supplies the series of the outputs. They are AC coupled to the differention the series and the series of the series of transmitter power supplies. They are a for the outputs. They are AC coupled to the for the series with the transition of transmitter power supplies. They are a for the series the series the series of the series the series and the series of the series the series and the series of the series the power supplies. They are actioned on the differention the series the series of the series the series and the series	a loser foult of some kind. Low ind aut, if is pulled up within the module p with a 4.7% - 10% W resistor on it is). Stilly with multiple data rates (most lik The input states are: and up with a 4.7% - 10K W resistor, interput states are: and up with a 4.7% - 10K W resistor, et-cose receiver sensitivity (as defr < 0.8%. W differential lines which should be hear board. The voltage swing an it hear page is per transmitted.	scales nomal operation. with a 4.7 - 10 K W resistor, he host board. Kely Fbre Channel fx and 2x Ratea). Pull up voltage between 2.0V and VccT, R+0,3V, ned by the standard in use). these lines investion of the resulted voltage of the SFP input pin with 33V su in a mount curved of no are famol on divergence of the SFP could pin with 33V su in a mount curved of no are famol on divergence of the SFP could pin with 33V su in a mount curved of no are famol on divergence of the SFP could pin with 33V su in a mount curved of no are famol on divergence of the SFP could pin with 33V su the result of the individuality filtered near the SFP connector,	supply voltage.	1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL TOLERANCES (UNLESS SPECIFIED) 4 PLACES ± 64 3 PLACES ± 64	1 fikeyi         Vec           2         TC           3         Vec           4         TC           5         Vec           6         Vcc           7         Gr           9         Gr           11         Vec           12         R00           13         Vec           14         R00           15         Vec           15         Vec           MMONLY         RAWN BY           A2TE         T           12/01/05         CCKED BY           CAVED BY         DATE	ET (ground) VeeT (ground) 30 D- TXFault 29 Set (ground) TX Disable 28 MOD-DEF(2) 27 aet (ground) MOD-DEF(2) 25 aund VeeC (ground) 22 cound Ground 23 ound Ground 23 ound Ground 21 eft (ground) Ground 21 eft (ground) Ground 21 perk (ground) LOS 18 Perk (ground) LOS 18 SCALE DESIGN UNITS METRIC © THRD METRIC PROJECT
Plug Sequence 1) TX Fault is Pull up vc Pull v	an open collector/draitinge between 207 and tage between 207 and state. The august will be search and august will be august will be august	n output, which should be pulled up with a VCCT, RAOX, when high, output indicates pulled to - 03X. I to shult down the transmitter optical output for a shult down the transmitter optical output for output to the should be pulled up and the Disabled all definition pins. They should be pulled up to VCCT likes Section IV for further debt wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible terraily pulled down with > 30k W resistor. Sandwidh bandwidh bandwidh the SFP module. The output the pulled on the differential (165 - 1000 mV single ended ) and fransmit poly single ended on the differential (165 - 1000 mV single ended ) and forustrike the pole subjurged on the differential (165 - 1000 mV single ended ) and forustrike the pole subjurged on the differential to box store, the receiver down in the series and forustrike the pole subjurged on the differential to the internally corrected within transmitter inputs. They are AC-coupled 00 down the store the low at the the level and the store the law and required on the differential to the store the law of the level down the store the law of the law of the level down the store the law of the law of the law of the store the law of the law of the law of the store the law of the law of the store law of the	a loser fault of some kind. Low ind sul, II is pulled up within the module p with a 4,7K - 10K W resistor on it list. Allty with multiple data rates (most lik The input states are: et up with a 4,7K - 10K W resistor, et up with a 4,7K - 10K W res	scales nomal operation. with a 4.7 - 10 K W resistor, he hast board. Kely Fbre Channel 1x and 2x Rates). Put up voltage between 2.0V and VccT, R+0.3V, he do the standard in use). In terminated with 100 W (differential) at the user SERDES. here lines mector pit. In terminated voltage at the SFP input pin with 3.3V su in the inductor of the user the SFP connector. It termination indic the module.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL         TOLERANCES           (UNLESS         SPECIFIED)           mm         INCH           4         PLACES           3         PLACES           2         PLACES           40         CE	I fikeyi         Vec           2         TE           3         Vec           4         TE           5         Vec           6         Vcc           7         Gr           9         Gr           10         Vcc           11         Vec           12         RDD           13         Vec           14         ROD           15         Vec           15         Vec           2         RDD           15         Vec           16         Vcc           17         Rom           18         Vec           19         Or           10         Vec           11         Vec           12         RDD           13         Vec           14         ROD           15         Vec           15         Vec           10         Vccconservec           12         RDD           14         ROD           15         Vec           16         ROD           17         Vcconservecconserveco	eT (ground) VeeT (ground) 30 D- TXFault 29 seT (ground) TX Disable 28 MOD-DEF(2) 27 seT (ground) MOD-DEF(2) 27 seT (ground) MOD-DEF(0) 25 cound Ground 23 cound Ground 23 cound Ground 21 eR (ground) Cround 21 eR (ground) Ground 21 eR (ground) Ground 21 eR (ground) UNTS 18 D- Rate Setect 19 seR (ground) LOS 18 D- VeeR (ground) 16 SCALE DESIGN UNTS 16 METRIC © THIRDE METRIC SECT 16 METRIC SECT 16 MODULE ASSEMBLY
Plug Sequence 1) TX Fault is Pull up vc Pull v	an open collector/draitinge between 207 and tage between 207 and state. The august will be search and august will be august will be august	n output, which should be pulled up with a VCCT, RAOX, when high, output indicates a pulled to - QAV and the shull down the transmitter optical output for a shull down the transmitter optical output for output the shull down the transmitter optical difference of the shull down the transmitter optical difference of the shull down the pulled up for VCCR takes Section V for further detail due to indicate that the module is present wire send interface for senicit D wire send interface for senicit D sordwith termit Bondwith able transmitter public, They are AC coupled to e module and is thus not required on the ord informative public strenge ender is undersenice of the senice of tests that offerencial (15 - 1000 AV single ended is and transmitter power supplies. They are at to provide a set hardings of the individence of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the transmitter power supplies. They are difference of tests that the test of test that the test of test te	a loser fault of some kind. Low ind sul, II is pulled up within the module p with a 4,7K - 10K W resistor on it list. Allty with multiple data rates (most lik The input states are: et up with a 4,7K - 10K W resistor, et up with a 4,7K - 10K W res	scales nomal operation. with a 4.7 - 10 K W resistor, he host board. Kely Fbre Channel 1x and 2x Rates). Put up voltage between 2.0V and VccT, R+0.3V, he do the standard in use). In terminated with 100 W (differential) at the user SERDES. here lines mector pit. In terminated voltage at the SFP input pin with 3.3V sub mector pit. In Vec will be individually filtered near the SFP connector, a) termination invide the module.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL         TOLERANCES           UNLESS         SPECIFIED           mm         INCH         Ø           4         PLACES         +         54           3         PLACES         +         54           2         PLACES         +         ±         54           1         PLACE         ±         54         54           ANGULAR         ±1/2.°         54         54	I fikeyi         Vec           2         TE           3         Vec           4         TE           5         Vac           6         Vcc           7         Gr           9         Gr           10         Vc           11         Vec           12         RDD           13         Ve           14         RD           15         Ve           15         Ve           2         Vc           15         Ve           16         Vc           17         Gr           18         Ve           14         RD           15         Ve           15         Ve           16         Vc           17         Vc           18         Vc           19         Vc           10         Vc           11         Vc           12         RDND           13         Vc           14         RD           15         Vc           16         Vc           17	ET (ground) VeeT (ground) 30 D- TXFauli 29 per (ground) TX Disable 28 MOD-DEF(2) 27 per (ground) MOD-DEF(2) 27 per (ground) MOD-DEF(1) 26 cound VeeC (ground) 24 iound Ground 23 iound Ground 21 Ground 21 get (ground) Ground 21 get (ground) Ground 21 per (ground) Ground 21 ground 21 ground Ground 21 ground 21
<ul> <li>Plug Sequenci</li> <li>1) TX Foull is Pull up vc Pull up vc in the low</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) The pull-ty</li> <li>3) Mod-Def 0</li> <li>7) The pull-ty</li> <li>8) Mod-Def 1</li> <li>7) Mod-Def 2</li> <li>4) This is an If implement</li> <li>10, 10, 10, 10, 10, 10, 10, 10, 10, 10,</li></ul>	an open collector/draitinge between 207 and tage between 207 and state. The august will be search and august will be august will be august	n output, which should be pulled up with a VCCT, RAOX, when high, output indicates pulled to - 03X. I to shult down the transmitter optical output for a shult down the transmitter optical output for output to the should be pulled up and the Disabled all definition pins. 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In Vec will be individually filtered near the SFP connector, a) termination invide the module.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	GENERAL TOLERANCES           IUNLESS SPECIFIED           mm         INCH           0         4 PLACES ±         ±           3 PLACES ±         ±         FR           2 PLACES ±         ±         FR           1 PLACE ±         ±         SE           1 PLACE ±         ±         SE	1 (key)         Vec           2         TC           3         Vec           4         TC           5         Vec           6         VC           7         Gn           9         Gr           10         Vc           12         RD           13         Vec           14         RD           15         Vec           14         RD           15         Vec           12         PC           14         RD           15         Vec           12         RD           15         Vec           12         RD           15         Vec           12         RD           15         Vec           12         RD           12         RD           12         RD           12         RD           12         RD           13         Vec           14         RD           15         Vec           15         Vec           15         RO           12 <td>ET (ground)         VeeT (ground)         30           -         TXFault         29           set (ground)         TX Disable         28           ba         MOD-DEF(2)         27           set (ground)         MOD-DEF(2)         26           ctr         MOD-DEF(1)         26           ctr         MOD-DEF(1)         26           ctr         MOD-DEF(1)         26           cound         Ground         23           ound         Ground         23           ound         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           per (ground)         Ground         10           &gt;         Rate Select         19           ef (ground)         UseR (ground)         16           Scale         MODUSTRIAL         ELECTR'L SI           MODULE         ASSEMBLY         MODULE           MOLEX         INCORPORATE         SOUMENT NO.</td>	ET (ground)         VeeT (ground)         30           -         TXFault         29           set (ground)         TX Disable         28           ba         MOD-DEF(2)         27           set (ground)         MOD-DEF(2)         26           ctr         MOD-DEF(1)         26           ctr         MOD-DEF(1)         26           ctr         MOD-DEF(1)         26           cound         Ground         23           ound         Ground         23           ound         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           ef (ground)         Ground         21           per (ground)         Ground         10           >         Rate Select         19           ef (ground)         UseR (ground)         16           Scale         MODUSTRIAL         ELECTR'L SI           MODULE         ASSEMBLY         MODULE           MOLEX         INCORPORATE         SOUMENT NO.
<ul> <li>Plug Sequenci</li> <li>1) TX Foull is Pull up vc Pull up vc in the low</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) TX decoble</li> <li>2) The pull-ty</li> <li>3) Mod-Def 0</li> <li>7) The pull-ty</li> <li>8) Mod-Def 1</li> <li>7) Mod-Def 2</li> <li>4) This is an If implement</li> <li>10, 10, 10, 10, 10, 10, 10, 10, 10, 10,</li></ul>	an open collector/draitinge between 207 and tage between 207 and state. The august will be search and august will be august will be august	n output, which should be pulled up with a VCCT, RAOX, when high, output indicates pulled to - 03X. I to shult down the transmitter optical output for a shult down the transmitter optical output for output to the should be pulled up and the Disabled all definition pins. They should be pulled up to VCCT likes Section IV for further debt wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible wire serial interface for serial D control the receiver bandwidh for compatible terraily pulled down with > 30k W resistor. Sandwidh bandwidh bandwidh the SFP module. The output the pulled on the differential (165 - 1000 mV single ended ) and fransmit poly single ended on the differential (165 - 1000 mV single ended ) and forustrike the pole subjurged on the differential (165 - 1000 mV single ended ) and forustrike the pole subjurged on the differential to box store, the receiver down in the series and forustrike the pole subjurged on the differential to the internally corrected within transmitter inputs. They are AC-coupled 00 down the store the low at the the level and the store the law and required on the differential to the store the law of the level down the store the law of the law of the level down the store the law of the law of the law of the store the law of the law of the law of the store the law of the law of the store law of the	a loser fault of some kind. Low ind sul, II is pulled up within the module p with a 4.7K - 10K W resistor on it list. Allty with multiple data rates (most lik The input states are: et up with a 6.7K - 10K W resistor, et up with a 6.7K - 10K W res	scales nomal operation. with a 4.7 - 10 K W resistor, he host board. Kely Fbre Channel 1x and 2x Rates). Put up voltage between 2.0V and VccT, R+0.3V, he do the standard in use). In terminated with 100 W (differential) at the user SERDES. here lines mector pit. In terminated voltage at the SFP input pin with 3.3V sub mector pit. In Vec will be individually filtered near the SFP connector, a) termination invide the module.		1 (key)         VeeT           2         TXFoult           3         TX Disoble           4         MOD-DEF(2)           5         MOD-DEF(1)           6         MOD-DEF(0)           7         Rofe Select           8         LOS           9         VeeR           10         VeeR	VeeT         20           TD-         19           TD+         18           VeeT         17           VccT         16           VccR         15           VeeR         14           RD+         13           RD-         12           VeeR         11	4 PLACES <u>+</u> <u>+</u> <u>BU</u> 3 PLACES <u>+</u> <u>+</u> CH 2 PLACES <u>+</u> <u>+</u> SE 1 PLACE <u>+</u> <u>+</u> <u>AP</u> ANGULAR <u>+</u> 1/2° MAGULAR <u>+</u> 1/2° MAGULAR <u>+</u> 1/2°	I likeyi         Vec           2         TC           3         Vec           4         TC           5         Vec           6         Vcc           7         Gr           9         Gr           10         Vc           11         Vec           12         RD           13         Vec           14         RD           15         Vec           15         Vec           15         Vec           15         Vec           15         Vec           15         Vec           16         Vcc           17         Vec           18         Vec           19         Vec           12         RD           15         Vec           15         Vec           16         SO           106         SO           24         THS           17         Vec	ET (ground) VeeT (ground) 30 D- TXFauli 29 per (ground) TX Disable 28 MOD-DEF(2) 27 per (ground) MOD-DEF(2) 27 per (ground) MOD-DEF(1) 26 cound VeeC (ground) 24 iound Ground 23 iound Ground 21 Ground 21 get (ground) Ground 21 get (ground) Ground 21 per (ground) Ground 21 ground 21 ground Ground 21 ground 21