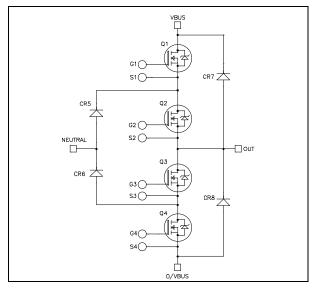
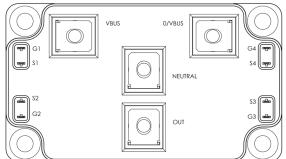


# Three level inverter SiC MOSFET Power Module





### **SiC Power MOSFET:**

 $V_{DSS} = 1200V ; R_{DSon} = 12m\Omega @ Tj = 25^{\circ}C$ 

#### **Application**

• Uninterruptible Power Supplies

#### **Features**

- SiC Power MOSFET
  - Low R<sub>DS(on)</sub>
  - High temperature performance

### • SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin emitter for easy drive
- Very low stray inductance
- M5 power connectors
- High level of integration
- AlN substrate for improved thermal performance

#### **Benefits**

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile
- RoHS Compliant

### All ratings (a) $T_i = 25$ °C unless otherwise specified

### Q1 to Q4 Absolute maximum ratings (per SiC MOSFET)

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Voltage		1200	V
Ţ	Continuous Drain Current	$T_c = 25^{\circ}C$	219	
$I_D$	Continuous Drain Current	$T_c = 80$ °C	164	A
$I_{DM}$	Pulsed Drain current			
$V_{GS}$	Gate - Source Voltage		-10/+25	V
$R_{DSon}$	Drain - Source ON Resistance		12	mΩ
$P_D$	Maximum Power Dissipation	$T_c = 25^{\circ}C$	925	W

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



### Q1 to Q4 Electrical Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V ; V_{DS} = 120$			300	μA	
D	Dunin Carres on Basistana	$V_{GS} = 20V$	$T_j = 25$ °C		8	12	
R <sub>DS(on)</sub>	Drain – Source on Resistance	$I_{\rm D} = 150 A$	$T_j = 150$ °C		14	21	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 30 \text{mA}$		2.1	2.4		V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$				1.8	μA

# Q1 to Q4 Dynamic Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$			8.4			
$C_{oss}$	Output Capacitance	$V_{\rm DS} = 1000 V$			0.66		nF	
$C_{rss}$	Reverse Transfer Capacitance	f = 1MHz			0.045			
$Q_{\mathrm{g}}$	Total gate Charge	$V_{GS} = -5/+20V$			483			
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 800V$			138		nC	
$Q_{\mathrm{gd}}$	Gate – Drain Charge	$I_D = 150A$			150			
$T_{d(on)}$	Turn-on Delay Time	$\begin{aligned} V_{GS} &= -5/+20V \\ V_{Bus} &= 800V \\ I_D &= 150A \; , \; T_J = 150^{\circ}C \\ R_L &= 5.3\Omega \; ; \; R_{Gext} = 6.7\Omega \end{aligned}$			35			
$T_{\rm r}$	Rise Time				40		ns	
$T_{d(off)}$	Turn-off Delay Time				150			
$T_{\mathrm{f}}$	Fall Time				70			
Eon	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$	$T_j = 150$ °C		3.3		mJ	
$E_{\rm off}$	Turn off Energy	$I_{D} = 150A$ $R_{Gext} = 6.7\Omega$	$T_j = 150$ °C		1.8		mJ	
$R_{\text{thJC}}$	Junction to Case Thermal Resistanc	e				0.135	°C/W	

# **Body diode ratings and characteristics** (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V	Diode Forward Voltage	$V_{GS} = -5V, I_{SD} = 75A$		3.3		V
$V_{\mathrm{SD}}$		$V_{GS} = -2V, I_{SD} = 75A$		3.1		v
$t_{rr}$	Reverse Recovery Time	1. 150A W 5W		45		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$I_{SD} = 150A$ ; $V_{GS} = -5V$ $V_{R} = 800V$ ; $di_{F}/dt = 3000A/\mu s$		1.2		μС
$I_{rr}$	Reverse Recovery Current	γη 300 γ , αιματ 3000 Α/μ3		40		Α



### CR5 & CR6 SiC diode ratings and characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions			Typ	Max	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage					600	V
T	Inv.   Reverse Leakage Current   Vp=600V  =	V -(00V	$T_j = 25^{\circ}C$		220	1320	^
$\mathbf{I}_{\mathrm{RM}}$		$T_{j} = 175^{\circ}C$		440	6600	μA	
$I_{\mathrm{F}}$	DC Forward Current	Tc = 125°C			220		Α
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 220A$	$T_i = 25^{\circ}C$		1.6	1.8	V
V <sub>F</sub>	Diode Forward Voltage		$T_i = 175^{\circ}C$		2	2.4	V
Qc	Total Capacitive Charge	$I_F = 220A, V_R = 600V$ $di/dt = 5000A/\mu s$			616		nC
С	Tatal Caracitana	$f = 1MHz, V_R =$	$MHz, V_R = 200V$		1430		"E
	Total Capacitance	$f = 1MHz, V_R = 400V$			1100		pF
$R_{thJC}$	Junction to Case Thermal Resistance					0.1	°C/W

# CR7 & CR8 diode ratings and characteristics (Per SiC diode)

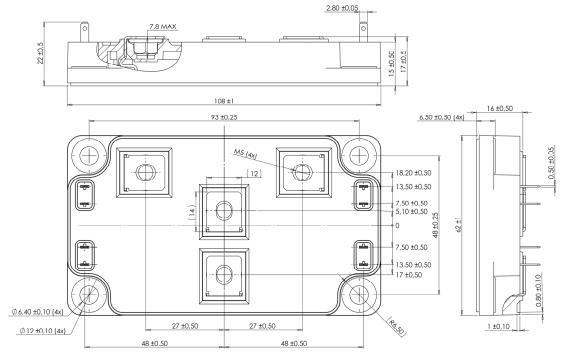
Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage					1200	V
Ţ	De come Leele e Comment	V <sub>R</sub> =1200V	$T_j = 25^{\circ}C$		280	1600	μА
$I_{RM}$	Reverse Leakage Current		$T_{j} = 175^{\circ}C$		520	3200	
$I_F$	DC Forward Current	$Tc = 125^{\circ}C$			160		A
$V_{\mathrm{F}}$	$V_{\rm F}$ Diode Forward Voltage $I_{\rm F} = 160 {\rm A}$	$T_i = 25^{\circ}C$		1.5	1.8	V	
<b>V</b> F	Diode Folward Voltage	$I_F = 160A$	$T_i = 175^{\circ}C$		2.2	3	V
Qc	Total Capacitive Charge	$I_F = 160A, V_R = 1200V$ di/dt = 4000A/ $\mu$ s			1040		nC
C	Total Capacitance	$f = 1MHz, V_R = 400V$			744		m.F.
C		$f = 1MHz, V_R =$	800V		536		pF
$R_{thJC}$	Junction to Case Thermal Resistance				0.14	°C/W	

# Thermal and package characteristics

Symbol	Characteristic					Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to o	case t=1 mir	n, 50/60	)Hz	4000		V
$T_{J}$	( )nerating ilinction temperature range		SiC M	OSFET	-40	150	
1 ј			SiC di	ode	-40	175	
$T_{JOP}$	Recommended junction temperature under switching conditions				-40	T <sub>J</sub> max -25	°C
$T_{STG}$	Storage Temperature Range	e Range				125	
$T_{\rm C}$	Operating Case Temperature					125	
Torque	Mounting torque	To heatsin	k	M6	3	5	N.m
Torque	Mounting torque For terminals		als	M5	2	3.5	IN.III
Wt	Package Weight					300	g



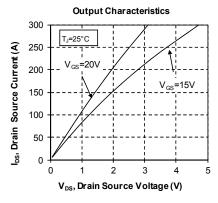
# SP6 Package outline (dimensions in mm)

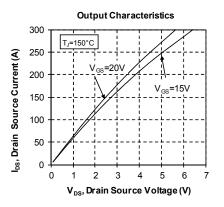


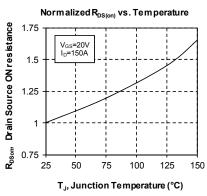
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

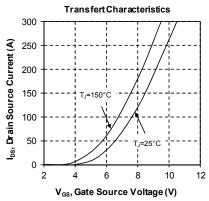


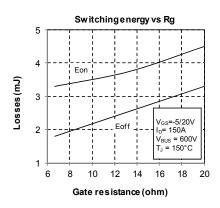
### Q1 to Q4 Typical performance curve

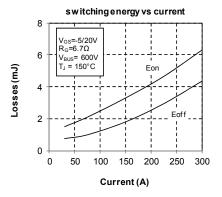


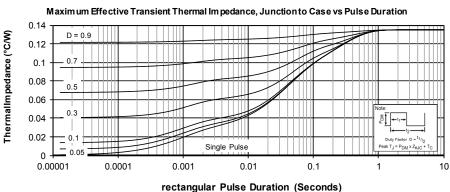








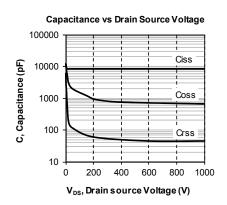


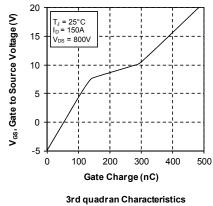


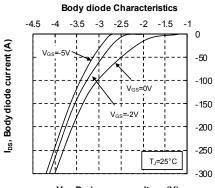
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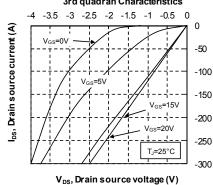


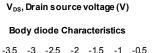
Gate Charge vs Gate Source Voltage

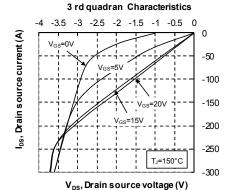


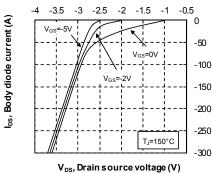


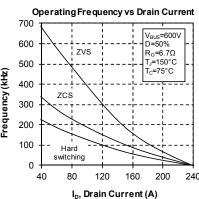










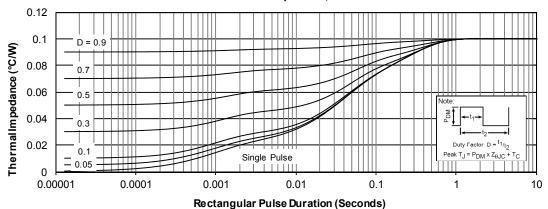


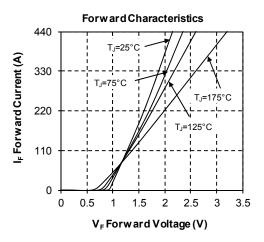
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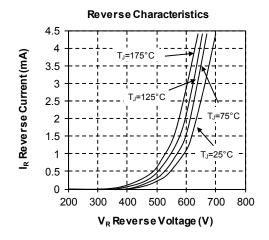


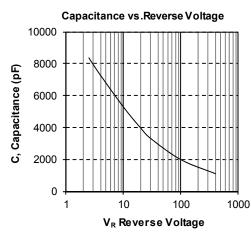
### CR5 & CR6 Typical performance curve

### Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration





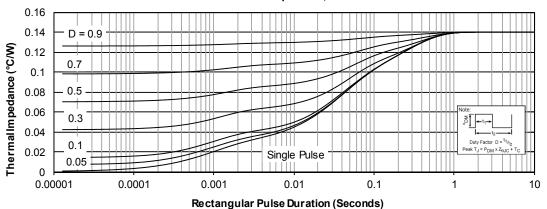


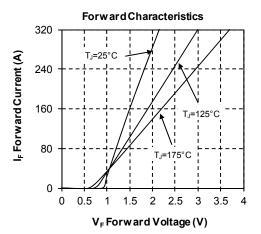


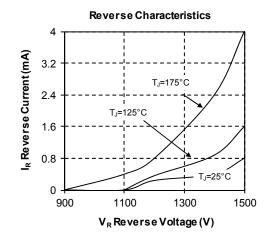


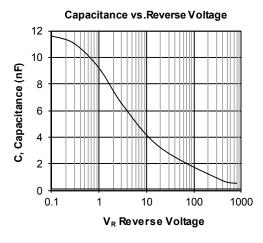
### **CR7 & CR8 Typical performance curve**

### Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration









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