

5V 4W CCFL Programmable Inverter Module

PRODUCTION DATASHEET

DESCRIPTION

The LXMG1618A-05-4xTM is a Single from the system battery, or AC adapter Output 4W Direct DriveTM CCFL (Cold directly to high frequency, high-voltage Cathode Fluorescent Lamp) Inverter waveform required to ignite and operate Module designed for driving LCD CCFL lamps. A 12V input inverter is also backlight lamps. It is ideal for driving available (LXMG1618A-12-4x). typically 8.4" to 12.1" panels.

externally programmable over a range of 5 CCFL backlight controller to provide a to 6.5mA in 0.5mA steps (PanelMatch) to wider temperature range (-30°C to 80°C) allow the inverter to properly match to a compared to the existing solutions offered wide array of LCD panel lamp current by Microsemi. specifications. The modules include a dimming input that permits brightness topology include stable fixed-frequency control from a DC voltage source, a PWM operation, secondary-side strike voltage signal or an external potentiometer.

LXMG1618A modules unlike the protection with fault timeout. LXMG1617A series do not provide wide range 'burst' mode dimming, rather modules are designed therefore as a dimming is provided by amplitude control higher of the output current waveform, and this replacement (see BRITE minimum input) limits the potential dim range to typically less than 5:1.

The module converts a DC voltage

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com

Protected By U.S. Patents: 5,923,129; 59320,121; 6,198,234; Patents Pending

The LXMG1618A modules contain The maximum output current is the newer highly integrated LX1691B

> Other benefits of the inverter's regulation and both open/shorted lamp

> The new LXMG1618A ("A Series") performance near for those customers and applications currently using the LXMG1618 inverters.

KEY FEATURES

- **Externally Programmable** Maximum Output Current
- Easy to Use Brightness Control
- Output Short-Circuit Protection and Automatic Strike-Voltage Regulation and Timeout
- Analog Current Amplitude **Dimming Method**
- Fixed Frequency Operation
- Rated From -30°C to 80°C
- UL60950 E175910
- **RoHS Compliant**

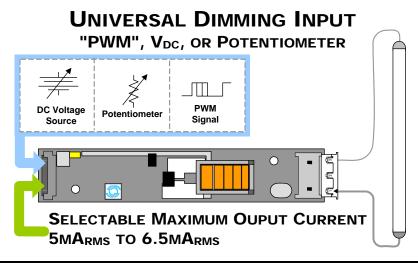
APPLICATIONS

- Medical Instrument Displays
- Portable Instrumentation
- Industrial Display Controls

BENEFITS

- Programmable Output Current Allows Inverter to Mate with a Wide Variety of LCD Panel's Specifications
- Output Open Circuit Voltage Regulation Minimizes Corona Discharge For High Reliability

PRODUCT HIGHLIGHT



PACKAGE ORDER INFO							
PART NUMBER	OUTPUT CONNECTOR	INVERTER MATES DIRECTLY TO PANEL CONNECTORS					
LXMG1618A-05-41	JST SM02(8.0)B-BHS-1-TB (LF)(SN), Yeon Ho 20015WR-05A00 or equivalent	JST BHR-03VS-1					
LXMG1618A-05-42	JST SM02B-BHSS-1-TB (LF)(SN), Yeon Ho 35001WR-02A00 or equivalent	JST BHSR-02VS-1					



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RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, may not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units	
raiametei	Symbol	Min	R.C.	Max	Office	
Input Supply Voltage Range (Fully Regulated Lamp Current)	V _{IN}	4.75	5	5.25	V	
Input Supply Voltage Range (Functional)		4.5	5	5.5		
Output Power	Po		3.5	4.0	W	
Linear BRITE Control Input Voltage Range ¹	V_{BRT_ADJ}	0		2.0	V	
Lamp Operating Voltage	V_{LAMP}	465	550	635*	V_{RMS}	
Lamp Current (Full Brightness)	I _{O(LAMP)}	5		6.5	mA _{RMS}	
Operating Ambient Temperature Range	T _A	-30		80	°C	

¹ The BRITE minimum input voltage level is 0V, whereas it is 0.65V in the original LXMG1618-05-4x inverter, The minimum V_{BRT ADJ} voltage depends on the panel characteristics and minimum lamp current specification, depending on the panel it can vary from 0V to 0.5V

ELECTRICAL CHARACTERISTICS

The following specifications apply over the recommended operating condition and ambient temperature of 0° C to 60° C except where otherwise noted.

Parameter	Symbol	Test Conditions	LXMG1618A-05-4x			Units	
Farameter	Syllibol	rest conditions	Min	Тур	Max	Uillis	
OUTPUT PIN CHARACTERISTICS							
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 5V$ $I_{SET1} = Ground$, $I_{SET2} = Ground$	4.5	5	5.5	mA _{RMS}	
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 5V$ $I_{SET1} = Ground$, $I_{SET2} = Open$	5.0	5.5	6.0	mA _{RMS}	
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 5V$ $I_{SET1} = Open$, $I_{SET2} = Ground$	5.5	6	6.5	mA _{RMS}	
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 5V$ $I_{SET1} = Open$, $I_{SET2} = Open$	6.0	6.5	7.0	mA _{RMS}	
Min. Average Lamp Current ²	I _{L(MIN)}	$V_{BRT_ADJ} = 0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 5V$ $I_{SET1} = I_{SET2} = Gnd$		1.6		mA _{RMS}	
Lamp Start Voltage	V _{LS}	-30°C < T _A < 80°C, V _{IN} > 4.5V	1300	1400		V _{RMS}	
Operating Frequency	f _O	$V_{BRT_ADJ} = 2.0V, \overline{SLEEP} \ge 2.0V, V_{IN} = 5V$	76	80	83	kHz	

² The Inverter is capable of a lower output current than may be recommended by the panel manufacturer. It is the user responsibility to set the minimum brightness (BRITE) input at or above the panel specification for minimum lamp current.

^{*} Total output power must not exceed 4W. Higher voltage lamps may require maximum output current to be set lower than 6.5mA_{RMS}



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ELECTRICAL CHARACTERISTICS (CONTINUED)

The following specifications apply over the recommended operating condition and ambient temperature of 0° C to 60° C except where otherwise noted.

	Parameter	Symbol Test Conditions	LXMG1618A-05-4x			Units			
	Farameter	Symbol	rest Conditions	Min	Тур	Max	Units		
•	BRITE INPUT								
	Input Current	I _{BRT}	$V_{BRT_ADJ} = 0V$ $V_{BRT_ADJ} = 3V$		-9 0		μA μA		
	Minimum Input for Max. Lamp Current	V _{BRT_ADJ}	I _{O(LAMP)} = Maximum Lamp Current		1.8	20	V		
	Maximum Input for Min. Lamp Current ³	V _{BRT_ADJ}	I _{O(LAMP)} = Minimum Lamp Current	0			V		
	Minimum PWM Input Frequency	F _{BRT_PWM}		2			kHz		
•	SLEEP BAR INPUT								
	RUN Mode	V _{SLEEP}		2.0			V		
	SLEEP Mode	V _{SLEEP}		-0.3		0.8	V		
•	SET _{1,2} INPUT								
	SET _{1,2} Low Threshold	V _L			0		V		
	Input Current	I _{SET}	V _{SET} = 0V		-270		μA		
•	POWER CHARACTERISTICS					•			
	Sleep Current	I _{IN(MIN)}	V _{IN} = 5V, SLEEP ≤ 0.8V		5	20	μA		
	Run Current	I _{RUN}	V_{IN} = 5V, $\overline{SLEEP} \ge 2.0V$, I_{SET1} = Open I_{SET2} = Ground, V_{LAMP} = $550V_{RMS}$		830		mA		
	Run Current Ripple Voltage	I _{RIPPLE}	V_{IN} = 5V, SLEEP \ge 2.0V, I_{SET1} = Open I_{SET2} = Ground, V_{LAMP} = 550 V_{RMS}		350		mVpp		
	Typical Efficiency	η	V_{IN} = 5V, $\overline{SLEEP} \ge 2.0V$, I_{SET1} = Open I_{SET2} = Ground, V_{LAMP} = $550V_{RMS}$		80		%		

³ The BRITE minimum input voltage level is 0V, whereas it is 0.5V in the original LXMG1618-05-4x inverter.

		FUNCTIONAL PIN DESCRIPTION					
Conn	Pin	DESCRIPTION					
CN1 (Molex assembly	53261-0871	or equivalent) Mates with 51021-0800 housing, 50079-8100 pins. Mates with LX9501G input cable					
CN1-1	V _{IN}	Main Input Power Supply (4.75V < V _{IN} < 5.25V)					
CN1-2		Wildin input 1 ower ouppry (4.70 v = vin = 0.20 v)					
CN1-3	GND	Power Supply Return					
CN1-4	GND	i owei Suppiy Netuiii					
CN1-5 SLEEP ON/OFF Control. (0V < SLEEP ≤ 0.8V = OFF, SLEEP ≥ 2.0V = ON)		ON/OFF Control. (0V < SLEEP ≤ 0.8V = OFF, SLEEP ≥ 2.0V = ON)					
CN1-6 BRITE Brightness Control (0V to 2.0V). 2.0V gives maximum lamp current.							
CN1-7 SET ₁ SET ₁ MSB Connecting this pin to ground decreases the output current (see Table 1)							
CN1-8 SET ₂ SET ₂ LSB Connecting this pin to ground decreases the output current (see Table 1)							
CN2 for LX	MG1618A-05	-41 and -42 (JST SM02(8.0)B-BHS-1-TB(LF)(SN) ; Yeon Ho 20015WR-05A00 or SM02B-BHSS-1-TB(LF)(SN) ; Yeon Ho 35001WR-02A00) or equivalent					
CN2-1	V _{HI}	High voltage connection to high side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground.					
CN2-2	V_{LO}	Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground					



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TABLE 1 SETTING OUTPUT CURRENT

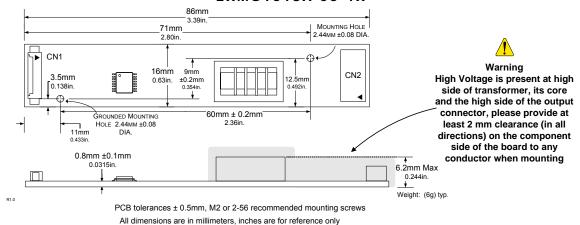
OUTPUT CURRENT SETTINGS

CET	CET				
SET₁ (Pin 7)	SET ₂ (Pin 8)	Nominal Output Current			
Open*	Open*	6.5mA			
Open*	Ground	6.0mA			
Ground	Open*	5.5mA			
Ground	Ground	5.0mA			

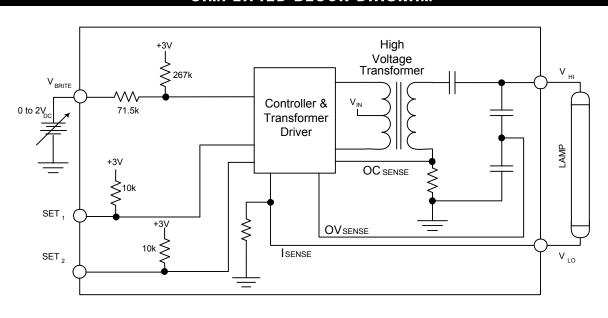
^{*} If driven by a logic signal it should be open collector or open drain only, not a voltage source.

PHYSICAL DIMENSIONS

LXMG1618A-05-4X



SIMPLIFIED BLOCK DIAGRAM





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TYPICAL APPLICATION

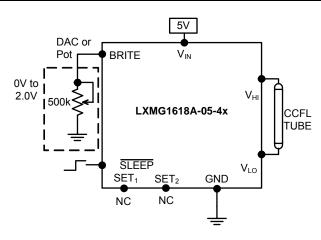


Figure 1 – Brightness Control (output current set to maximum)

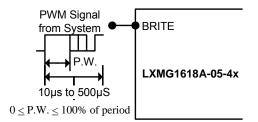


Figure 1A - PWM Brightness Control

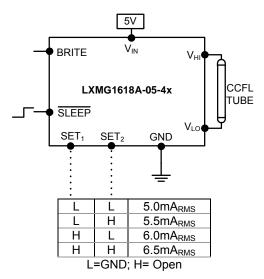


Figure 2 – Max Output Current (SET₁ and SET₂ Inputs)

- The brightness control may be a voltage output DAC or other voltage source, a digital pot or 500K manual pot. The inverter contains effectively an internal 338k pull-up to 3V to bias the pot, if desired; add a resistor to set the lower threshold voltage above the absolute minimum dim level capability. A 3.3V Logic Level PWM signal from a micro-controller may also be used as shown in Figure 1A.
- If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to V_{LO}. This wire is typically white.
- Use the SET₁ and SET₂ (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufacturers. Generally the best lamp lifetime correlates with driving the CCFL at the manufacturer's nominal current setting. However the SET₁ and SET₂ inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using an open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. In conjunction with a light sensor or other timer the panel could be set to higher brightness (maximum output current) for daytime illumination and lower brightness (minimum or typical output current) at nighttime. Since the dim ratio is a factor of the peak output current, using this technique the effective dim ratio can be increased. Conversely, the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course any possible degradation on lamp life from such practices is the user's responsibility since not all lamps are designed to be overdriven.
- The inverter has a built-in fault timeout function. If the output is open (lamp disconnected or broken) or shorted the inverter will attempt to strike the lamp up to about one second, after which (without success) the inverter will shutdown. Supply current draw in the shutdown mode is about 8mA. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V_{IN} input supply.



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NOTES

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