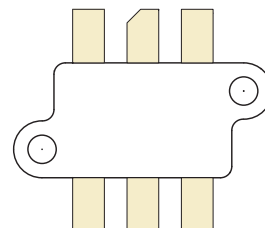



RF POWER MOSFET
P-CHANNEL ENHANCEMENT MODE


The ARF301 is a P-CHANNEL RF power transistor in a high efficiency flangeless package. It is designed for high voltage operation in narrow band ISM and MRI power amplifiers at frequencies up to 45MHz. The transistor is well matched to the ARF300 N-CHANNEL RF power transistor making the pair well suited for bridge configurations

- Specified 125 Volt, 27 MHz Characteristics:
 - Output Power = 300 Watts.
 - Gain = 15dB (Class E)
 - Efficiency = 80%
- RoHS Compliant 
- High Performance
- High Voltage Breakdown and Large SOA for Superior Ruggedness
- Low Thermal Resistance.
- Capacitance matched with ARF300 N-Channel

Maximum Ratings
All Ratings: $T_c = 25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-Source Voltage	500	V
V_{DGO}	Drain-Gate Voltage	500	
I_D	Continuous Drain Current @ $T_c = 25^\circ\text{C}$	20	A
V_{GS}	Gate-Source Voltage	± 30	V
P_D	Total Power Dissipation @ $T_c = 25^\circ\text{C}$	833	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 175	$^\circ\text{C}$
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	

Static Electrical Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 250 \mu A$)	500			V
$V_{DS(ON)}$	On State Drain Voltage ¹ ($I_{D(ON)} = 10A, V_{GS} = 10V$)		8	10	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}, V_{GS} = 0V$)			25	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 50V_{DSS}, V_{GS} = 0, T_c = 125^\circ\text{C}$)			250	
I_{GSS}	Gate-Source Leakage Current ($V_{DS} = \pm 30V, V_{GS} = 0V$)			± 100	nA
g_{fs}	Forward Transconductance ($V_{DS} = 15V, I_D = 10A$)	5	8		mhos
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 10mA$)	-2.5	- 4	-5	Volts

Thermal Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction to Case			0.15	$^\circ\text{C/W}$
$R_{\theta JHS}$	Junction to Sink (High Efficiency Thermal Joint Compound and Planar Heat Sink Surface.)			0.27	


CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Dynamic Characteristics

ARF301

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		2000	2200	pF
C_{oss}	Output Capacitance	$V_{DS} = 50V$		320	360	
C_{rss}	Reverse Transfer Capacitance	$f = 1MHz$		62	70	

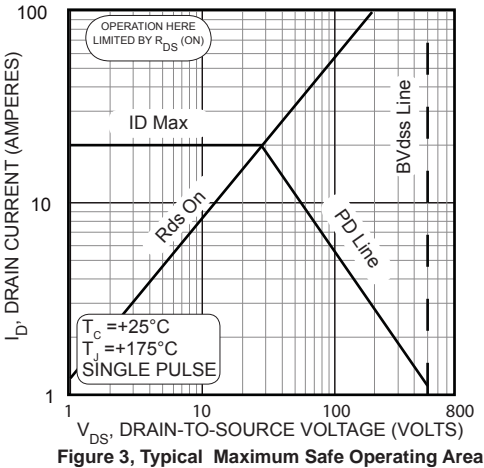
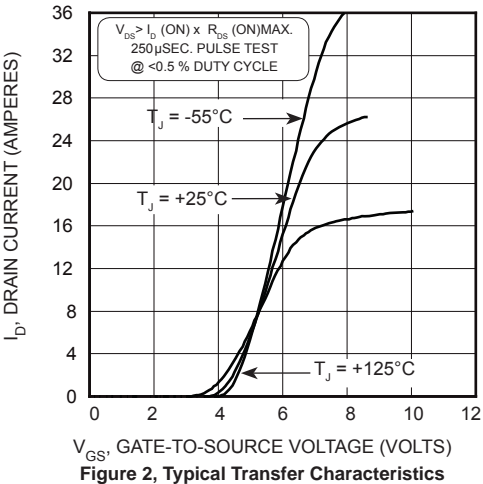
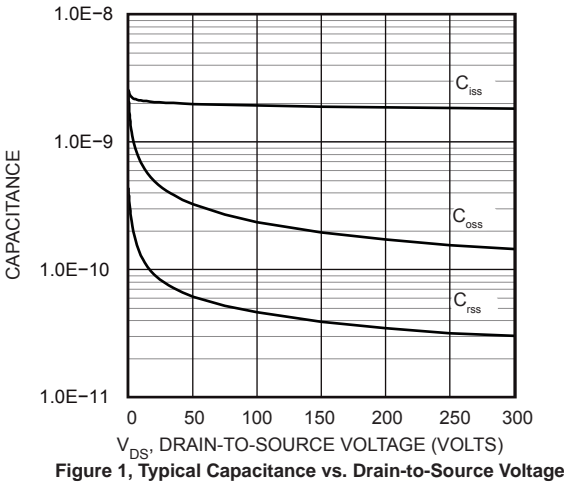
Functional Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
G_{PS}	Common Source Amplifier Power Gain	$f = 27MHz$	15	17		dB
η	Drain Efficiency	$I_{dq} = 0mA$ $V_{DD} = 125V$	80	85		%
Ψ	Electrical Ruggedness VSWR 10:1	$P_{OUT} = 300W$	No Damage			

1. Pulse Test: Pulse width < 380 μS , Duty Cycle < 2%.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

Dynamic Characteristics



Dynamic Characteristics

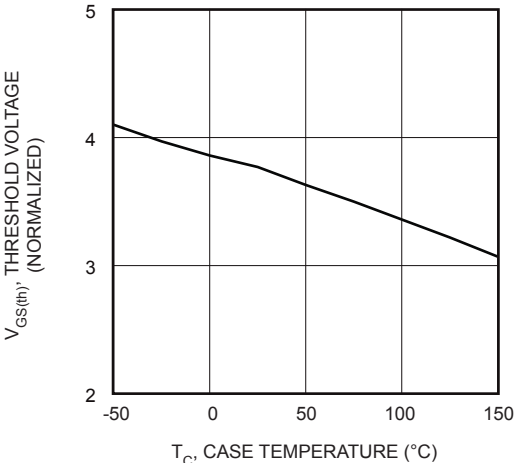


Figure 4, Typical Threshold Voltage vs Temperature

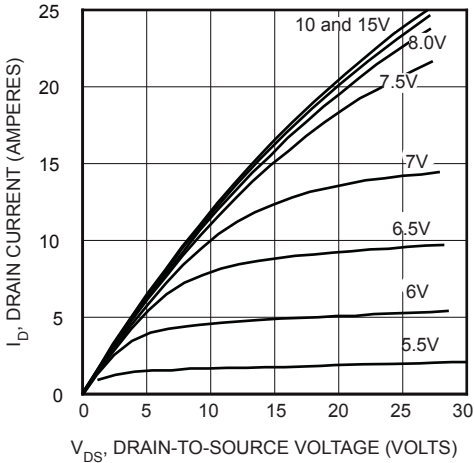


Figure 5, Typical Output Characteristics

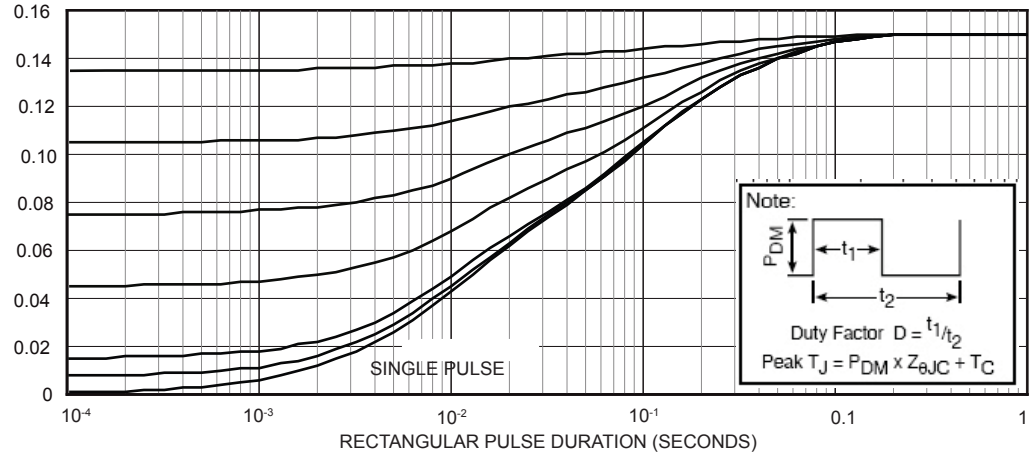


FIGURE 6a, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

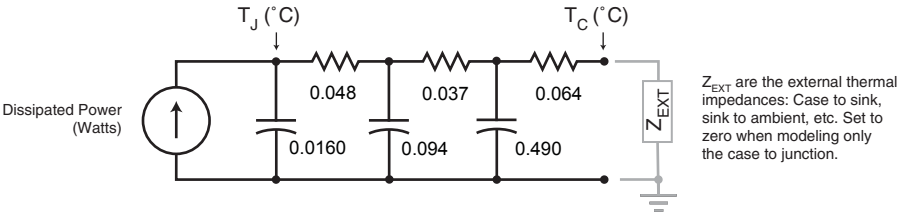


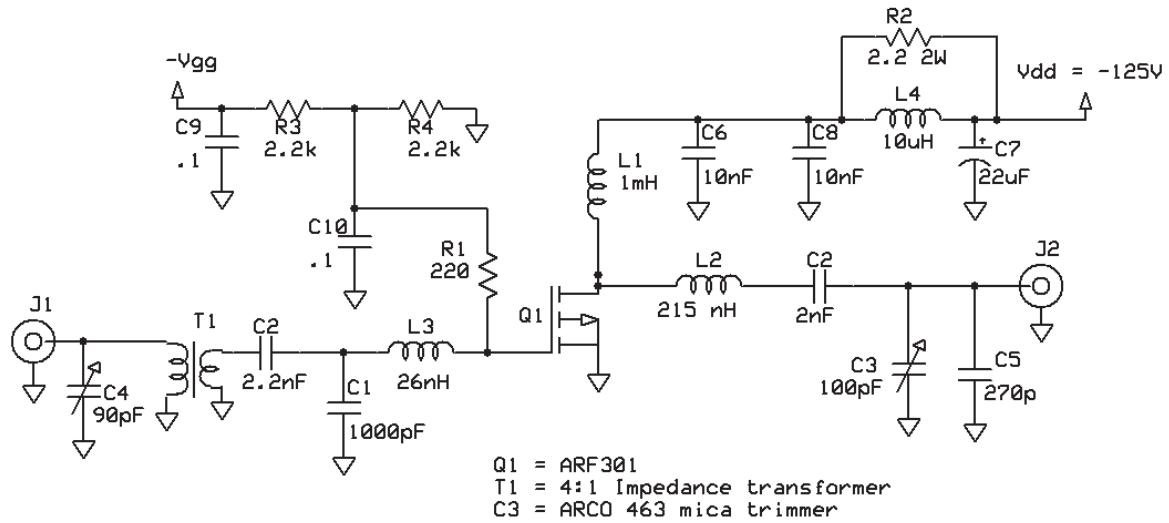
Figure 6b, TRANSIENT THERMAL IMPEDANCE MODEL

Table 1 - Typical Class AB Large Signal Input - Output Impedance

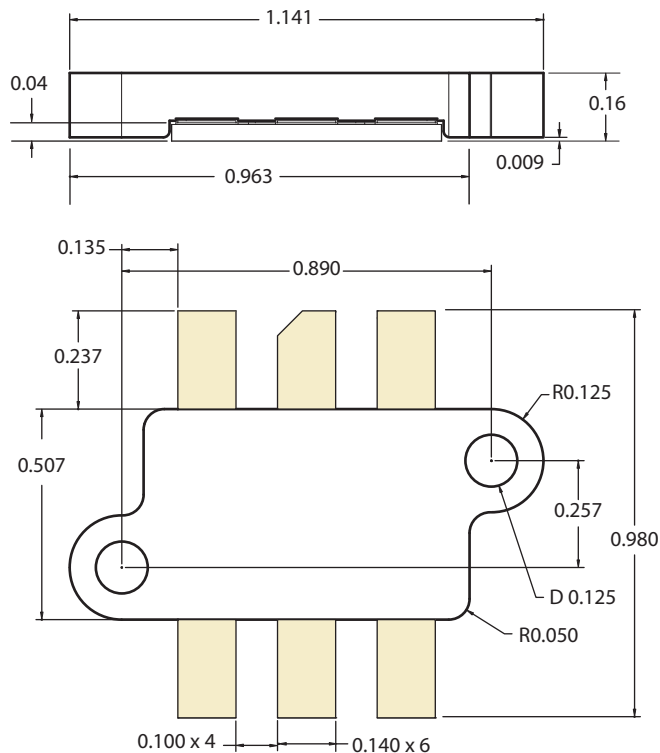
Freq. (MHz)	$Z_{in} (\Omega)$	$Z_{OL} (\Omega)$
2.0	18 - j 10.6	20.9 - j 1.3
13.56	2.7 - j 4.5	17.8 - j 7.4
27.12	1.9 - j 1.6	12.3 - j 10.2
40.68	1.77 - j 0.18	8.0 - j 10

Z_{IN} - Gate shunted with 25 Ω $I_{dq} = 0$
 Z_{OL} - Conjugate of optimum load for 300 Watts output at $V_{dd}=125V$

Class CE 27.12 MHz amplifier



T11 Package Outline



Use 4-40 (M3) screws for mounting.
 Torque = 4-6 in-lb (0.45- 0.7 Nm).



ATTENTION: This is a high power device. Special considerations must be followed in mounting to ensure proper operation of these devices. Incorrect mounting can cause internal temperatures to exceed the maximum allowable operating junction temperature. Refer to Microsemi Application Note #1810 before starting system design.
<http://www.microsemi.com/support/micnotes/1810.pdf>