

About Micrel

Micrel, Inc. is a leading global manufacturer of mixed signal analog, power, LAN, and clock management semiconductors. Micrel's customers consist the of world's leading OFMs automotive servina consumer, enterprise, industrial, mobile, and telecommunications markets. Micrel Inc.'s corporate headquarters is located in San Jose, CA, with a global presence in Asia, the Americas, and Europe.

www.micrel.com

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Micrel's innovative EtherSynch™ technology combines Ethernet communications, IEEE 1588v2 distributed synchronization, and precision GPIO in a highly-integrated, energy-efficient solution. A hardware-based architecture provides extensive synchronization and communications processing, freeing the limited host CPU resources. Precision GPIO extends the system synchronization to locally-connected devices through a highly-configurable GPIO facility that reduces the per-device cost for Ethernet attachments.

www.ethersynch.com



KSZ84xx EtherSynch[™] IEEE 1588v2 Over Ethernet Product Family

KSZ8441HLI — EtherSynch IEEE 1588v2 10/100 Controller with Generic Host Bus Interface

The KSZ8441 controller enables end-device attachments for industrial Ethernet and power substation automation networks. Key applications include industrial Ethernet standards, including EtherNet/IP[™], PROFINET[®], Modbus TCP[™], and POWERLINK, along with IEC 61850 electrical power substation automation networks that employ centralized (star-wired) topologies.

The KSZ8441 controller simplifies integration with Host CPUs that do not provide embedded Ethernet MACs and has been optimized to offload synchronization and communications host processing through its hardwire-intensive architecture.

Micrel offers a precision timing protocol (PTPv2) synchronization stack solution integrated with the KSZ8441 driver to accelerate the design cycle. The KSZ8441 switch is also interoperable with leading third-party PTP stacks.

Micrel features the industry's most robust and cost-effective Ethernet switches and physical layer tranceivers, built upon Micrel's low-power physical layer technology, advanced power management, and innovative design.



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Features

- Comprehensive PTP clock modes.
- Hardware-assisted PTP implementation.
- Extensive GPIO synchronized to precision clock.
- Bypass mode and source address filtering.
- Flexible Host Interface Options: MII, RMII, and Generic Host Interface.
- PTP protocol stack integrated with KSZ8441 driver.
- Compact 10mm x 10mm, 64-pin, LQFP package.
- Advanced power management.

Benefits

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- Supports broad range of topologies.
- Reduces Host CPU processing load and network traffic.
- Extends system synchronization to locally-connected devices.
- Enables distributed, daisy-chained topologies.
- Supports a wide range of Host CPUs.
- Reduces time to deployment with pre-qualified solution.
- · Less board resources required reduces BOM costs.
- Industry's lowest power consumption and energy efficiency.



KSZ8441 readily integrates with host CPUs that do not provide an embedded Ethernet MAC

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