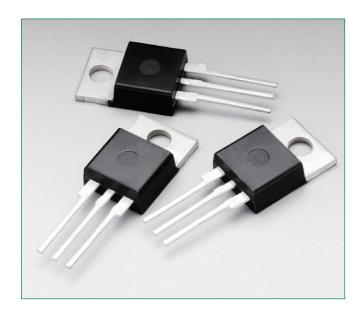


BTB08-600CW3G, BTB08-800CW3G





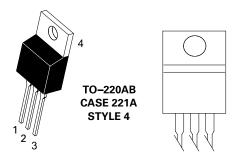
Description

Designed for high performance full-wave ac control applications where high noise immunity and high commutating di/dt are required.

Features

- Blocking Voltage to 800 V
- On-State Current Rating of 8 A RMS at 80°C
- Uniform Gate Trigger Currents in Three Quadrants
- High Immunity to dV/ dt - 1500 V/s minimum at 125°C
- Minimizes Snubber Networks for Protection
- Industry Standard TO-220AB Package
- High Commutating dl/ dt – 3.0 A/ms minimum at 125°C
- These Devices are Pb-Free

Pin Out



Functional Diagram



Additional Information







Resources



Samples



Maximum Ratings † (T₁ = 25°C unless otherwise noted)

Rating	Part Number	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1) (T _J = -40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open)			600 800	V
On-State RMS Current (Full Cycle Sine Wave, 60 Hz, TC = 80°C)	I _{T (RMS)}	8.0	А	
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, T _C = 25°C)		I _{TSM}	90	А
Circuit Fusing Considerations (t = 10 ms)			36	A ² sec
Non-Repetitive Surge Peak Off-State Voltage (T _J = 25°C, t = 10ms)			V _{DRM} /V _{RRM} +100	V
Peak Gate Current ($T_J = 125$ °C, t = 20ms)			4.0	А
Peak Gate Power (Pulse Width ≤ 1.0 μs, TC = 80°C)			20	W
Average Gate Power (T _J = 125°C)			1.0	W
Operating Junction Temperature Range			-40 to +125	°C
Storage Temperature Range			-40 to +150	°C

[†] Indicates JEDEC Registered Data

Maximum Ratings † (T_J = 25°C unless otherwise noted)

	Rating	Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R _{ejc} R _{eja}	2.5 60	°C/W
Maximum Lead Temperature for Solderi 10 seconds	ng Purposes, 1/8" from case for	T _L	260	°C

[†] Indicates JEDEC Registered Data

Electrical Characteristics - OFF (T_c = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Repetitive Blocking Current	T ₁ = 25°C	I _{DRM} ,	-	-	0.005	m ^
$(V_D = V_{DRM} = V_{RRM}, Gate Open)$	$T_J = 110^{\circ}C$	I	-	-	1.0	mA

Electrical Characteristics - ON

Characteristic		Symbol	Min	Тур	Max	Unit
Peak On-State Voltage (Note 2) (I _{TM} = ±11 A Peak)		V_{TM}		-	1.55	V
	MT2(+), G(+)		2.5	_	35	mA
Gate Trigger Current (Continuous dc) ($V_D = 12 \text{ V}, R_L = 30 \Omega$)	MT2(+), G(-)	I _{GT}	2.5	-	35	
	MT2(-), G(-)		2.5	_	35	
Holding Current $(V_D = 12 \text{ V}, \text{ Gate Open, Initiating Current} = \pm 100 \text{ mA})$		I _H	-	_	45	mA
	MT2(+), G(+)	IL	-	_	50	mA
Latching Current $(V_D = 24 \text{ V, I}_G = 42 \text{ mA})$	MT2(+), G(-)		-	-	80	
	MT2(-), G(-)		-	-	50	
	MT2(+), G(+)		0.5	_	1.7	
Gate Trigger Voltage $(V_D = 12 \text{ V}, R_L = 30 \Omega)$	MT2(+), G(-)	V _{GT}	0.5	_	1.1	V
	MT2(-), G(-)		0.5	-	1.1	
	MT2(+), G(+)		0.2	-	-	
Gate Non-Trigger Voltage (T _J = 125°C)	MT2(+), G(-)	V _{GD}	0.2	_	-	V
	MT2(-), G(-)		0.2	-	-	

^{2.} Indicates Pulse Test: Pulse Width \leq 2.0 ms, Duty Cycle \leq 2%.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

^{1.} V_{DBM} and V_{BBM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



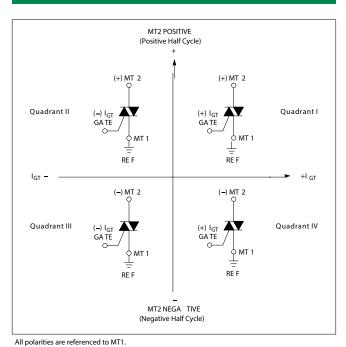
Dynamic Characteristics

Characteristic	Symbol	Min	Тур	Max	Unit
Rate of Change of Commutating Current, See Figure 10. (Gate Open, TJ = 125°C, No Snubber)	(dl/dt) _c	3.0	_	_	A/ms
Critical Rate of Rise of On-State Current (TJ = 125°C, f = 120 Hz, IG = 2 x IGT, tr \leq 100 ns)	dl/dt	_	_	50	A/µs
Critical Rate-of-Rise of Off-State Voltage ($V_D = 0.66 \times V_{DRM'}$ Exponential Waveform, Gate Open, $T_J = 125$ °C)	dv/dt(c)	1500	_	_	V/µs

Voltage Current Characteristic of SCR

Symbol	Parameter
V _{DRM}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
V _{RRM}	Peak Repetitive Reverse Off State Voltage
IRRM	Peak Reverse Blocking Current
V_{TM}	Maximum On State Voltage
I _H	Holding Current

Quadrant Definitions for a Triac



With in—phase signals (using standard AC lines) quadrants I and III are used

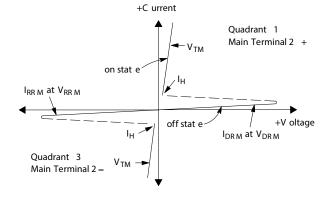




Figure 1. RMS Current Derating

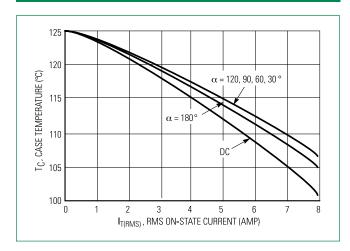


Figure 2. On-State Power Dissipation

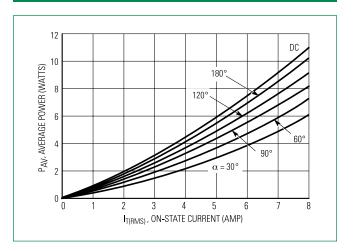


Figure 3. On-State Characteristics

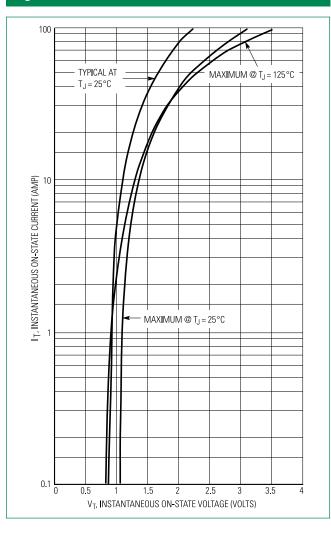


Figure 4. Thermal Response

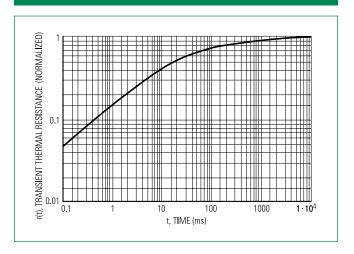
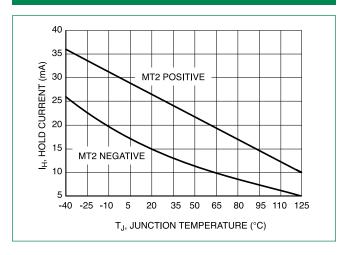


Figure 5. Typical Hold Current Variation





Typical Characteristics

Figure 6. Typical Gate Trigger Current Variation

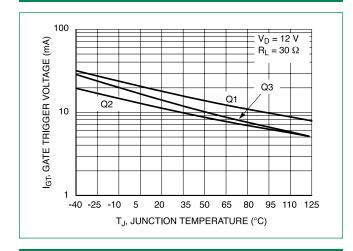


Figure 7. Typical Gate Trigger Current Variation

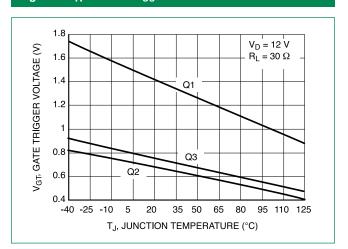


Figure 8. Critial Rate of Rise of Off-State Voltage (Exponential Waveform)

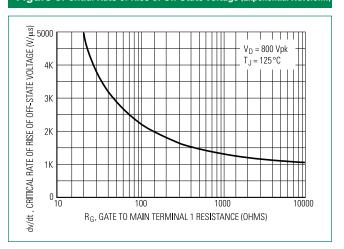
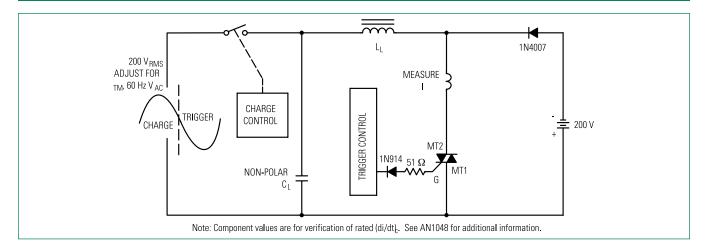
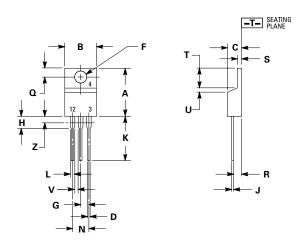


Figure 9. Simplified Test Circuit to Measure the Critical Rate of Rise of Commutating Current (di/dt)_c

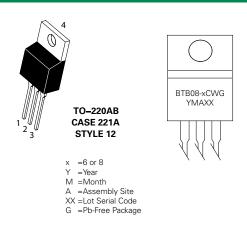




Dimensions



Part Marking System



D:	Inc	Inches		neters
Dim	Min	Max	Min	Max
Α	0.590	0.620	14.99	15.75
В	0.380	0.420	9.65	10.67
С	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.41	2.67
Н	0.110	0.130	2.79	3.30
J	0.018	0.024	0.46	0.61
K	0.540	0.575	13.72	14.61
L	0.060	0.075	1.52	1.91
N	0.195	0.205	4.95	5.21
Q	0.105	0.115	2.67	2.92
R	0.085	0.095	2.16	2.41
S	0.045	0.060	1.14	1.52
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045		1.15	
Z		0.080		2.04

Pin Assignment				
Main Terminal 1				
Main Terminal 2				
3 Gate				
No Connection				

Ordering Information

Device	Package	Shipping
BTB08-600CW3G	TO-220AB (Pb-Free)	500 Units / Rail
BTBt08-800CW3G	TO-220AB (Pb-Free)	500 Units / Rail

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

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