

AUTOMOTIVE CURRENT TRANSDUCER HC2F100-SN CLIPS









Introduction

The HC2F CLIPS Family is for the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC2F CLIPS family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 80 A up to 250 A
- Maximum RMS primary admissible current: defined by busbar to have T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratiometric (in sensitivity and offset)
- · Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- · Very low thermal sensitivity drift
- · Wide frequency bandwidth
- No insertion losses.

Automotive applications

- Electrical Power Steering
- Starter Generators
- Converters ...

Principle of HC2F CLIPS Family

The open loop transducers uses a Hall effect integrated circuit. The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current I_p to be measured. The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

B
$$(I_p)$$
 = constant (a) x I_p

The Hall voltage is thus expressed by:

$$V_{H} = (R_{H}/d) \times I \times constant (a) \times I_{P}$$

Except for \mathbf{I}_{p} , all terms of this equation are constant. Therefore:

$$V_{H}$$
 = constant (b) x I_{P}

The measurement signal $\mathbf{V}_{\rm H}$ amplified to supply the user output voltage or current.

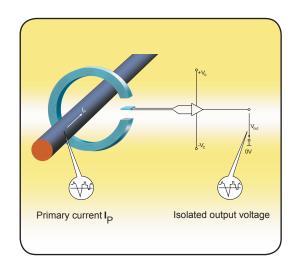


Fig. 1: Principle of the open loop transducer



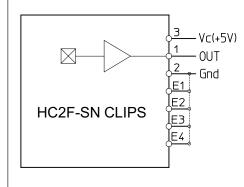
Dimensions HC2F100-SN CLIPS family (in mm)

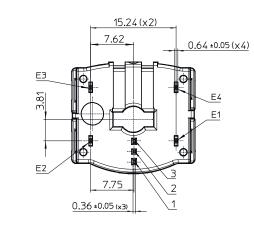


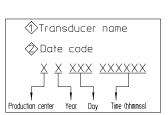
Secondary connection

Terminals	Designations
3	Supply voltage + 5 V DC
1	V_{out}
2	Ground
E1 to E4	Ground (*)

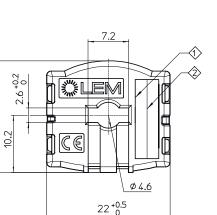
Connection



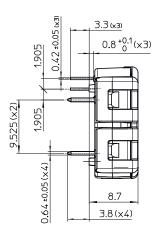




Positive primary current direction



20 ±0.25



Bill of materials

Plastic case
 Magnetic core
 Pins and primary bus bar
 Popper alloy tin plated (lead free)

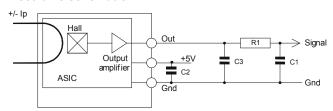
Mass6 g

Remarks

General tolerance ± 0.2 mm

 $\bullet \quad \textbf{V}_{\text{OUT}} > \ \, \frac{\textbf{V}_{\textbf{c}}}{\textbf{2}} \quad \text{when } \textbf{I}_{\text{p}} \text{ flows in the direction of the arrow}.$

Electronic schematic



Power supply decoupling capacitor: C2 = 47 nF

EMC protection capacitor C3 = 4.7 nF

Optional:

High frequency signal noise filter:

 $R1 > 100 \Omega$

C1 = defined according to the system frequency bandwidth

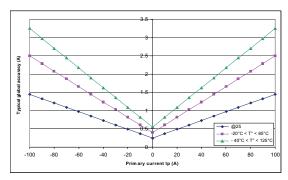


Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	А	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	-40 < T _A < 125°C	
Ambient storage temperature	T _s	°C	-40 < T _s < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	
Maximum admissible vibration	γ	m.s ⁻²	100	see page 5/5
Rms voltage for AC insulation test 50 Hz, 1 min	V _d	V	1500	see page 5/5
Creepage distance	dCp	mm	1.67	
Clearance	dCI	mm	1.80	CTI=425

Operating characteristics

Parameter	Symbol	Unit	S	pecification	on	Conditions	
Farameter	Syllibol		Min	Typical	Max	Conditions	
Electrical Data							
Primary current, measuring range	I _{PM}	Α	-100		100	@ - 40°C < T° < 125°C	
Supply voltage 1)	V _c	V	4.75	5.00	5.25	@ - 40°C < T° < 125°C	
Output voltage (Analog)	V _{OUT}	V	$V_{OUT} = (V_c/5) \times (2.5 + 0.02 \times I_p)$		-0.02 x I _P)	@ - 40°C < T° < 125°C	
Sensitivity	G	V/A	0.0196	0.02	0.0204	$\textcircled{0}$ \textbf{T}_{A} = 25°C; \textbf{V}_{C} = 5 V	
Offset voltage	V _o	V	2.481	2.5	2.519	@ $V_{c} = 5 \text{ V}; T_{A} = 25^{\circ}\text{C}; I_{p} = 0 \text{ A}$	
Current consumption	I _c	mA	-	15	20	@ - 40° C < T° < 125° C; 4.75 V < $V_{_{\rm C}}$ < 5.25 V	
Load resistance	R _L	ΚΩ	2	-	-		
Output internal resistance	R _{OUT}	Ω	-	-	10		
Sensitivity error	$\epsilon_{_{ m G}}$	%	-2.0	± 0.7	2.0	0 T _A = 25°C, V _C = 5 V; Gth = 0.02 V/A	
	I _{OE}	Α	-0.7	± 0.25	0.7	@ V = 5.00 V/. T = 05°C	
Electrical offset	V _{OE}	mV	-14	± 5	14	\bigcirc V _C = 5.00 V; T _A = 25°C	
Manadiaeffeet	I _{OM}	А	-0.25	± 0.15	0.25	@ After excursion to $\pm I_p$; $T_A = 25^{\circ}C$	
Magnetic offset	V _{OM}	mV	-5	± 3	5	WAITER EXCUISION TO E I _p , I _A = 25 C	
I _{OE}	TCI _{OEAV}	mA/°C	-7	± 4	7		
Average temperature coefficient of						@ - 40°C < T° < 125°C; V _c = 5.00 V	
$\mathbf{V}_{_{\mathrm{OE}}}$	TCV _{OEAV}	mV/°C	-0.14	± 0.08	0.14		
Average temperature coefficient of G	TCG _{AV}	%/°C	-0.04	± 0.02	0.04	@ - 40°C < T° < 125°C; V _C = 5.00 V	
Linearity error	$\epsilon_{\scriptscriptstyle \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \!$	% I _P	-1.0	± 0.2	1.0	@ I _P ; V _C = 5.00 V, T _A = 25°C	
Response time	t,	μs	-	15	20	@ di/dt = 50 A/µs; I _T = 60 A	
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 20 A rms	
Output voltage noise peak-peak	V _{no p-p}	mV	-	35	43	@ T _A = 25°C; 0 Hz < f < 1 MHz	
Output voltage noise rms	V _{no rms}	mV	-	-	4	@ T _A = 25°C; 0 Hz < f < 1 MHz	



Notes: $^{1)}$ The output voltage \mathbf{V}_{OUT} is fully ratiometric. The offset and sensitivity are dependent on the supply voltage \mathbf{V}_{C} relative to the following formula:

$$I_{P} = \left(V_{\text{out}} - \frac{V_{\text{c}}}{2}\right) \times \frac{1}{G} \times \frac{5}{V_{\text{c}}} \quad \text{with G in (V/A)}$$

²⁾ Small signal only to avoid excessive heating of the busbar, the magnetic core and the ASIC.



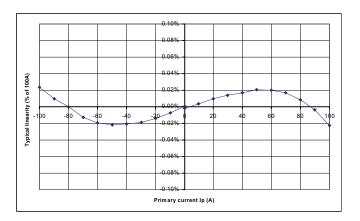


Fig. 3: Typical linearity versus primary current at T° = 25°C

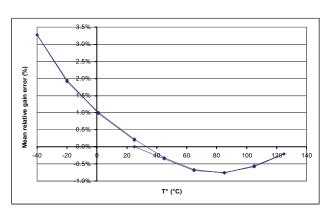


Fig. 5: Typical sensitivity error versus temperature

Influence of the external magnetic field

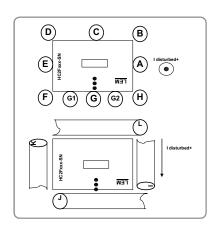
Test conditions:

- transducer sample : HC2F100-SN CLIPS

- diameter of the disturbing conductor : 6 mm

- dimension of the primary bus-bar : 6 x 2 x 200 mm

- tested at ambient temperature



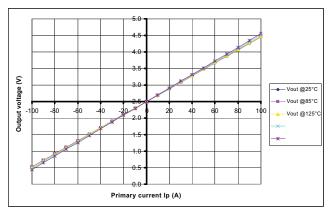


Fig. 4: Typical output voltage versus primary current across temperature

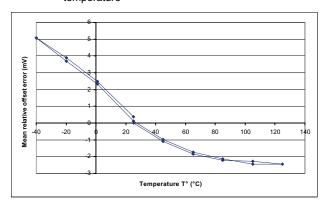
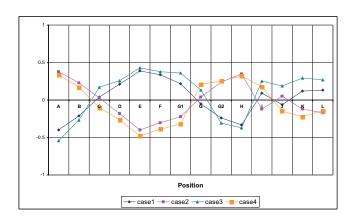


Fig. 6: Typical offset error versus temperature

Current levels:

	CASE 1	CASE 2	CASE 3	CASE 4
I disturbant	+ 100 A	- 100 A	+ 100 A	- 100 A
I primary	0 A	0 A	+ 100 A	+ 100 A





PERFORMANCES PARAMETERS DEFINITIONS

Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear $\mathbf{I}_{\rm C}$ amplifier gain.

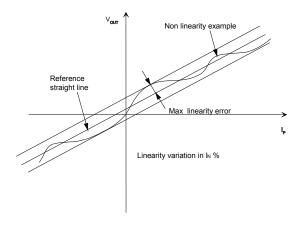
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{P\,max}$.

Linearity:

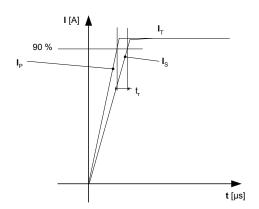
The maximum positive or negative discrepancy with a reference straight line $V_{\text{OUT}} = f(I_p)$.

Unit: linearity (%) expressed with full scale of I_{P may}.



Response time (delay time) t.:

The time between the primary current signal and the output signal reach at 90 % of its final value



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_p)$, it must establish the relation:

 $V_{out}(I_{p}) = V_{c}/5 (G \times I_{p} + 2.5) (*)$

(*) For all symetrics transducers.

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

 $I_{OT} = I_{OE} \max - I_{OE} \min$

The Offset drift \mathbf{TCI}_{OEAV} is the \mathbf{I}_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation \mathbf{G}_{T} is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 $\mathbf{G}_{\scriptscriptstyle T}$ = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.

The sensitivity drift \mathbf{TCG}_{AV} is the \mathbf{G}_{T} value divided by the temperature range.

Offset voltage @ $I_p = 0$ A:

Is the output voltage when the primary current is null. The ideal value of \textbf{V}_{O} is $\textbf{V}_{\text{C}}/2$ at $\textbf{V}_{\text{C}}=5$ V. So, the difference of \textbf{V}_{O} - $\textbf{V}_{\text{C}}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions		
Low T° storage	IEC 60068 Part 2-1	T° - 40°C / 100 H not connected		
Thermal shocks	IEC 60068 Part 2-14	T° - 30°C to 110°C /1000 cycles not connected		
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V		
Hight T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V		
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V		
Pressure cooker		T° 125°C / 100 % RH, P 0.178 Mpa/100 H supply voltage = 5 V		
Mechanical Tests				
Vibration	IEC 60068-2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis		
Drop test	IEC 60068 Part 2-29	Heigh 750 mm concret floor each directions		
EMC Test				
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge =1		

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