Product Brief

Intel® Celeron® and Intel® Celeron® M Processors on 45nm

Embedded Computing



Intel® Celeron® and Intel® Celeron® M Processors on 45nm for Embedded Computing

Product Overview

Intel® Celeron® and Intel® Celeron® M processors on 45nm process technology deliver both proven technology and exceptional value for thermally sensitive embedded, communication and storage applications. Available in both dual-core and single-core options, these high-performance, low-power processors offer several enhancements over previous-generation processors. They create ideal solutions for small-to-medium business and enterprise communications applications, storage appliances, gaming platforms, and other embedded devices. While incorporating advanced processor technology, they remain software-compatible with previous IA-32 processors.



Product Highlights

- Processors:
 - Intel® Celeron® processor T3100[△] with two cores at 1.9 GHz core speed, 800 MHz front-side bus (FSB) and 35 W thermal design power (TDP).
- Intel® Celeron® M processor ULV 722 $^{\!\Delta}$ at 1.2 GHz core speed, 800 MHz FSB and 5.5 W TDP.
- Intel® Celeron® M processor ULV 723^{Δ} at 1.2 GHz core speed, 800 MHz FSB and 10 W TDP.
- Chipset support:
 - Intel Celeron M processors ULV 722 and ULV 723 are validated with the Mobile Intel® GS45 Express chipset, while the T3100 processor is validated with the Mobile Intel® GM45 Express chipset. Both chipsets include the Mobile Intel® Graphics Media Accelerator 4500MHD and Intel® Clear Video Technology. Enhanced graphics and 3D rendering performance make the platform ideal for embedded applications such as industrial control, retail and transaction solutions, gaming platforms, and digital signage.
 - Intel Celeron processor T3100 is also validated with the Mobile Intel® GL40 Express chipset, making it an excellent choice for value-oriented applications.
 - Intel Celeron M processor ULV 722 is also validated with the Intel® 3100 chipset, which combines server-class memory and I/O controller functions into a single component. This integrated chipset supports a wide range of performance-intensive, thermally sensitive embedded, communications and storage applications.

- On-die, 1 MB L2 cache with Advanced Transfer Cache (ATC) architecture delivers a high data throughput channel between the Level 2 cache and processor cores.
- Data Prefetch Logic speculatively fetches data to the L2 cache before the L1 cache requests occur, reducing bus cycle penalties.
 Data Cache Unit Streamer enhances performance of the L2 pre-fetcher by requesting L1 warm-ups earlier. Write Order Buffer depth is enhanced to help with write-back latency performance.
- New Streaming SIMD Extensions 4 instruction set delivers expanded capabilities, enhanced performance and greater energy efficiency for most embedded applications involving graphics, video encoding and processing, 3D imaging, gaming, web servers, and application servers.
- Execute Disable Bit, when combined with a supporting operating system, allows memory to be marked as executable or non-executable. If code attempts to run in non-executable memory, the processor raises an error to the operating system. This feature can prevent some classes of viruses or worms that exploit buffer overrun vulnerabilities and can thus help improve the overall security of the system. Please refer to the IA-32 Intel® Architecture Software Developer's Manual for more details (intel.com/products/processor/manuals/index.htm).
- Intel® 64 Architecture² supports 64-bit instructions, providing flexibility for 64-bit and 32-bit applications and operating systems.

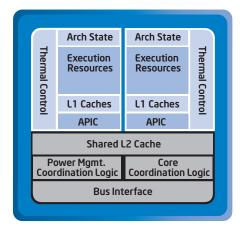
Product Overview (continued)

- Embedded lifecycle support protects system investment by enabling extended product availability for embedded, storage and communications customers.
- Along with a strong ecosystem of hardware and software vendors, including members of the Intel® Embedded and Communications
 Alliance (intel.com/go/eca), Intel helps developers cost-effectively meet design challenges and shorten time-to-market.

Software Overview

A number of independent operating system and BIOS vendors provide support for these platforms:

Operating System	Contact
Microsoft Windows* XP	Intel provides drivers ³
Microsoft Windows* Embedded Standard	Intel provides drivers ³
Microsoft Windows* Embedded POSReady	Intel provides drivers ³
Red Hat Enterprise Linux* 5	Red Hat
Novell SUSE Linux* Enterprise 10	Novell
Wind River Linux*	Wind River
Wind River VxWorks* 6.6	Wind River



Block diagram for the Intel® Celeron® processor T3100^a with two cores on 45nm

BIOS

American Megatrends Insyde Software Phoenix Technologies

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Product Number	Core Speed	Number of Cores	Front-Side Bus Speed	L2 Cache	Thermal Design Power	VID	Tjunction	Package ⁴
Intel® Celeron® processor T3100∆								
AV80577NG0371M	1.9 GHz	2	800 MHz	1 MB	35 W	0.8 V - 1.25 V	0-105° C	479 µFC-BGA
AW80577GG0371ML	1.9 GHz	2	800 MHz	1 MB	35 W	0.8 V - 1.25 V	0-105° C	478 µFC-PGA
Intel® Celeron® M processor ULV 722 ^Δ								
AV80585VG0091MP	1.2 GHz	1	800 MHz	1 MB	5.5 W	0.775 V - 1.1 V	0-100° C	956 µFC-BGA (SFF)
Intel® Celeron® M processor ULV 723 ^a								
AV80585VG0091M	1.2 GHz	1	800 MHz	1 MB	10 W	0.775 V - 1.1 V	0-100° C	956 µFC-BGA (SFF)

Intel in Embedded and Communications: Intel.com/embedded

- a Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families.
- See http://www.intel.com/products/processor_number for details.
- ¹ Enabling Execute Disable Bit functionality requires a PC with a processor with Execute Disable Bit capability and a supporting operating system. Check with your PC manufacturer on whether your system delivers Execute Disable Bit functionality.
- ² 64-bit computing on Intel® architecture requires a computer system with a processor, chipset, BIOS, operating system, device drivers and applications enabled for Intel® 64 architecture. Performance will vary depending on your hardware and software configurations. Consult with your system vendor for more information.
- ³ Drivers available at: downloadcenter.intel.com (enter chipset name).
- 4 SFF=small form factor package.

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Printed in USA 0609/KSC/OCG/XX/PDF





