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953202BGT

Category: **Desktop Chipsets**

953202 Generic Part: PC CLOCK Market Group: PC MAIN CLOCK Description:

The ICS953202 is part of a whole new line of ICS clock generators and buffers called TCHâ,¢ (Timing Control Hub). This part incorporates ICS's newest clock technology which offers more robust features and functionality. Employing the use of a serially programmable I2C interface, this device can adjust the output clocks by configuring the frequency $setting, the output divider \ ratios, selecting \ the \ ideal \ spread \ percentage, the \ output \ skew, the \ output \ strength, and$ enabling/disabling each individual output clock. M/N control can configure output frequency with resolution up to 0.1MHz increment.

Output Features:

• 2 - Differential pair push-pull CPU clocks @ 3.3V

• 6 - PCI-Ex differential pairs

• 7 - PCICLK @ 3.3V

• 3 - AGPCLK @ 3.3V

• 1 - 48MHz @ 3.3V fixed.

• 1 - 24/48MHz @ 3.3V

• 2 - REF @ 3.3V, 14.318MHz.

You may also like.



Parameters

Package	TSSOP 56 (PA56)	
Voltage	3.3 V	
Package	TSSOP 56	
Speed	NA	
Temperature	С	
Status	Active	
Sample	No	
Minimum Order Quantity	2000	
Factory Order Increment	2000	

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Туре	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	TSSOP 6.10 MM
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Standard
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temprature	240°C
Rebake Conditions	N/A
Length	14.0
Mark	G
Width	6.1
Pitch	0.5
Thickness	1.0
Status	Active